**Preliminary** 

## **SP4T Absorptive RF Switch**

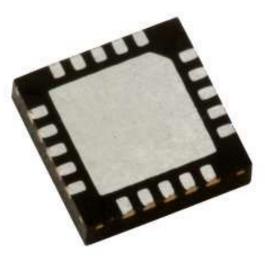
#### **Features**

- High Isolation Of > 40 dB @ 2 GHz
- Low Insertion Loss Of 1.5dB @ 2 GHz
- DC To 4 GHz Operating Frequency
- Integrated CMOS Control Logic
- Integrated ESD Protection on Digital I/O
- Single Positive Supply Voltage
- Ultra Small LPCC<sup>TM</sup> Packaging
- Impedance matched to 75 Ohm systems

### **Product Description**

The Honeywell HRF-SW1021 is a high performance single pole four throw (SP4T) absorptive RF switch that is ideal for use in wireless basestation and handset applications that require minimum power and minimum insertion loss.

The HRF-SW1021 is manufactured with Honeywell's patented Silicon On Insulator (SOI) CMOS technology, which provides the performance of GaAs with the economy and integration capabilities of conventional CMOS technology.



HRF-SW1021 in LPCC™ Package

## RF Electrical Specifications @ + 25°C

Results @ Vdd=5.0 +/- 10%, Vss = 0 unless otherwise stated, Z0= 75 ohms

Parameter	Test Condition	Frequency	Minimum	Typical	Maximum	Units
Insertion Loss		DC – 1.0GHz		1.35		dB
		2.0 GHz		1.50		dB
		3.0 GHz		1.90		dB
Isolation		DC – 1.0GHz		52		dB
		2.0 GHz		40		dB
		3.0 GHz		31		dB
VSWR*		DC - 0.5 GHz		1.1:1		Ratio
		0.5 – 2.0 GHz		1.2:1		Ratio
		2.0 - 3.0 GHz		1.3:1	1.4:1	Ratio
1dB Compression	Input Power					
	Vss = Gnd	DC - 3.0 GHz		20		dBm
	Vss = -3	DC - 3.0 GHz		30		dBm
Input IP3	Two-Tone Inputs Up To + 5 dBm					
put o	Vss = Gnd	DC - 3.0 GHz		35		dBm
	Vss = -3	DC – 3.0 GHz		47		dBm
Trise, Tfall*	10% To 90%			10		nS
Ton, Toff	50% Cntl To 90%/10%Rf			20		nS
Transients	In-Band			10		mV

\*By design

Web Site: www.mysoiservices.com
Email: mysoiservices@honeywell.com

2002 1021W Published June 2002 Page 1

Honeywell Solid State Electronics Center 12001 State Highway 55 Plymouth, Minnesota 55441-4799 1-800-323-8295



**Preliminary** 

## DC Electrical Specifications @ + 25°C

Parameter	Minimum	Typical	Maximum	Units
Single V <sub>DD</sub> Supply Voltage	3.3*	5.0	5.5	٧
CMOS Logic Level (0)	0		0.8	V
CMOS Logic Level (1)	$V_{DD} - 0.8$		$V_{DD}$	V
Input Leakage Current			10	uA

<sup>\*</sup> Note, performance curves are for Vdd = +5.0 +/- 10%

## Absolute Maximum Ratings<sup>1</sup>

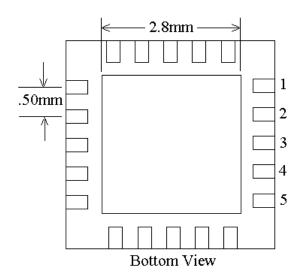
Parameter	Absolute Maximum	Units
$V_{DD}$	+6.0	V
Vin Digital Logic 0	Vss - 0.6	V
Vin Digital Logic 1	Vdd + 0.6	V
Maximum Input Power	> 35	dBm
ESD Voltage (Control Lines)	2K	V
Operating Temperature Range	+85	Degrees C
Storage Temperature Range	+125	Degrees C

<sup>(</sup>Note 1) Operation beyond any of these parameters may cause permanent damage.

Latch-Up: Unlike conventional CMOS RF switches, Honeywell's HRF-SW1021 is immune to latch-up.

**ESD Protection:** Although this device contains ESD protection circuitry on all digital inputs, conventional precautions should be taken to ensure that the Absolute Maximum Ratings are not exceeded.

## Package Outline Drawing



Bottom View, 20 Pin 4X4 mm LPCC<sup>™</sup> Package ASAT LPCC Marketing Outline Dwg. # GMJ00004 For more information see http://www.asat.com

Web Site: Email:

2002 1021W

www.mysoiservices.com mysoiservices@honeywell.com



Preliminary

#### **Truth Table**

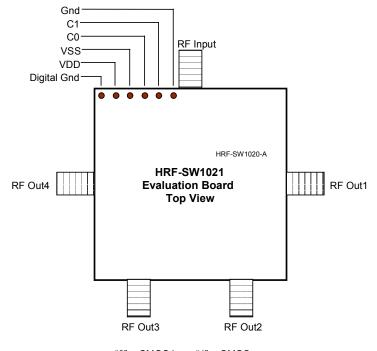
C1	C0	RF Output 1	RF Output 2	RF Output 3	RF Output 4
0	0	RFINPUT			
0	1		RFINPUT		
1	0			RFINPUT	
1	1				RFINPUT

<sup>&</sup>quot;0" = CMOS Low, "1" = CMOS High

### **Pin Configuration**

Pin	Function	Pin	Function
1	GROUND	11	C1
2	RFOUTPUT4	12	C0
3	GROUND	13	GROUND
4	VDD	14	RFOUTPUT1
5	DIGITAL GROUND	15	GROUND
6	RFOUTPUT3	16	GROUND
7	GROUND	17	GROUND
8	GROUND	18	RFINPUT
9	GROUND	19	GROUND
10	RFOUTPUT2	20	VSS

### **Evaluation Circuit Board Connections**



"0" = CMOS Low, "1" = CMOS

Note:

HRF-SW1021 is the Z0 = 75 ohm version of the SW1020 evaluation board.

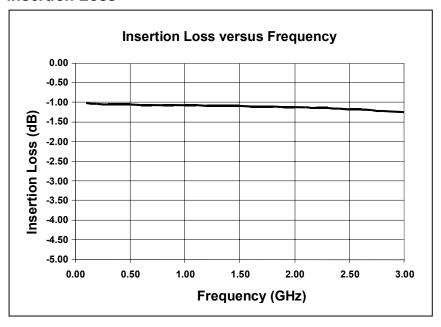
2002 1021W



**Preliminary** 

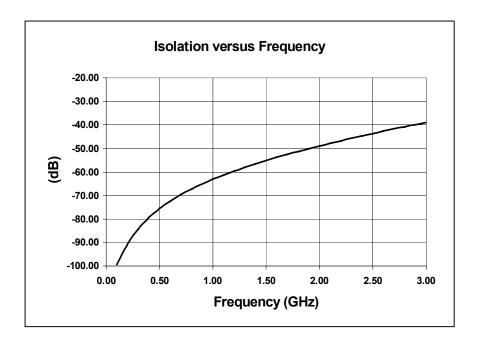
#### **Performance Curves**

#### Insertion Loss



The Insertion Loss curve shows the worst case loss versus frequency at Vdd = +5.0 +/- 10%, Ta = 25C, Z0 = 75 Ohms

#### Isolation

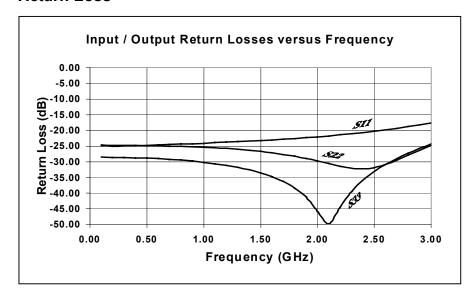


The Isolation curve shows the typical isolation of an "off" state output to the insertion path.

2002 1021W

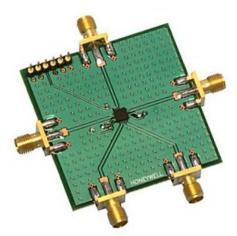
Preliminary

#### Return Loss



The return loss curve shows the input return loss S11, the output return loss in the insertion path S22, and the output return loss in the isolation state S33.

#### **Evaluation Circuit Board**



**HRF-SW1021 Evaluation Board** 

Honeywell's evaluation board provides an easy to use method of evaluating the RF performance of our switch. Simply connect power, DC and RF signals to be measuring switch performance in less than 10 minutes.



Preliminary

## **Evaluation Circuit Board Layout Design Details**

Item	Description
PCB	Impedance Matched Multi-Layer FR4
Switch	HRF-SW1021 RF Switch
Chip Capacitor	Panasonic Model ECU-E1C103KBQ Capacitor, .01uf 0402 10% 16V
RF Connector	Johnson Connectors Model 142-0701-801 SMA RF Coaxial Connector
DC Pin	Mil-Max Model 800-10-064-10-001 Header Pins

### **Ordering Information**

Ordering Number	Product
HRF-SW1021-B	Delivered In Chip Tubes
HRF-SW1021-TR	Delivered On Tape And Reel <sup>2</sup>
HRF-SW1021-E	Engineering Evaluation Board

(Note 2) Contact Honeywell for details

LPCC<sup>™</sup> is a registered Trademark of ASAT Ltd.

Honeywell reserves the right to make changes to improve reliability, function or design. Honeywell does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights nor the rights of others.