FULL SIZE D.I.L. M package M6321 thru M6322

HALF SIZE D.I.L. H package H6321 thru H6322



Thru-Hole/Gull Wing

Industrial: -40° to +85°C 1 KHz to 125 MHz

GUARANTEED CAPTURE RANGE/ABSOLUTE PULL RANGE

Guaranteed Capture Range (GCR) and Absolute Pull Range (APR) are terms often used interchangeably. MF's Guaranteed Capture Range (GCR) is defined as the minimum guaranteed frequency deviation or "pull" (in ppm) around the nominal frequency, with all effects of temperature, variations in V_{DD} and load taken into account. This amount of absolute frequency deviation is available under all operating conditions for modulation or capturing other signals. No additional frequency capture allowances are necessary.

FEATURES

- Industrial temperature range of -40 to +85°C allows for use in harsh environmental conditions
- · Excellent incremental and best-straight-line linearity
- Start-up time is less than 5ms
- · Each unit is ATE-tested to guarantee full compliance with all electrical specifications

TYPICAL APPLICATIONS

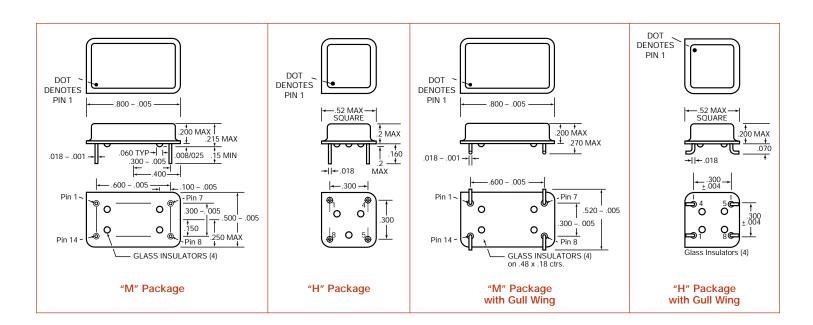
- · Phase locked loops and data acquisition projects, including:
- xDSL customer premise equipment
- Cable modems
- ATM/SONET/SDH

Description

These thru-hole VCXOs generate a 3.3 volt HCMOS/TTL frequency output which is controlled ("pulled") by an input voltage. MF Electronics' VCXO specification defines not only the end-point frequency/ voltage parameters, but also the center voltage at which the nominal frequency is acheived.

CONNECTIONS

	Full Size	Half Size		
Pin 1.	Control Voltage, V _C	Control Voltage, V _C		
Pin 4.		Ground & Case		
Pin 5.		Output		
Pin 7.	Ground & Case			
Pin 8.	Output	+3.3V, V _{DD}		
Pin14.	+3.3V, V _{DD}			







VOLTAGE CONTROLLED CRYSTAL OSCILLATORS HCMOS/TTL 3.3V

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Center Frequency is at 1.5V with ±50 ppm stability

MODEL	Control Voltage (Volts)	Frequency Deviation (ppm)	Guaranteed Capture Range (ppm)	Control Voltage at Center Frequency	Center Frequency Stability (ppm)
6321	0.5 to 2.5	± 75 to 150	± 75	1.5	± 30, typ
6322	0.5 to 2.5	± 100 to 200	± 100	1.5	± 50, max

DESCRIPTIONS

M/201 II/201	75
M6321, H6321	±75 ppm capture when using using 0.5 to 2.5V
	control-voltage and 1.5V center with ±50 ppm stability
M6322, H6322	±100 ppm capture when using using 0.5 to 2.5V
	control-voltage and 1.5V center with ±50 ppm stability

ENVIRONMENTAL SPECIFICATIONS

Temperature Range

Operating -40° to +85°C Storage -55° to +125°C

Temperature Cycle – Not to exceed ±5 ppm change when exposed to 2 hours maximum at each temperature from 0 to 120°C, with 25°C reference

Shock – 1000 Gs, 0.35 ms, 1/2 sine wave, 3 shocks in each plane **Vibration** – 10-2000 Hz of .06" d.a. or 20 Gs, whichever is less

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Humidity - Resistant to 85° R.H. at 85°C

MECHANICAL SPECIFICATIONS

Gross Leak – Each unit checked in 125°C fluorocarbon

Fine Leak – Mass spectrometer leak rate less than 2 X 10^{-8} atmos, cc/sec of helium

Pins - Kovar, nickel plated with 60/40 solder coat

Bend Test – Will withstand two bends of 90° from reference

Header - Steel, with nickel plate

Case - Stainless steel, type 304

Marking - Permanent black epoxy ink or laser marked

Resistance to Solvents - MIL STD 202, Method 215

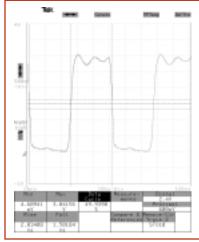


Fig. 1 M6323-19.44M, without load

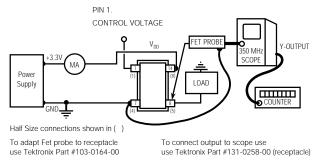
ELECTRICAL SPECIFICATIONS

Frequency Stability Includes calibration at 25°C, operating temperature, change of input voltage, change of load, shock and vibration.

Center Frequency Range

 $V_C = 2.5V$ 1 KHz to 125 MHz

	0			
	MIN	TYP	MAX	UNITS
Input Voltage, V _{DD}	3.0	3.3	3.6	volts
Input Current				
1 KHz to 10 MHz		8	14	mA
10.1 to 25 MHz		15	20	mA
25.1 to 50 MHz		20	30	mA
50.1 to 75 MHz		25	35	mA
75.1 to 125 MHz		30	40	mA
Output Levels				
"0" Level, sinking 16 mA			0.4	volts
"1" Level, sourcing 8 mA	V _{DD} 4			volts
Rise and Fall Times				
CMOS, 15 pf,				
20 to 80% (<60 MHz)		3.0	4	ns
CMOS, 30 pf,		4.0	-	
20 to 80% (<60 MHz) CMOS, 50 pf,		4.0	5	ns
20 to 80% (<60 MHz)		6.0	8	ns
,		0.0	U	113
CMOS, 15 pf,		2.0	2.5	
20 to 80% (>60 MHz) CMOS, 30 pf,		2.0	2.5	ns
20 to 80% (>60 MHz)		3.0	4.5	ns
,		3.0	4.5	113
Symmetry CMOS @ F000 V		40/50	45/55	
CMOS, @ 50% V _{DD}		48/52	45/55	percent
Aging				
First year		3		ppm
After first year		1		ppm/yr
Input Impedance		15	1000	Kohms
Control Voltage Bandwidth	15	150		KHz



ALL OSCILLATORS HAVE INTERNAL BYPASS CAPACITORS

TEST CIRCUIT



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M6322-19.412M, TYPICAL

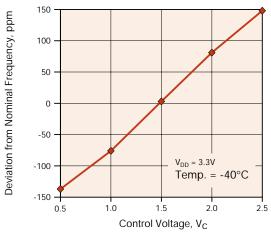


Fig. 2 Frequency vs. Control Voltage

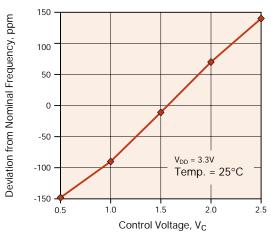


Fig. 3 Frequency vs. Control Voltage

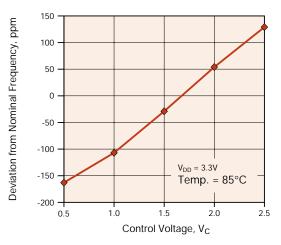


Fig. 4 Frequency vs. Control Voltage

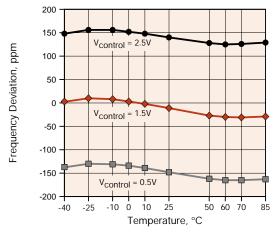
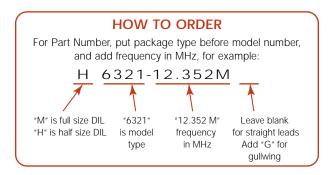


Fig. 5 Frequency Deviation vs. Temperature



SS# Rev. M6321 A



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