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1-of-8 Decoder with 3-State Output



ADE-205-397 (Z) 1st. Edition Sep. 2000

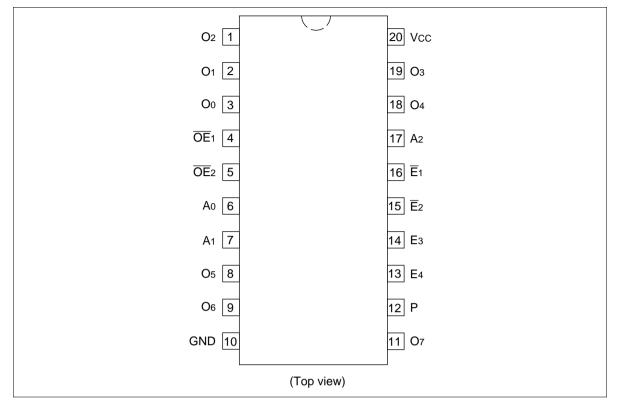
Description

The HD74AC538 decoder/demultiplexer accepts three Address (A0 to A2) input signal and decodes them to select one of eight mutually exclusive outputs. A polarity control input (P) determines whether the outputs are active LOW or active HIGH. A HIGH signal on either of the active LOW output Enable (\overline{OE}) inputs forces all outputs to the high impedance state. Two active HIGH and two active LOW input enables are available for easy expansion to 1-of-32 decoding with four packages, or for data demultiplexing to 1-of-8 or 1-of-16 destinations.

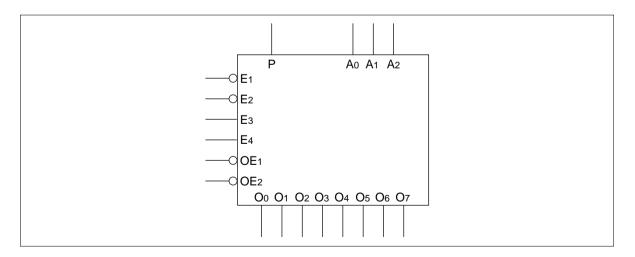
Features

- Output Polarity Control
- Data Demultiplexing Capability
- Multiple Enables for Expansion
- Outputs Source/Sink 24 mA

Pin Arrangement



Logic Symbol



Pin Names

A₀ to A₂ Address Inputs

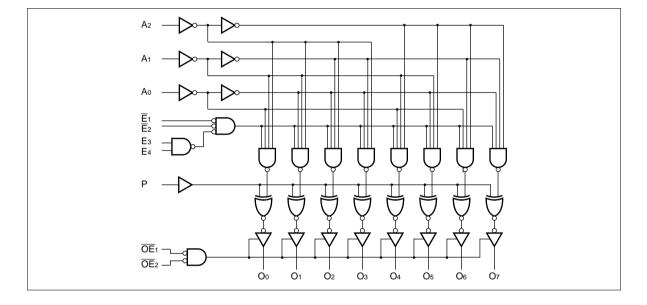
 $\overline{E}_1, \overline{E}_2$ Enable Inputs (Active LOW) E_3, E_4 Enable Inputs (Active HIGH)

P Polarity Control Input

 \overline{OE}_1 , \overline{OE}_2 Output Enable Inputs (Active LOW)

O₀ to O₇ 3-State Outputs

Logic Diagram



Truth Table

	Inputs						Outputs										
Function	OE,	OE ₂	Ε ₁	$\overline{E}_{\scriptscriptstyle 2}$	E ₃	E ₄	A ₂	A ₁	A_0	O ₀	O ₁	O ₂	O ₃	O ₄	O ₅	O ₆	O ₇
High impedance	Н	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Z	Z	Z	Z	Z	Z	Z
	Z	Н	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Z	Z	Z	Z	Z	Z	Z	Z
Disable	L	L	Н	Χ	Χ	Χ	Χ	Χ	Χ	Out	puts	equa	l inpu	ıt			
	L	L	Χ	Н	Χ	Χ	Χ	Χ	Χ								
	L	L	Χ	Χ	L	Χ	Χ	Χ	Χ								
	L	L	Χ	Χ	Χ	L	Χ	Χ	Χ								
Active HIGH output	L	L	L	L	Н	Н	L	L	L	Н	L	L	L	L	L	L	L
(P = L)	L	L	L	L	Н	Н	L	L	Н	L	Н	L	L	L	L	L	L
	L	L	L	L	Н	Н	L	Н	L	L	L	Н	L	L	L	L	L
	L	L	L	L	Н	Н	L	Н	Н	L	L	L	Н	L	L	L	L
	L	L	L	L	Н	Н	Н	L	L	L	L	L	L	Н	L	L	L
	L	L	L	L	Н	Н	Н	L	Н	L	L	L	L	L	Н	L	L
	L	L	L	L	Н	Н	Н	Н	L	L	L	L	L	L	L	Н	L
	L	L	L	L	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	Н
Active LOW output	L	L	L	L	Н	Н	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
(P = L)	L	L	L	L	Н	Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н
	L	L	L	L	Н	Н	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н
	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
	L	L	L	L	Н	Н	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н
	L	L	L	L	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н
	L	L	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н
	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L

H: High Voltage Level
L: Low Voltage Level

X : ImmaterialZ : High Impedance

DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	80	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = Worst case
Maximum quiescent supply current	I _{cc}	8.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, $Ta = 25^{\circ}\text{C}$

AC Characteristics: HD74AC538

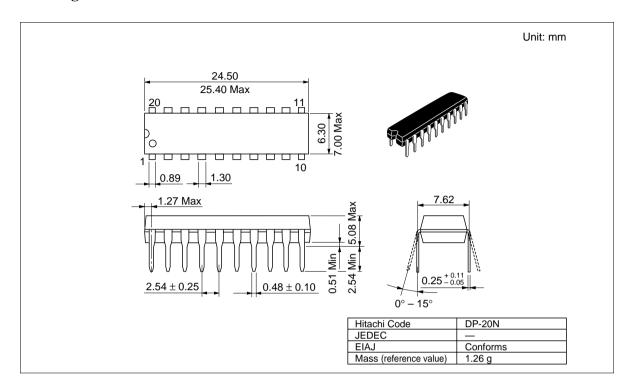
			Ta = +25°C C _L = 50 pF		Ta = -40° C to $+85^{\circ}$ C C _L = 50 pF			
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	10.5	17.5	1.0	20.0	ns
A_n to O_n		5.0	1.0	8.0	12.5	1.0	14.0	
Propagation delay	t _{PHL}	3.3	1.0	9.5	17.5	1.0	20.0	ns
A_n to O_n		5.0	1.0	7.0	12.0	1.0	14.0	
Propagation delay	t _{PLH}	3.3	1.0	11.0	19.5	1.0	23.0	ns
\overline{E}_1 , or \overline{E}_2 to O_n		5.0	1.0	8.0	14.5	1.0	16.5	
Propagation delay	t _{PHL}	3.3	1.0	10.0	19.5	1.0	23.0	ns
\overline{E}_1 , or \overline{E}_2 to O_n		5.0	1.0	8.0	14.5	1.0	16.5	_
Propagation delay	t _{PLH}	3.3	1.0	11.0	19.5	1.0	23.0	ns
\overline{E}_3 , or \overline{E}_4 to O_n		5.0	1.0	8.5	14.5	1.0	17.0	_
Propagation delay	t _{PHL}	3.3	1.0	10.5	20.0	1.0	23.5	ns
\overline{E}_3 , or \overline{E}_4 to O_n		5.0	1.0	8.0	15.0	1.0	18.0	_
Propagation delay	t _{PLH}	3.3	1.0	10.5	15.5	1.0	17.5	ns
P to O _n		5.0	1.0	9.0	11.0	1.0	12.5	_
Propagation delay	t _{PHL}	3.3	1.0	9.0	15.0	1.0	17.0	ns
P to O _n		5.0	1.0	7.5	10.5	1.0	11.5	_
Propagation delay	t _{PZH}	3.3	1.0	7.0	14.0	1.0	15.5	ns
\overline{OE}_{n} to O_{n}		5.0	1.0	5.0	8.5	1.0	9.5	_
Propagation delay	t _{PZL}	3.3	1.0	8.5	16.5	1.0	19.0	ns
\overline{OE}_{n} to O_{n}		5.0	1.0	5.5	9.5	1.0	11.5	_
Propagation delay	t _{PHZ}	3.3	1.0	7.0	14.0	1.0	15.5	ns
\overline{OE}_{n} to O_{n}		5.0	1.0	6.0	10.5	1.0	11.5	
Propagation delay	t _{PLZ}	3.3	1.0	9.0	14.5	1.0	17.0	ns
$\overline{OE}_{\mathtt{n}}$ to $O_{\mathtt{n}}$		5.0	1.0	7.0	10.5	1.0	12.0	_

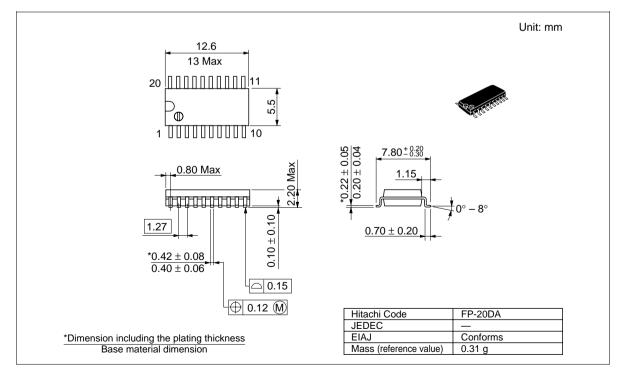
Note: 1. Voltage Range 3.3 is 3.3 V \pm 0.3 V Voltage Range 5.0 is 5.0 V \pm 0.5 V

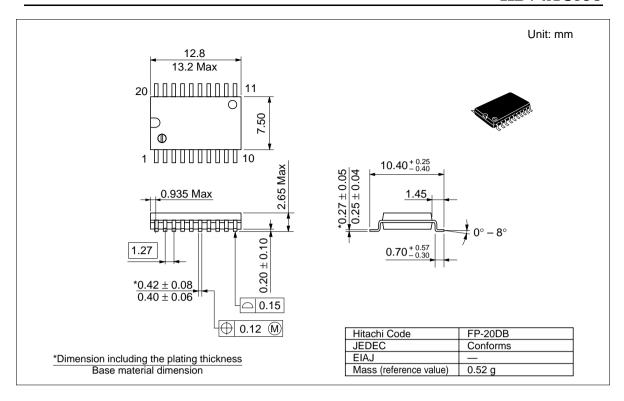
Capacitance

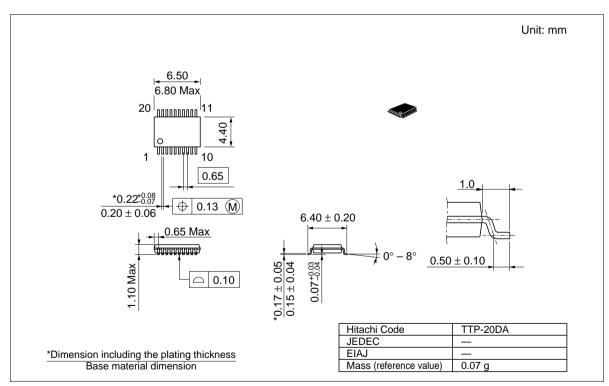
Item	Symbol	Тур	Unit	Condition
Input capacitance	C _{IN}	4.5	pF	$V_{cc} = 5.5 \text{ V}$
Power dissipation capacitance	C_{PD}	100	pF	V _{CC} = 5.0 V

Package Dimensions









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