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Synchronous Up/Down Decade Counter (Dual Clock Line)
Synchronous Up/Down 4-bit Binary Counter (Dual Clock Line)



ADE-205-465 (Z) 1st. Edition Sep. 2000

Description

The HD74HC192 is a decade counter, and the HD74HC193 is a binary counter. Both counters have two separate clock inputs, an up count input and a down count input. All outputs of the flip-flops are simultaneously triggered on the low to high transition of either clock while the other input is held high. The direction of counting is determined by which input is clocked.

These counters may be preset by entering the desired data on the data A, data B, data C, and data D inputs. When the load input is taken low the data is loaded independently of either clock input. This feature allows the counters to be used as divide-by-n counters by modifying the count length with the preset inputs.

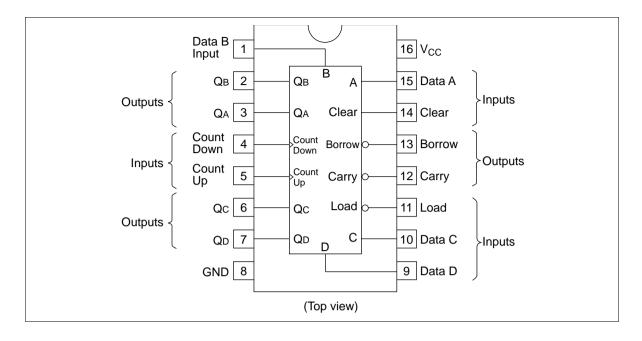
In addition both counters can also be cleared. This is accomplished by inputting a high on the clear input. All 4 internal stages are set to a low level independently of either count input.

Both a borrow and carry output are provided to enable cascading of both up and down counting functions. The borrow output produces a negative going pulse when the counter underflows and the carry outputs a pulse when the counter overflows. The counters can be cascaded by connecting the carry and borrow outputs of one device to the count up and count down inputs, respectively, of the next device.

Features

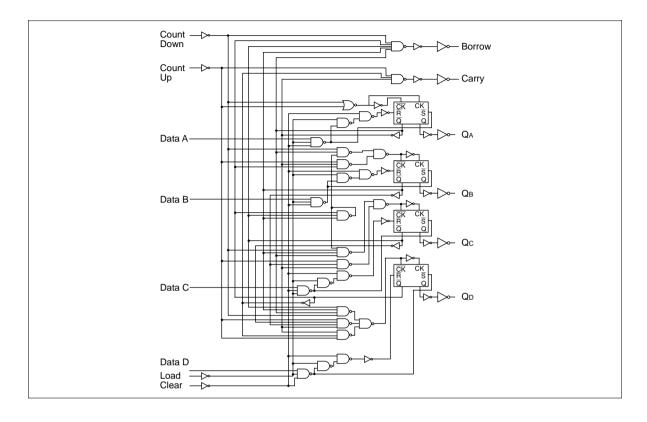
- High Speed Operation: t_{pd} (Clock Up or Count Down to Q) = 21 ns typ ($C_L = 50 \text{ pF}$)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 μA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

Pin Arrangement

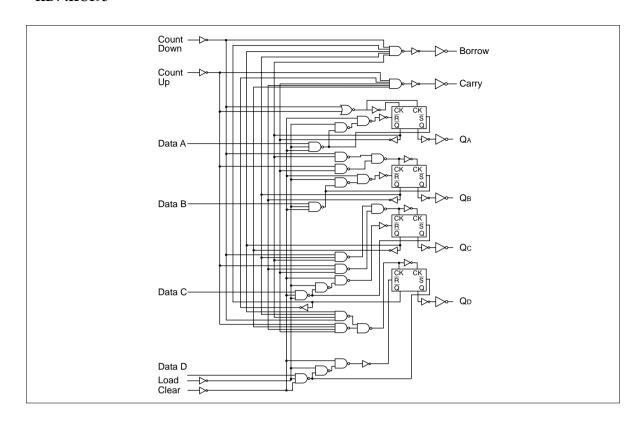


Logic Diagram

HD74HC192



HD74HC193

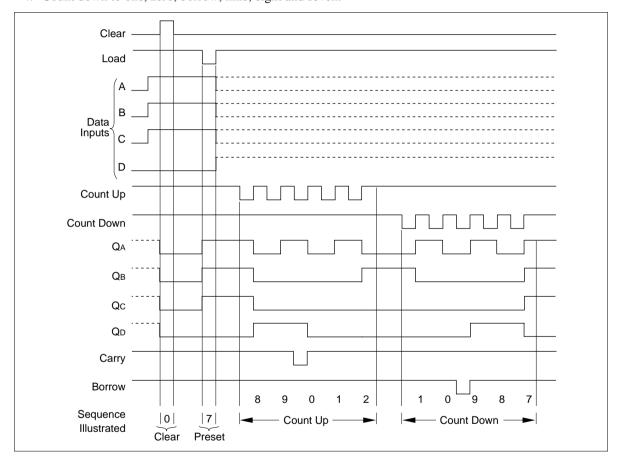


Timing Chart

HD74HC192

Illustrated below is the following sequence:

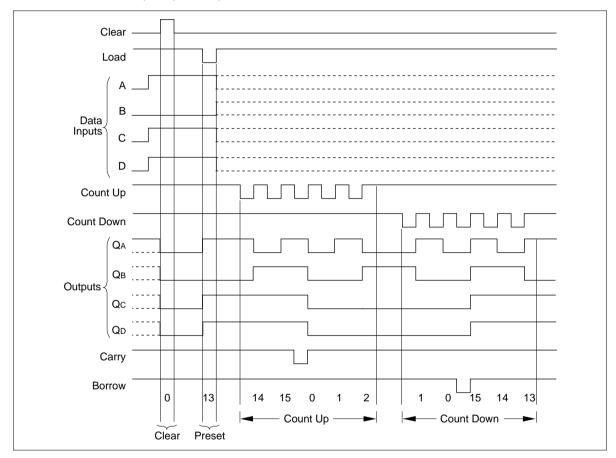
- 1. Clear outputs to zero.
- 2. Load (preset) to binary seven.
- 3. Count up to eight, nine, zero, one and two.
- 4. Count down to one, zero, borrow, nine, eight and seven.



HD74HC193

Illustrated below is the following sequence:

- 1. Clear outputs to zero.
- 2. Load (preset) to binary thirteen.
- 3. Count up to fourteen, fifteen, zero, one and two.
- 4. Count down to one, zero, borrow, fifteen and thirteen.



DC Characteristics

			Ta =	= 25°(Ta = - +85°C	–40 to			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	าร
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	<u></u>	_	3.15	_	_		
		6.0	4.2	_	_	4.2	_	_		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5		4.4	_	_		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	3 —	_	4.13	_	=		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	3 —	_	5.63	_	=		$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	=		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5	_	_	0.26	_	0.33	=		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	=		I _{OL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{cc} or GN	ND
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μА	Vin = V _{cc} or GN	ND, lout = $0 \mu A$

AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

	Ta = -40 to
Ta = 25°C	+85°C

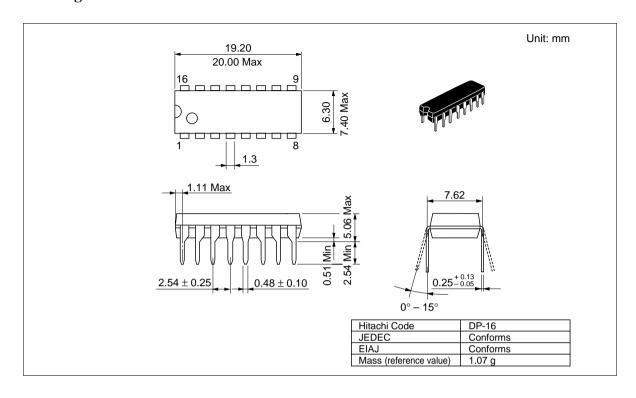
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Maximum clock	f _{max}	2.0	_	_	4	_	3	MHz	
frequency		4.5		_	20	_	16	_	
		6.0	_	_	24	_	19	=	
Propagation delay	t _{PLH}	2.0	_	_	140	_	175	ns	Count up to Carry
time		4.5		14	28	_	35	_	
		6.0	_	_	24	_	30	_	
	t _{PHL}	2.0	_	_	130	_	165		
		4.5	_	15	26	_	33	_	
		6.0	_	_	22	_	28	_	
	t _{PLH}	2.0	_	_	130	_	165	_	Count down to Borrow
		4.5		14	26	_	33	_	
		6.0	_	_	22	_	28	=	
	t _{PHL}	2.0	_	_	130	_	165	=	
		4.5		15	26	_	33	_	
		6.0	_	_	22	_	28	=	
	t _{PLH}	2.0	_	_	215	_	270	=	Count up or down to Q
		4.5		21	43	_	54	_	
		6.0	_	_	37	_	46	=	
	t _{PHL}	2.0	_	_	275	_	345	=	
		4.5		21	55	_	69	_	
		6.0		_	47	_	59	_	
	t _{PLH}	2.0	_	_	230	_	290	=	Load to Q
		4.5		17	46	_	58	_	
		6.0		_	39	_	49	_	
	t _{PHL}	2.0	_	_	290	_	365	=	
		4.5	_	23	58	_	73	=	
		6.0	_	_	49	_	62	=	
	t _{PHL}	2.0	_	_	265	_	335	=	Clear to Q
		4.5	_	24	53	_	66	_	
		6.0	_	_	45	_	56	_	
Pulse width	t _w	2.0	80	_	_	100	_	ns	
		4.5	16	8	_	20	_	_	
		6.0	14	_	_	17	_	_	

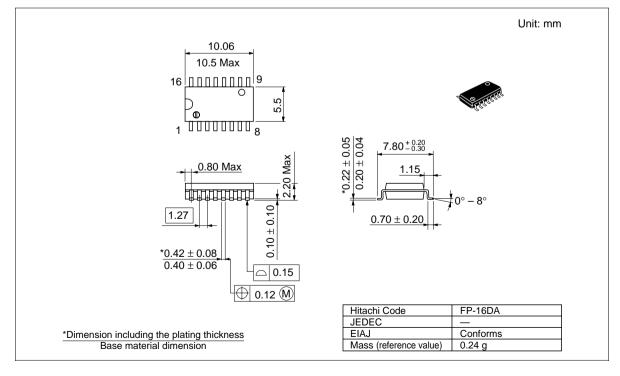
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$) (cont)

	Ta = -40 to
Ta = 25°C	+85°C

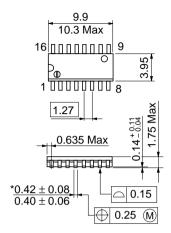
Symbol	V_{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
t _h	2.0	5	_	_	5	_	ns	Data to Load
	4.5	5	-3	_	5	_		
	6.0	5	_	_	5	_		
t _{su}	2.0	100	_	_	125	_	ns	Data to Load
	4.5	20	4	_	25	_	=	
	6.0	17	_	_	21	_	=	
t _{rem}	2.0	50	_	_	65	_	ns	Clear to Clock
	4.5	10	-1	_	13	_	=	
	6.0	9	_	_	11	_	-	
t _{TLH}	2.0	_	_	75	_	95	ns	
\mathbf{t}_{THL}	4.5	_	5	15	_	19	=	
	6.0	_	_	13	_	16	_	
Cin	_	_	5	10	_	10	pF	
	$t_{\rm su}$ $t_{\rm rem}$ $t_{\rm TLH}$ $t_{\rm THL}$	$\begin{array}{c} t_{\text{h}} & 2.0 \\ \hline 4.5 \\ \hline 6.0 \\ \hline \\ t_{\text{su}} & 2.0 \\ \hline 4.5 \\ \hline 6.0 \\ \hline \\ t_{\text{rem}} & 2.0 \\ \hline \\ 4.5 \\ \hline \hline 6.0 \\ \hline \\ \\ t_{\text{TLH}} & 2.0 \\ \hline \\ t_{\text{THL}} & 4.5 \\ \hline \\ \hline \\ \hline \\ \hline \\ \hline \\ \\ \hline \\ \hline \\ \hline \\ \hline $	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c} t_{\text{h}} & \begin{array}{c} 2.0 & 5 & - \\ 4.5 & 5 & -3 \\ \hline 6.0 & 5 & - \\ \end{array} \\ t_{\text{su}} & \begin{array}{c} 2.0 & 100 & - \\ \hline 4.5 & 20 & 4 \\ \hline 6.0 & 17 & - \\ \end{array} \\ t_{\text{rem}} & \begin{array}{c} 2.0 & 50 & - \\ \hline 4.5 & 10 & -1 \\ \hline 6.0 & 9 & - \\ \end{array} \\ t_{\text{TLH}} & \begin{array}{c} 2.0 & - & - \\ \hline 4.5 & - & 5 \\ \hline 6.0 & - & - \\ \end{array} \end{array}$	$\begin{array}{c} t_{\text{h}} \\ & \begin{array}{ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c} t_{\rm h} \\ t_{\rm h} \\ \hline \\ 2.0 \\ \hline \\ 4.5 \\ \hline \\ 5 \\ \hline \\ -3 \\ \hline \\ -3 \\ \hline \\ -5 \\ \hline $	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

Package Dimensions

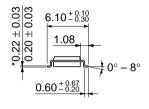




Unit: mm







*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN				
JEDEC	Conforms				
EIAJ	Conforms				
Mass (reference value)	0.15 g				

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