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HD74AC02

Quad 2-Input NOR Gate

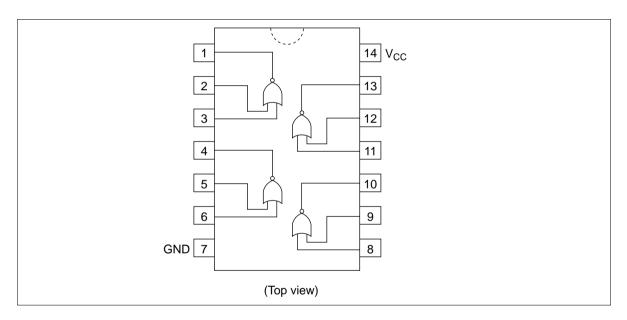


ADE-205-356 (Z) 1st. Edition Sep. 2000

Feature

• Outputs Source/Sink 24 mA

Pin Arrangement



DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	40	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = Worst case
Maximum quiescent supply current	I _{cc}	4.0	μА	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, $Ta = 25^{\circ}\text{C}$

HD74AC02

AC Characteristics

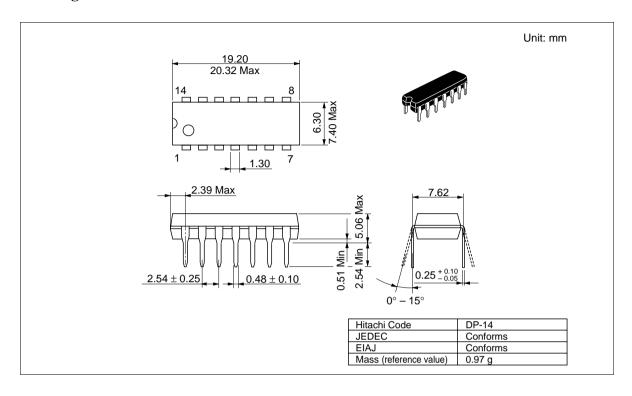
ltem	Symbol	V _{cc} (V)*1	Ta = +25°C C _∟ = 50 pF			Ta = -40° C to $+85^{\circ}$ C C _L = 50 pF		
			Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.0	6.0	1.0	6.5	_
Propagation delay	t _{PHL}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.5	6.5	1.0	7.0	_

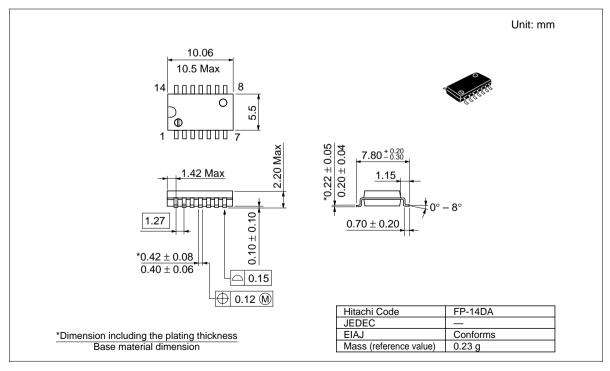
Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

Capacitance

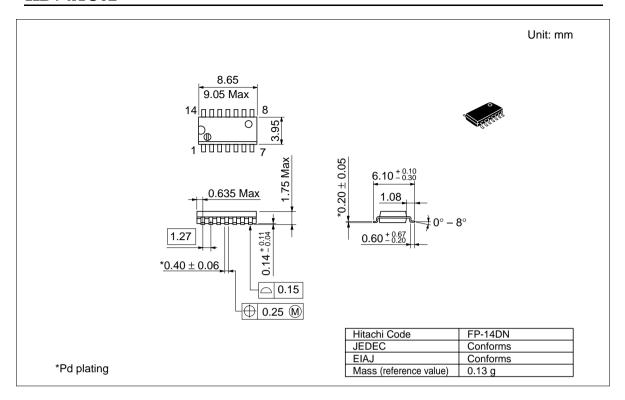
Item	Symbol	Тур	Unit	Condition	
Input capacitance	C _{IN}	4.5	pF	$V_{CC} = 5.5 \text{ V}$	
Power dissipation capacitance	C_{PD}	30.0	pF	$V_{CC} = 5.0 \text{ V}$	

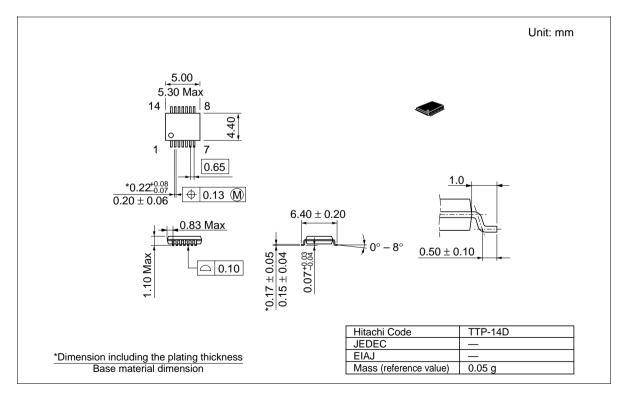
Package Dimensions





HD74AC02





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