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Renesas Technology Corp.  
Customer Support Dept.  
April 1, 2003

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# HD74ALVCH16831

## 1-to 4 Address Register / Driver with 3-state Outputs



ADE-205-194 (Z)  
Preliminary  
1st. Edition  
March 1998

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### Description

This 1-bit to 4-bit address register / driver is designed for 2.3 V to 3.6 V  $V_{CC}$  operation. The device is ideal for use in applications in which a single address bus is driving four separate memory locations. The HD74ALVCH16831 can be used as a buffer or a register, depending on the logic level of the select ( $\overline{SEL}$ ) input. When  $\overline{SEL}$  is logic high, the device is in the buffer mode. The outputs follow the inputs and are controlled by the two output enable ( $\overline{OE}$ ) controls. Each  $\overline{OE}$  controls two groups of nine outputs. When  $\overline{SEL}$  is logic low, the device is in the register mode. The register is an edge triggered D-type flip flop. On the positive transition of the clock (CLK) input, data set up at the A inputs is stored in the internal registers.  $\overline{OE}$  controls operate the same as in buffer mode. When  $\overline{OE}$  is logic low, the outputs are in a normal logic state (high or low logic level). When  $\overline{OE}$  is logic high, the outputs are in the high impedance state. To ensure the high impedance state during power up or power down,  $\overline{OE}$  should be tied to  $V_{CC}$  through a pullup resistor; the minimum value of the resistor is determined by the current sinking capability of the driver.  $\overline{SEL}$  and  $\overline{OE}$  do not affect the internal operation of the flip flops. Old data can be retained or new data can be entered while the outputs are in the high impedance state. Active bus hold circuitry is provided to hold unused or floating data inputs at a valid logic level.

### Features

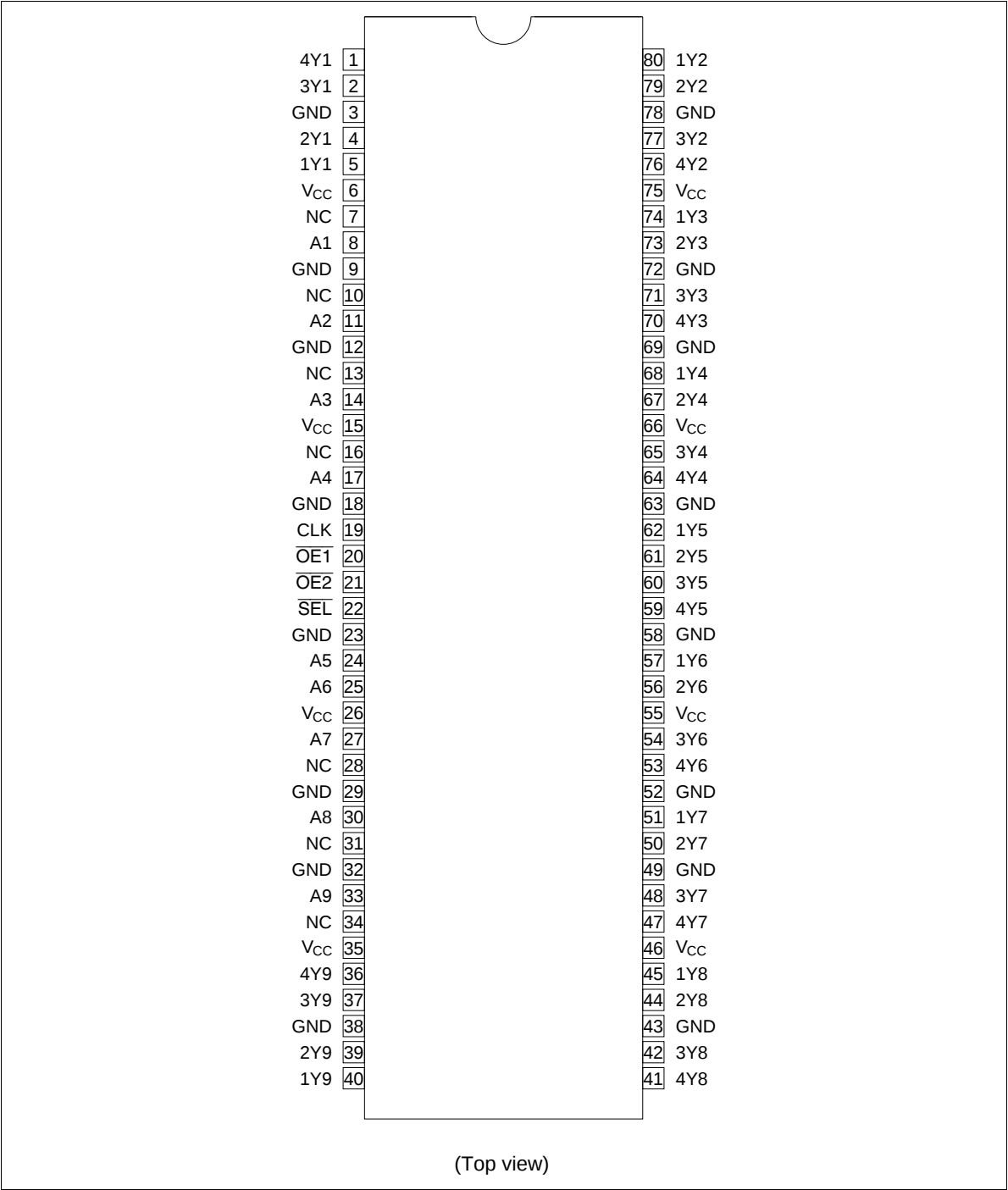
- $V_{CC} = 2.3 \text{ V to } 3.6 \text{ V}$
- Typical  $V_{OL}$  ground bounce  $< 0.8 \text{ V}$  ( $@V_{CC} = 3.3 \text{ V}$ ,  $T_a = 25^\circ\text{C}$ )
- Typical  $V_{OH}$  undershoot  $> 2.0 \text{ V}$  ( $@V_{CC} = 3.3 \text{ V}$ ,  $T_a = 25^\circ\text{C}$ )
- High output current  $\pm 24 \text{ mA}$  ( $@V_{CC} = 3.0 \text{ V}$ )
- Bus hold on data inputs eliminates the need for external pullup / pulldown resistors

Function Table

| Inputs |     |     |   | Output Y |
|--------|-----|-----|---|----------|
| OE     | SEL | CLK | A |          |
| H      | X   | X   | X | Z        |
| L      | H   | X   | L | L        |
| L      | H   | X   | H | H        |
| L      | L   | ↑   | L | L        |
| L      | L   | ↑   | H | H        |

H : High level  
L : Low level  
X : Immaterial  
Z : High impedance  
↑ : Low to high transition

Pin Arrangement



## Absolute Maximum Ratings

| Item                                                                               | Symbol                | Ratings                | Unit             | Conditions                  |
|------------------------------------------------------------------------------------|-----------------------|------------------------|------------------|-----------------------------|
| Supply voltage                                                                     | $V_{CC}$              | −0.5 to 4.6            | V                |                             |
| Input voltage <sup>*1</sup>                                                        | $V_I$                 | −0.5 to 4.6            | V                |                             |
| Output voltage <sup>*1, 2</sup>                                                    | $V_O$                 | −0.5 to $V_{CC} + 0.5$ | V                |                             |
| Input clamp current                                                                | $I_{IK}$              | −50                    | mA               | $V_I < 0$                   |
| Output clamp current                                                               | $I_{OK}$              | ±50                    | mA               | $V_O < 0$ or $V_O > V_{CC}$ |
| Continuous output current                                                          | $I_O$                 | ±50                    | mA               | $V_O = 0$ to $V_{CC}$       |
| $V_{CC}$ , GND current / pin                                                       | $I_{CC}$ or $I_{GND}$ | ±100                   | mA               |                             |
| Maximum power dissipation at $T_a = 55^\circ\text{C}$ (in still air) <sup>*3</sup> | $P_T$                 | 1                      | W                | TVSOP                       |
| Storage temperature                                                                | $T_{stg}$             | −65 to 150             | $^\circ\text{C}$ |                             |

Notes: Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

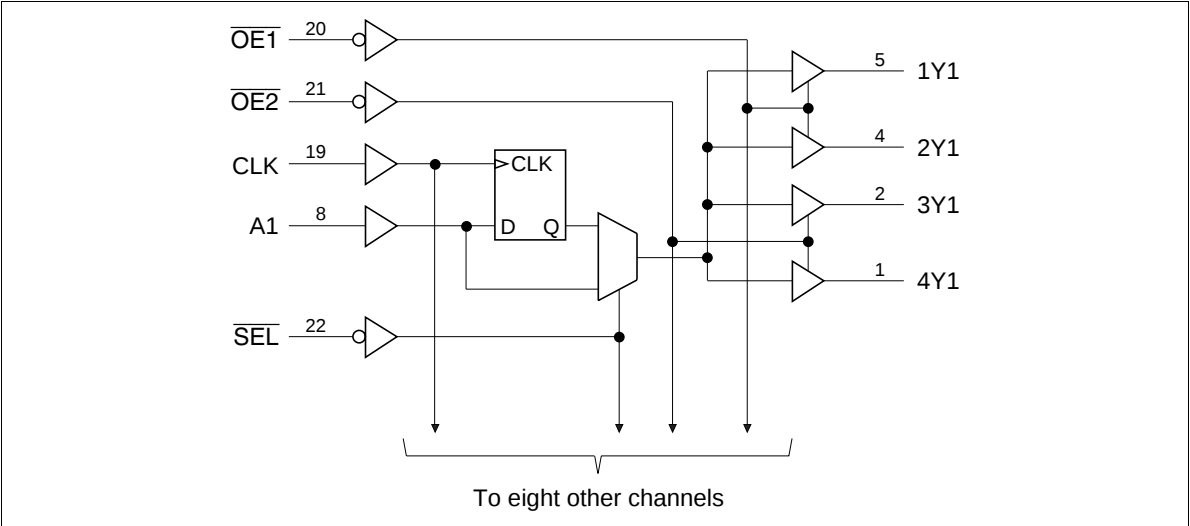
1. The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.
2. This value is limited to 4.6 V maximum.
3. The maximum package power dissipation is calculated using a junction temperature of  $150^\circ\text{C}$  and a board trace length of 750 mils.

## Recommended Operating Conditions

| Item                               | Symbol                | Min | Max      | Unit             | Conditions              |
|------------------------------------|-----------------------|-----|----------|------------------|-------------------------|
| Supply voltage                     | $V_{CC}$              | 2.3 | 3.6      | V                |                         |
| Input voltage                      | $V_I$                 | 0   | $V_{CC}$ | V                |                         |
| Output voltage                     | $V_O$                 | 0   | $V_{CC}$ | V                |                         |
| High level output current          | $I_{OH}$              | —   | −12      | mA               | $V_{CC} = 2.3\text{ V}$ |
|                                    |                       | —   | −12      |                  | $V_{CC} = 2.7\text{ V}$ |
|                                    |                       | —   | −24      |                  | $V_{CC} = 3.0\text{ V}$ |
| Low level output current           | $I_{OL}$              | —   | 12       | mA               | $V_{CC} = 2.3\text{ V}$ |
|                                    |                       | —   | 12       |                  | $V_{CC} = 2.7\text{ V}$ |
|                                    |                       | —   | 24       |                  | $V_{CC} = 3.0\text{ V}$ |
| Input transition rise or fall rate | $\Delta t / \Delta v$ | 0   | 10       | ns / V           |                         |
| Operating temperature              | $T_a$                 | −40 | 85       | $^\circ\text{C}$ |                         |

Note: Unused control inputs must be held high or low to prevent them from floating.

Logic Diagram



Electrical Characteristics (Ta = -40 to 85°C)

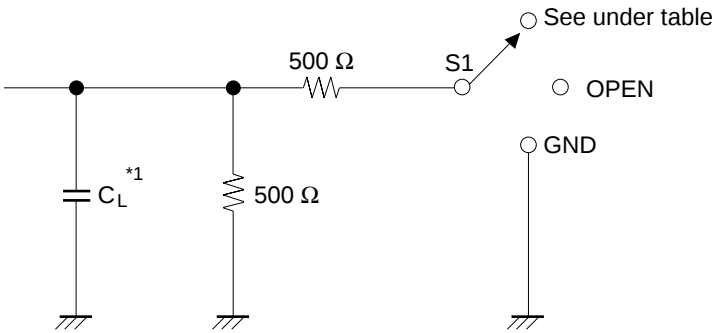
| Item                     | Symbol                 | V <sub>CC</sub> (V) | Min                  | Max  | Unit | Test Conditions                                                                                 |
|--------------------------|------------------------|---------------------|----------------------|------|------|-------------------------------------------------------------------------------------------------|
| Input voltage            | V <sub>IH</sub>        | 2.3 to 2.7          | 1.7                  | —    | V    |                                                                                                 |
|                          |                        | 2.7 to 3.6          | 2.0                  | —    |      |                                                                                                 |
|                          | V <sub>IL</sub>        | 2.3 to 2.7          | —                    | 0.7  |      |                                                                                                 |
|                          |                        | 2.7 to 3.6          | —                    | 0.8  |      |                                                                                                 |
| Output voltage           | V <sub>OH</sub>        | 2.3 to 3.6          | V <sub>CC</sub> -0.2 | —    | V    | I <sub>OH</sub> = -100 μA                                                                       |
|                          |                        | 2.3                 | 2.0                  | —    |      | I <sub>OH</sub> = -6 mA, V <sub>IH</sub> = 1.7 V                                                |
|                          |                        | 2.3                 | 1.7                  | —    |      | I <sub>OH</sub> = -12 mA, V <sub>IH</sub> = 1.7 V                                               |
|                          |                        | 2.7                 | 2.2                  | —    |      | I <sub>OH</sub> = -12 mA, V <sub>IH</sub> = 2.0 V                                               |
|                          |                        | 3.0                 | 2.4                  | —    |      | I <sub>OH</sub> = -12 mA, V <sub>IH</sub> = 2.0 V                                               |
|                          |                        | 3.0                 | 2.0                  | —    |      | I <sub>OH</sub> = -24 mA, V <sub>IH</sub> = 2.0 V                                               |
|                          | V <sub>OL</sub>        | 2.3 to 3.6          | —                    | 0.2  |      | I <sub>OL</sub> = 100 μA                                                                        |
|                          |                        | 2.3                 | —                    | 0.4  |      | I <sub>OL</sub> = 6 mA, V <sub>IL</sub> = 0.7 V                                                 |
|                          |                        | 2.3                 | —                    | 0.7  |      | I <sub>OL</sub> = 12 mA, V <sub>IL</sub> = 0.7 V                                                |
|                          |                        | 2.7                 | —                    | 0.4  |      | I <sub>OL</sub> = 12 mA, V <sub>IL</sub> = 0.8 V                                                |
|                          |                        | 3.0                 | —                    | 0.55 |      | I <sub>OL</sub> = 24 mA, V <sub>IL</sub> = 0.8 V                                                |
|                          |                        |                     |                      |      |      |                                                                                                 |
| Input current            | I <sub>IN</sub>        | 3.6                 | —                    | ±5   | μA   | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                        |
|                          | I <sub>IN (hold)</sub> | 2.3                 | 45                   | —    |      | V <sub>IN</sub> = 0.7 V                                                                         |
|                          |                        | 2.3                 | -45                  | —    |      | V <sub>IN</sub> = 1.7 V                                                                         |
|                          |                        | 3.0                 | 75                   | —    |      | V <sub>IN</sub> = 0.8 V                                                                         |
|                          |                        | 3.0                 | -75                  | —    |      | V <sub>IN</sub> = 2.0 V                                                                         |
|                          |                        | 3.6                 | —                    | ±500 |      | V <sub>IN</sub> = 0 to 3.6 V *1                                                                 |
| Off state output current | I <sub>OZ</sub>        | 3.6                 | —                    | ±10  | μA   | V <sub>OUT</sub> = V <sub>CC</sub> or GND                                                       |
| Quiescent supply current | I <sub>CC</sub>        | 3.6                 | —                    | 40   | μA   | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                        |
|                          | ΔI <sub>CC</sub>       | 3.0 to 3.6          | —                    | 750  | μA   | V <sub>IN</sub> = one input at (V <sub>CC</sub> -0.6) V, other inputs at V <sub>CC</sub> or GND |

Note: 1. This is the bus hold maximum dynamic current required to switch the input from one state to another.

## Switching Characteristics (Ta = -40 to 85°C)

| Item                    | Symbol           | V <sub>CC</sub> (V) | Min | Typ | Max | Unit | FROM<br>(Input)         | TO<br>(Output) |
|-------------------------|------------------|---------------------|-----|-----|-----|------|-------------------------|----------------|
| Maximum clock frequency | f <sub>max</sub> | 2.5±0.2             | 150 | —   | —   | MHz  |                         |                |
|                         |                  | 2.7                 | 150 | —   | —   |      |                         |                |
|                         |                  | 3.3±0.3             | 150 | —   | —   |      |                         |                |
| Propagation delay time  | t <sub>PLH</sub> | 2.5±0.2             | 1.2 | —   | 4.0 | ns   | A                       | Y              |
|                         |                  | 2.7                 | —   | —   | 4.1 |      |                         |                |
|                         |                  | 3.3±0.3             | 1.6 | —   | 3.6 |      |                         |                |
|                         | t <sub>PHL</sub> | 2.5±0.2             | 1.1 | —   | 4.5 |      | CLK                     | Y              |
|                         |                  | 2.7                 | —   | —   | 4.4 |      |                         |                |
|                         |                  | 3.3±0.3             | 1.5 | —   | 3.9 |      |                         |                |
|                         |                  | 2.5±0.2             | 1.3 | —   | 5.2 |      | $\overline{\text{SEL}}$ | Y              |
|                         |                  | 2.7                 | —   | —   | 5.2 |      |                         |                |
|                         |                  | 3.3±0.3             | 1.7 | —   | 4.4 |      |                         |                |
| Output enable time      | t <sub>ZH</sub>  | 2.5±0.2             | 1.1 | —   | 5.1 | ns   | $\overline{\text{OE}}$  | Y              |
|                         |                  | 2.7                 | —   | —   | 5.0 |      |                         |                |
|                         |                  | 3.3±0.3             | 1.2 | —   | 4.3 |      |                         |                |
| Output disable time     | t <sub>HZ</sub>  | 2.5±0.2             | 1.4 | —   | 5.5 | ns   | $\overline{\text{OE}}$  | Y              |
|                         |                  | 2.7                 | —   | —   | 4.7 |      |                         |                |
|                         |                  | 3.3±0.3             | 1.6 | —   | 4.5 |      |                         |                |
| Setup time              | t <sub>su</sub>  | 2.5±0.2             | 2.0 | —   | —   | ns   |                         |                |
|                         |                  | 2.7                 | 2.0 | —   | —   |      |                         |                |
|                         |                  | 3.3±0.3             | 1.6 | —   | —   |      |                         |                |
| Hold time               | t <sub>h</sub>   | 2.5±0.2             | 0.7 | —   | —   | ns   |                         |                |
|                         |                  | 2.7                 | 0.5 | —   | —   |      |                         |                |
|                         |                  | 3.3±0.3             | 1.1 | —   | —   |      |                         |                |
| Pulse width             | t <sub>w</sub>   | 2.5±0.2             | 3.3 | —   | —   | ns   |                         |                |
|                         |                  | 2.7                 | 3.3 | —   | —   |      |                         |                |
|                         |                  | 3.3±0.3             | 3.3 | —   | —   |      |                         |                |
| Input capacitance       | C <sub>IN</sub>  | 3.3                 | —   | 4.5 | —   | pF   | Control inputs          |                |
|                         |                  | 3.3                 | —   | 5.0 | —   |      | Data inputs             |                |
| Output capacitance      | C <sub>O</sub>   | 3.3                 | —   | 7.5 | —   | pF   |                         |                |

Test Circuit

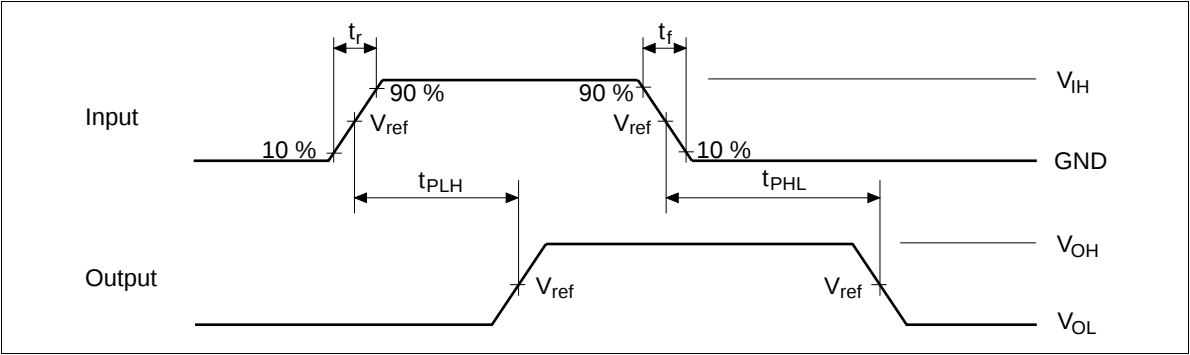


Load Circuit for Outputs

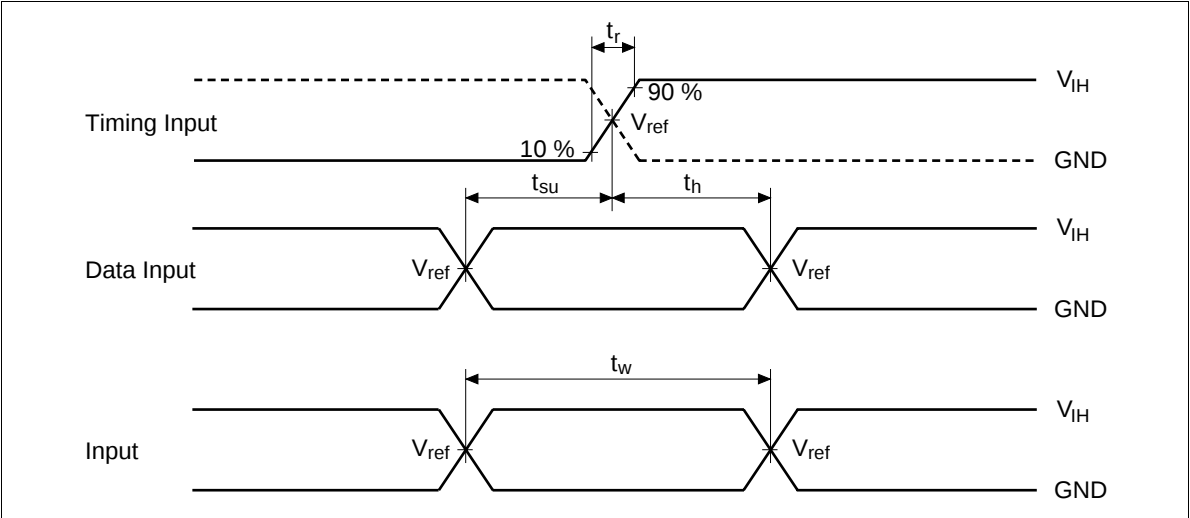
| Symbol                                          | Vcc=2.5±0.2V        | Vcc=2.7V,<br>3.3±0.3V |
|-------------------------------------------------|---------------------|-----------------------|
| t <sub>PLH</sub> /t <sub>PHL</sub>              | OPEN                | OPEN                  |
| t <sub>su</sub> /t <sub>h</sub> /t <sub>w</sub> |                     |                       |
| t <sub>ZH</sub> /t <sub>HZ</sub>                | GND                 | GND                   |
| t <sub>ZL</sub> /t <sub>LZ</sub>                | 2 × V <sub>CC</sub> | 6.0 V                 |
| C <sub>L</sub>                                  | 30 pF               | 50 pF                 |

Note:        1.    C<sub>L</sub> includes probe and jig capacitance.

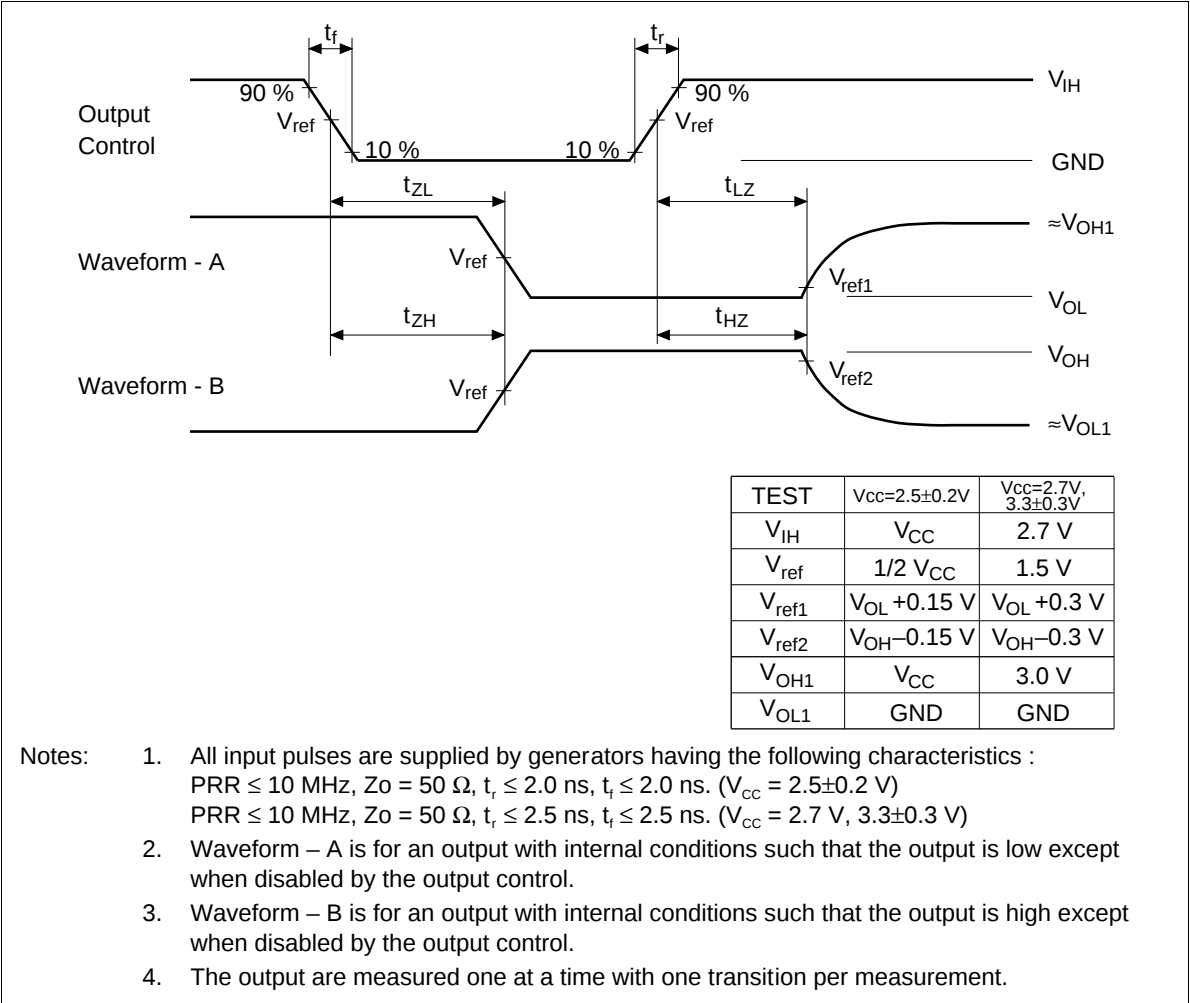
Waveforms – 1



Waveforms – 2

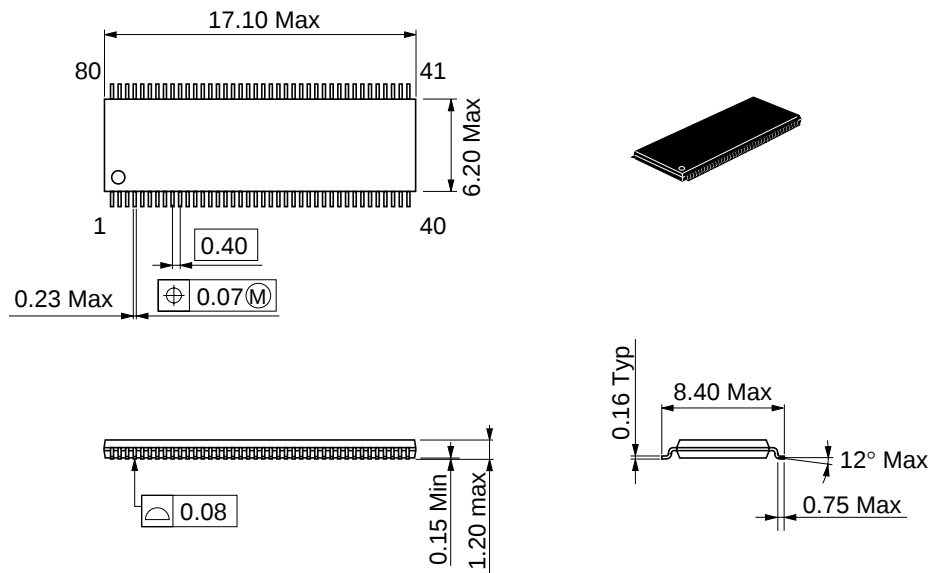


Waveforms – 3



Package Dimensions

Unit : mm



|              |   |
|--------------|---|
| Hitachi code | — |
| EIAJ code    | — |
| JEDEC code   | — |

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# HITACHI

## **Hitachi, Ltd.**

Semiconductor & IC Div.  
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan  
Tel: Tokyo (03) 3270-2111  
Fax: (03) 3270-5109

### **For further information write to:**

Hitachi Semiconductor  
(America) Inc.  
2000 Sierra Point Parkway  
Brisbane, CA. 94005-1897  
U S A  
Tel: 800-285-1601  
Fax: 303-297-0447

Hitachi Europe GmbH  
Continental Europe  
Dornacher Straße 3  
D-85622 Feldkirchen  
München  
Tel: 089-9 91 80-0  
Fax: 089-9 29 30-00

Hitachi Europe Ltd.  
Electronic Components Div.  
Northern Europe Headquarters  
Whitebrook Park  
Lower Cookham Road  
Maidenhead  
Berkshire SL6 8YA  
United Kingdom  
Tel: 01628-585000  
Fax: 01628-585160

Hitachi Asia Pte. Ltd.  
16 Collyer Quay #20-00  
Hitachi Tower  
Singapore 049318  
Tel: 535-2100  
Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd.  
Unit 706, North Tower,  
World Finance Centre,  
Harbour City, Canton Road  
Tsim Sha Tsui, Kowloon  
Hong Kong  
Tel: 27359218  
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