

To all our customers

Regarding the change of names mentioned in the document, such as Hitachi Electric and Hitachi XX, to Renesas Technology Corp.

The semiconductor operations of Mitsubishi Electric and Hitachi were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Hitachi, Hitachi, Ltd., Hitachi Semiconductors, and other Hitachi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Renesas Technology Home Page: <http://www.renesas.com>

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
2. Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein.

The information described here may contain technical inaccuracies or typographical errors.

Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (<http://www.renesas.com>).

4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.

HD74LV1G125A

Bus Buffer Gate with 3-state Output



ADE-205-323D (Z)

5th. Edition
Feb. 2003

Description

The HD74LV1G125A has a bus buffer gate with 3-state output in a 5 pin package. Output is disabled when the associated output enable ($\overline{OE}$) input is high. To ensure the high impedance state during power up or power down, $\overline{OE}$ should be connected to V_{cc} through a pull-down resistor; the minimum value of the resistor is determined by the current sourcing capability of the driver. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

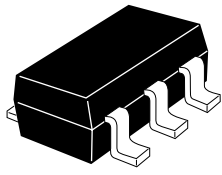
- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV125A
Supply voltage range : 1.65 to 5.5 V
Operating temperature range : -40 to $+85^{\circ}\text{C}$
- All inputs V_{ih} (Max.) = 5.5 V (@ V_{cc} = 0 V to 5.5 V)
All outputs V_o (Max.) = 5.5 V (@ V_{cc} = 0 V, Output : Z)
- Output current ± 6 mA (@ V_{cc} = 3.0 V to 3.6 V), ± 12 mA (@ V_{cc} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G125ACME	CMPAK-5 pin	CMPAK-5V	CM	E (3,000 pcs/reel)
HD74LV1G125AVSE	VSON-5 pin	TNP-5DV	VS	E (3,000 pcs/reel)

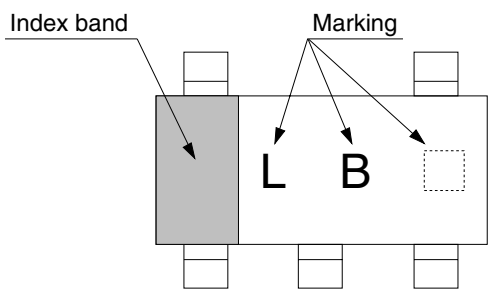
Note: Please consult the sales office for the above package availability.

Outline and Article Indication

• HD74LV1G125A

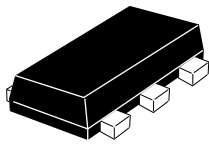


CMPAK-5

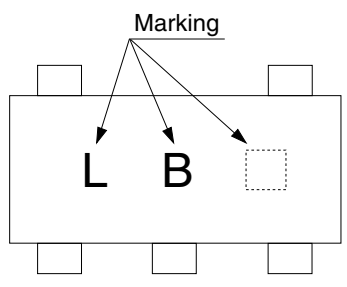


□ = Control code
(— or blank)

• HD74LV1G125A



VSON-5



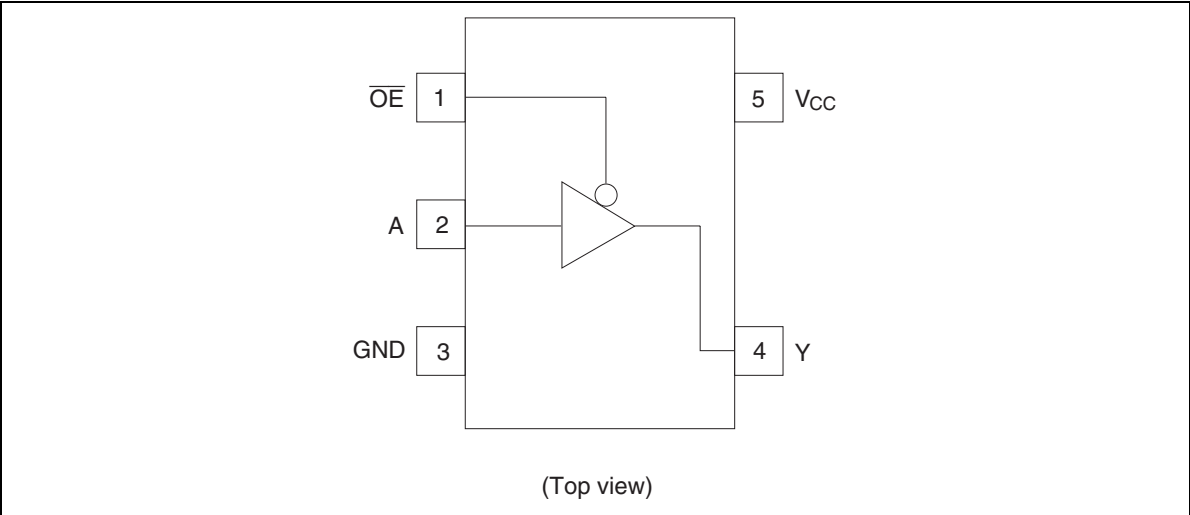
□ = Control code

Function Table

Inputs		Output Y
$\overline{OE}$	A	
L	H	H
L	L	L
H	X	Z

H : High level
 L : Low level
 X : Immaterial
 Z : High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	-0.5 to 7.0	V	
Input voltage range ^{*1}	V_I	-0.5 to 7.0	V	
Output voltage range ^{*1,2}	V_O	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
		-0.5 to 7.0		V_{CC} : OFF or Output : Z
Input clamp current	I_{IK}	-20	mA	$V_I < 0$
Output clamp current	I_{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±50	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air) ^{*3}	P_T	200	mW	
Storage temperature	T_{stg}	-65 to 150	°C	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
		0	5.5		Output : Z
Output current	I_{OL}	—	1	mA	$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		—	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		—	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		—	12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
	I_{OH}	—	−1		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		—	−2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		—	−6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		—	−12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Input transition rise or fall rate	$\Delta t / \Delta v$	0	300	ns / V	$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		0	200		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		0	100		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	T_a	−40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

- $T_a = -40$ to 85°C

Item	Symbol	V_{CC} (V) *	Min	Typ	Max	Unit	Test condition
Input voltage	V_{IH}	1.65 to 1.95	$V_{CC} \times 0.75$	—	—	V	
		2.3 to 2.7	$V_{CC} \times 0.7$	—	—		
		3.0 to 3.6	$V_{CC} \times 0.7$	—	—		
		4.5 to 5.5	$V_{CC} \times 0.7$	—	—		
	V_{IL}	1.65 to 1.95	—	—	$V_{CC} \times 0.25$		
		2.3 to 2.7	—	—	$V_{CC} \times 0.3$		
		3.0 to 3.6	—	—	$V_{CC} \times 0.3$		
		4.5 to 5.5	—	—	$V_{CC} \times 0.3$		
Hysteresis voltage	V_H	1.8	—	0.25	—	V	$V_T^+ - V_T^-$
		2.5	—	0.30	—		
		3.3	—	0.35	—		
		5.0	—	0.45	—		
Output voltage	V_{OH}	Min to Max	$V_{CC} - 0.1$	—	—	V	$I_{OH} = -50 \mu\text{A}$
		1.65	1.4	—	—		$I_{OH} = -1 \text{ mA}$
		2.3	2.0	—	—		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	—	—		$I_{OH} = -6 \text{ mA}$
		4.5	3.8	—	—		$I_{OH} = -12 \text{ mA}$
	V_{OL}	Min to Max	—	—	0.1		$I_{OL} = 50 \mu\text{A}$
		1.65	—	—	0.3		$I_{OL} = 1 \text{ mA}$
		2.3	—	—	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	—	—	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	—	—	0.55		$I_{OL} = 12 \text{ mA}$
Input current	I_{IN}	0 to 5.5	—	—	± 1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Off state output current	I_{OZ}	Min to Max	—	—	± 5	μA	$V_O = 5.5 \text{ V or GND}$
Quiescent supply current	I_{CC}	5.5	—	—	10	μA	$V_{IN} = V_{CC} \text{ or GND, } I_O = 0$
Output leakage current	I_{OFF}	0	—	—	5	μA	$V_{IN} \text{ or } V_O = 0 \text{ to } 5.5 \text{ V}$
Input capacitance	C_{IN}	3.3	—	3.0	—	pF	$V_{IN} = V_{CC} \text{ or GND}$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

- $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	13.5	23.5	1.0	26.0	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	19.0	33.0	1.0	36.0		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	13.7	26.5	1.0	29.0	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{ZL}	—	20.5	36.0	1.0	38.0		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	8.3	20.0	1.0	22.5	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{LZ}	—	13.0	29.5	1.0	32.0		$C_L = 50 \text{ pF}$		

- $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	6.8	13.0	1.0	15.5	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	8.7	16.5	1.0	18.5		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	7.0	13.0	1.0	15.5	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{ZL}	—	8.8	16.5	1.0	18.5		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	5.1	14.7	1.0	17.0	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{LZ}	—	7.3	18.2	1.0	20.5		$C_L = 50 \text{ pF}$		

- $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	4.8	8.0	1.0	9.5	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	6.1	11.5	1.0	13.0		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	4.8	8.0	1.0	9.5	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{ZL}	—	6.2	11.5	1.0	13.0		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	4.1	9.7	1.0	11.5	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{LZ}	—	5.5	13.2	1.0	15.0		$C_L = 50 \text{ pF}$		

Switching Characteristics (cont)

- $V_{CC} = 5.0 \pm 0.5 \text{ V}$

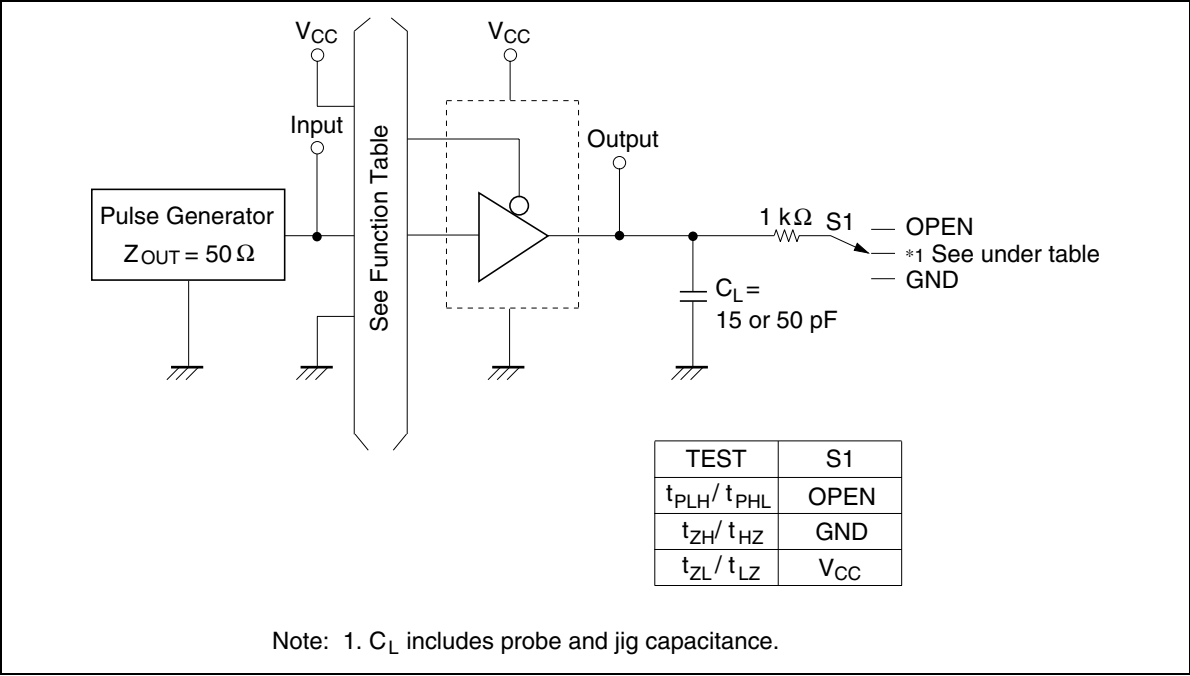
Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	3.4	5.5	1.0	6.5	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	4.3	7.5	1.0	8.5		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	3.4	5.1	1.0	6.0	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{ZL}	—	4.4	7.1	1.0	8.0		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	3.2	6.8	1.0	8.0	ns	$C_L = 15 \text{ pF}$	$\overline{OE}$	Y
	t_{LZ}	—	4.0	8.8	1.0	10.0		$C_L = 50 \text{ pF}$		

Operating Characteristics

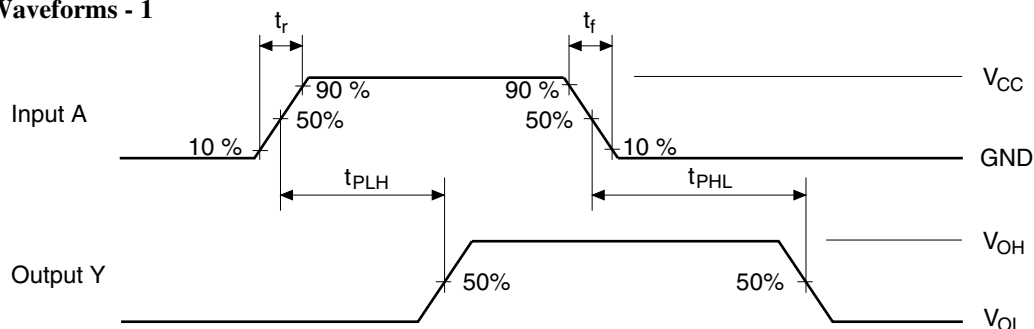
- $C_L = 50 \text{ pF}$

Item	Symbol	$V_{CC} \text{ (V)}$	Ta = 25°C			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation	C_{PD}	3.3	—	10.5	—	pF	$f = 10 \text{ MHz}$
capacitance		5.0	—	11.5	—		

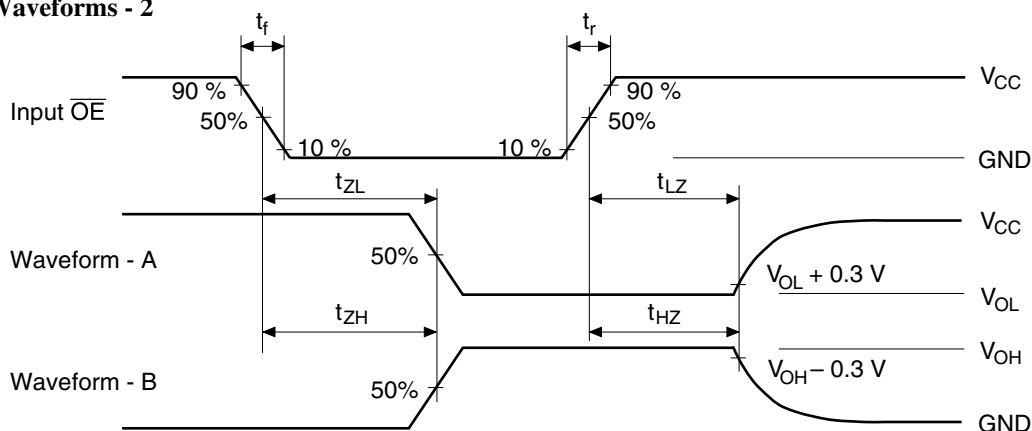
Test Circuit



• Waveforms - 1



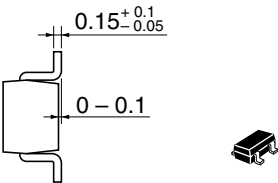
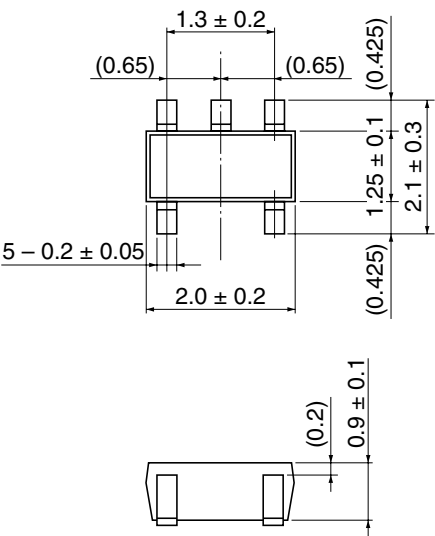
• Waveforms - 2



- Notes:
1. Input waveform : $PRR \leq 1 \text{ MHz}$, $Z_o = 50 \Omega$, $t_r \leq 3 \text{ ns}$, $t_f \leq 3 \text{ ns}$.
 2. Waveform - A is for an output with internal conditions such that the output is low except when disabled by the output control.
 3. Waveform - B is for an output with internal conditions such that the output is high except when disabled by the output control.
 4. The output are measured one at a time with one transition per measurement.

Package Dimensions

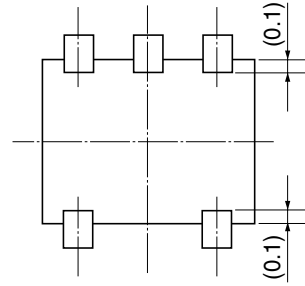
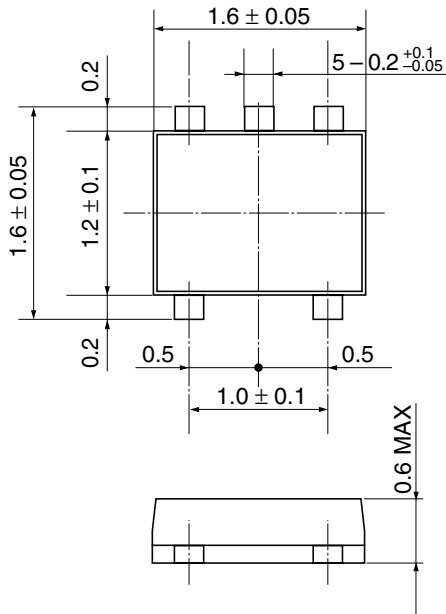
Unit: mm



*Sn-Bi plating

Hitachi Code	CMPAK-5V
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.006 g

Unit: mm



*Sn-Bi plating

Hitachi Code	TNP-5DV
JEDEC	—
JEITA	—
Mass (reference value)	0.002 g

Disclaimer

1. Hitachi neither warrants nor grants licenses of any rights of Hitachi's or any third party's patent, copyright, trademark, or other intellectual property rights for information contained in this document. Hitachi bears no responsibility for problems that may arise with third party's rights, including intellectual property rights, in connection with use of the information contained in this document.
2. Products and product specifications may be subject to change without notice. Confirm that you have received the latest product standards or specifications before final design, purchase or use.
3. Hitachi makes every attempt to ensure that its products are of high quality and reliability. However, contact Hitachi's sales office before using the product in an application that demands especially high quality and reliability or where its failure or malfunction may directly threaten human life or cause risk of bodily injury, such as aerospace, aeronautics, nuclear power, combustion control, transportation, traffic, safety equipment or medical equipment for life support.
4. Design your application so that the product is used within the ranges guaranteed by Hitachi particularly for maximum rating, operating supply voltage range, heat radiation characteristics, installation conditions and other characteristics. Hitachi bears no responsibility for failure or damage when used beyond the guaranteed ranges. Even within the guaranteed ranges, consider normally foreseeable failure rates or failure modes in semiconductor devices and employ systemic measures such as fail-safes, so that the equipment incorporating Hitachi product does not cause bodily injury, fire or other consequential damage due to operation of the Hitachi product.
5. This product is not designed to be radiation resistant.
6. No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without written approval from Hitachi.
7. Contact Hitachi's sales office for any questions regarding this document or Hitachi semiconductor products.

Sales Offices

HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits
 Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
 Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL <http://www.hitachisemiconductor.com/>

For further information write to:

Hitachi Semiconductor (America) Inc.
 179 East Tasman Drive
 San Jose, CA 95134
 Tel: <1> (408) 433-1990
 Fax: <1> (408) 433-0223

Hitachi Europe Ltd.
 Electronic Components Group
 Whitebrook Park
 Lower Cookham Road
 Maidenhead
 Berkshire SL6 8YA, United Kingdom
 Tel: <44> (1628) 585000
 Fax: <44> (1628) 778322

Hitachi Europe GmbH
 Electronic Components Group
 Dornacher Str 3
 D-85622 Feldkirchen
 Postfach 201, D-85619 Feldkirchen
 Germany
 Tel: <49> (89) 9 9180-0
 Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd.
 Hitachi Tower
 16 Collyer Quay #20-00
 Singapore 049318
 Tel: <65>-6538-6533/6538-8577
 Fax: <65>-6538-6933/6538-3877
 URL: <http://semiconductor.hitachi.com.sg>

Hitachi Asia Ltd.
 (Taipei Branch Office)
 4/F, No. 167, Tun Hwa North Road
 Hung-Kuo Building
 Taipei (105), Taiwan
 Tel: <886>-(2)-2718-3666
 Fax: <886>-(2)-2718-8180
 Telex: 23222 HAS-TP
 URL: <http://semiconductor.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
 Group III (Electronic Components)
 7/F., North Tower
 World Finance Centre,
 Harbour City, Canton Road
 Tsim Sha Tsui, Kowloon Hong Kong
 Tel: <852>-2735-9218
 Fax: <852>-2730-0281
 URL: <http://semiconductor.hitachi.com.hk>

Copyright © Hitachi, Ltd., 2003. All rights reserved. Printed in Japan.

Colophon 7.0