

To all our customers

Regarding the change of names mentioned in the document, such as Hitachi Electric and Hitachi XX, to Renesas Technology Corp.

The semiconductor operations of Mitsubishi Electric and Hitachi were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Hitachi, Hitachi, Ltd., Hitachi Semiconductors, and other Hitachi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Renesas Technology Home Page: <http://www.renesas.com>

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
2. Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein.

The information described here may contain technical inaccuracies or typographical errors.

Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (<http://www.renesas.com>).

4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.

HD29051

Dual Differential Line Drivers/Receivers With 3 State Outputs



ADE-205-035A (Z)
2nd. Edition
Mar. 1993

Description

The HD29051 features differential line drivers/receivers with three state output designed to meet the spec of EIA RS-422A and 423A. Each device has two drivers/receivers in a 16 pin package.

The device becomes in enable state when active high for a driver and active low for a receiver.

Features

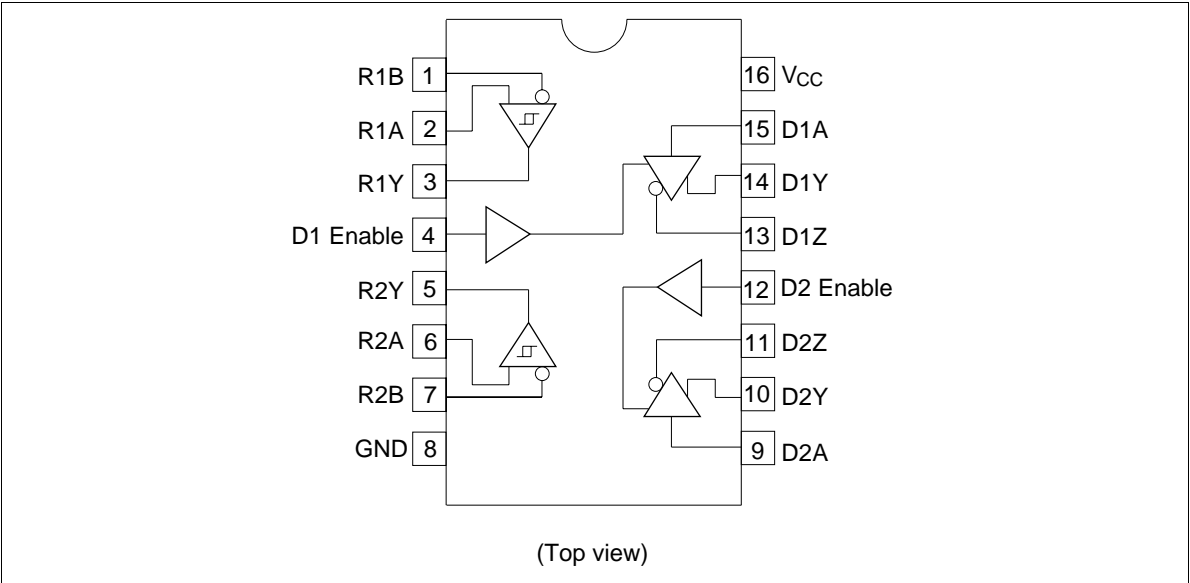
Driver

- Built in current restriction when short circuit
- Power up/down protection.
- High output current $I_{OH} = -40 \text{ mA}$
 $I_{OL} = 40 \text{ mA}$

Receiver

- Input hysteresis (Typ. 50 mV)
- In phase input voltage $\pm 200 \text{ mV}$ of input sensitivity in the range -7 to $+12 \text{ V}$.

Pin Arrangement



Function Table

Drivers				Receivers	
Input A	Enable	Output Y	Output Z	Differential Input A – B	Output Y
L	H	L	H	$V_{ID} \geq 0.2 \text{ V}$	H
H	H	H	L	$-0.2 \text{ V} < V_{ID} < 0.2 \text{ V}$?
X	L	Z	Z	$V_{ID} \leq -0.2 \text{ V}$	L

H : High level
L : Low level
Z : High impedance
X : Immaterial
? : Irrelevant

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply Voltage* ¹	V_{CC}	7	V
Input Voltage A, B* ³	V_{IN}	± 25	V
Differential Input Voltage* ^{2*3}	V_{ID}	± 25	V
Output Current* ³	I_O	50	mA
Enable Input Voltage	V_{IE}	5.5	V
Input Voltage* ⁴	V_{IN}	5.5	V
Output Applied Voltage* ^{4*5}	V_O	-1.0 to 7.0	V
Operating Temperature Range	Topr	0 to 70	°C
Storage Temperature Range	Tstg	-65 to 150	°C

Notes: 1. All voltage values except for differential input voltage are with respect to network ground terminal.

2. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input.

3. Only receiver

4. Only driver

5. Z state

6. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	V_{CC}	4.75	5.0	5.25	V
In Phase Input Voltage* ¹	V_{IC}	-7.0	—	12	V
Differential Input Voltage* ¹	V_{ID}	-6.0	—	6.0	V
Enable Input Voltage	V_{IE}	0	—	5.25	V
Input Voltage* ²	V_{IN}	0	—	5.25	V
Operating Temperature	Topr	0	25	70	°C

Notes: 1. Only receiver

2. Only driver

Electrical Characteristics (Ta = 0 to +70°C)

Driver

Item	Symbol	Min	Typ	Max	Unit	Conditions
Input Voltage	V_{IHD}	2.0	—	—	V	
	V_{ILD}	—	—	0.8	V	
Input Clamp Voltage	V_{IKD}	—	—	-1.5	V	$V_{CC} = 4.75\text{ V}$, $I_I = -18\text{ mA}$
Output Voltage	V_{OHD}	2.5	—	—	V	$V_{CC} = 4.75\text{ V}$, $I_{OH} = -20\text{ mA}$
		2.4	—	—	V	$V_{CC} = 4.75\text{ V}$, $I_{OH} = -40\text{ mA}$
	V_{OLD}	—	—	0.45	V	$V_{CC} = 4.75\text{ V}$, $I_{OL} = 20\text{ mA}$
		—	—	0.5	V	$V_{CC} = 4.75\text{ V}$, $I_{OL} = 40\text{ mA}$
Output Leak Current	I_{OZD}	-100	—	100	μA	$V_{CC} = 5.25\text{ V}$, $V_O = 0.5\text{ V}$ Enable = 0.8 V
		-100	—	100	μA	$V_{CC} = 5.25\text{ V}$, $V_O = 2.7\text{ V}$ Enable = 0.8 V
	$I_{O(Off)}$	—	—	-100	μA	$V_{CC} = 0\text{ V}$, $V_O = -0.25\text{ V}$
		—	—	100	μA	$V_{CC} = 0\text{ V}$, $V_O = 6.0\text{ V}$
Input Current	I_{ID}	—	—	100	μA	$V_{CC} = 5.25\text{ V}$, $V_I = 5.25\text{ V}$
	I_{IHD}	—	—	20	μA	$V_{CC} = 5.25\text{ V}$, $V_I = 2.7\text{ V}$
	I_{IHD}	—	—	-360	μA	$V_{CC} = 5.25\text{ V}$, $V_I = 0.4\text{ V}$
Differential Output Voltage	$\Delta V_{OC} $	—	—	0.4	V	
	$ V_{OD2} $	2.0	—	—	V	
	$\Delta V_{OD} $	—	—	0.4	V	
Short Circuit Output Current*1	I_{OSD}	-30	—	-150	mA	$V_{CC} = 5.25\text{ V}$, $V_O = 0\text{ V}$

Electrical Characteristics (Ta = 0 to +70°C)

Receiver

Item	Symbol	Min	Typ	Max	Unit	Conditions
Differential Input Threshold Voltage*2	V_{THR}	—	—	0.2	V	$V_O \geq 2.7\text{ V}$, $-7.0\text{ V} < V_{IC} < 12\text{ V}$
		-0.2	—	—	V	$V_O \leq 0.45\text{ V}$, $-7.0\text{ V} < V_{IC} < 12\text{ V}$
Input Current	I_{IBR}	—	—	1.0	mA	$V_{IN} = 12\text{ V}$, $0\text{ V} \leq V_{CC} \leq 5.25\text{ V}$
		—	—	-0.8	mA	$V_{IN} = -7\text{ V}$, $0\text{ V} \leq V_{CC} \leq 5.25\text{ V}$
Output Voltage	V_{OHR}	2.7	—	—	V	$V_{CC} = 4.75\text{ V}$, $I_O = -400\text{ mA}$ $V_{ID} = 0.4\text{ V}$, $-7.0\text{ V} < V_{IC} < 12\text{ V}$
	V_{OLR}	—	—	0.45	V	$V_{CC} = 4.75\text{ V}$, $I_O = 8.0\text{ mA}$ $V_{ID} = -0.4\text{ V}$, $-7.0\text{ V} < V_{IC} < 12\text{ V}$
Short Circuit Output Current*1	I_{OSR}	-15	—	-85	mA	$V_{CC} = 5.25\text{ V}$, $V_O = 0\text{ V}$, $V_{ID} = 3.0\text{ V}$

Supply

Item	Symbol	Min	Typ	Max	Unit	Conditions
Supply Current	I_{CC}	—	55*3	80	mA	$V_{CC} = 5.25\text{ V}$

Notes: 1. Not more than one output should be shorted at a time, and duration of the short circuit should not exceed one second.

2. In this table, only the threshold voltage is expressed in algebra.

3. All typical values are at $V_{CC} = 5\text{ V}$, $T_a = 25^\circ\text{C}$.

Switching Characteristics ($T_a = 25^\circ\text{C}$, $V_{CC} = 5\text{ V}$)**Driver**

Item	Symbol	Min	Typ	Max	Unit	Conditions
Propagation Delay Time	t_{PLHD}	—	—	20	ns	$C_L = 30\text{ pF}$, $R_L = 75\ \Omega$ to GND $R_L = 180\ \Omega$ to V_{CC}
	t_{PHLD}	—	—	20	ns	$C_L = 30\text{ pF}$, $R_L = 75\ \Omega$ to GND $R_L = 180\ \Omega$ to V_{CC}
Propagation Delay Time Difference	t_{SKD}^{*1}	—	—	4	ns	$C_L = 30\text{ pF}$, $R_L = 75\ \Omega$ to GND $R_L = 180\ \Omega$ to V_{CC}
Output Enable Time	t_{ZHD}	—	—	20	ns	$C_L = 30\text{ pF}$, $R_L = 75\ \Omega$ to GND
	t_{ZLD}	—	—	35	ns	$C_L = 30\text{ pF}$, $R_L = 180\ \Omega$ to V_{CC}
Output Disable Time	t_{HZD}	—	—	20	ns	$C_L = 10\text{ pF}$, $R_L = 75\ \Omega$ to GND
	t_{LZD}	—	—	25	ns	$C_L = 10\text{ pF}$, $R_L = 180\ \Omega$ to V_{CC}

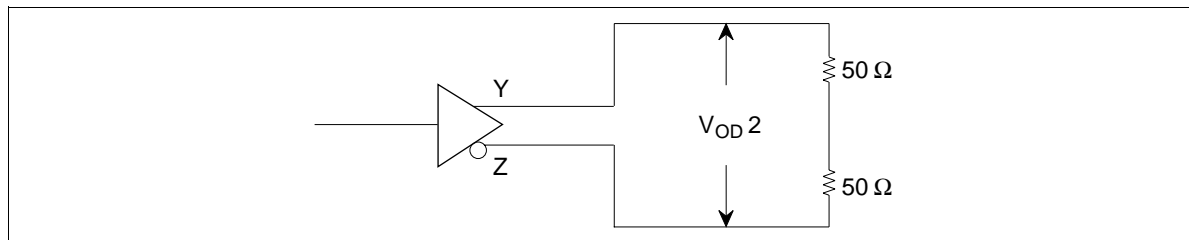
Receiver

Item	Symbol	Min	Typ	Max	Unit	Conditions
Propagation Delay Time	t_{PLHR}	—	—	40	ns	$C_L = 15\text{ pF}$
	t_{PHLR}	—	—	40	ns	$C_L = 15\text{ pF}$

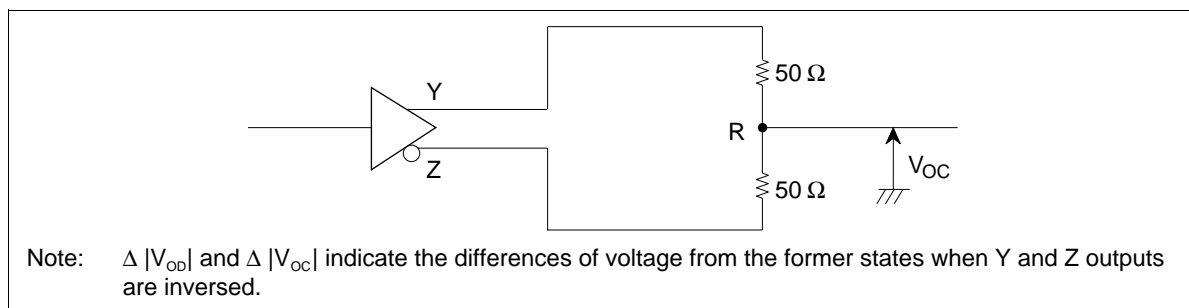
Note: 1. $t_{SKD} = |t_{PLHD} - t_{PHLD}|$

DC Test ($|V_{OD2}|$, $\Delta |V_{OD}|$, V_{OC} , $\Delta |V_{OC}|$)

$|V_{OD2}|$, $\Delta |V_{OD}|$ Test



V_{OC} , $\Delta |V_{OC}|$ Test

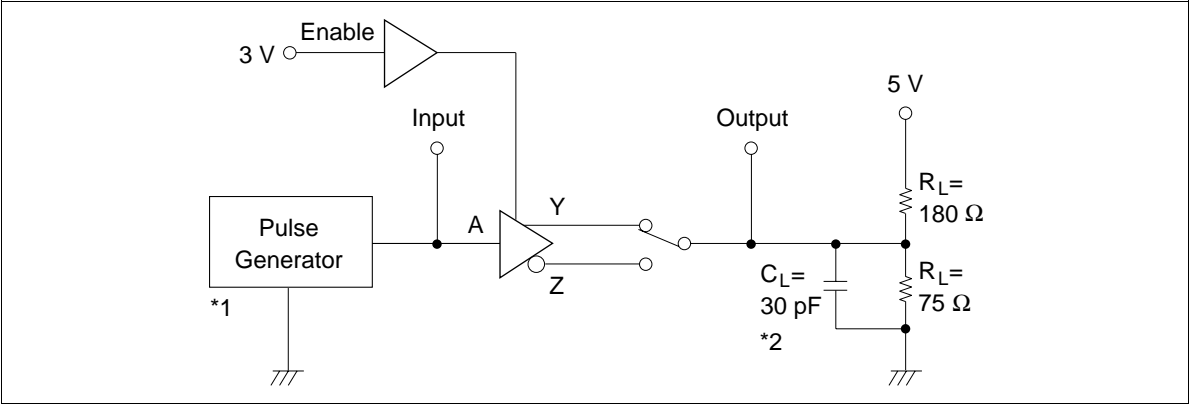


$$\Delta |V_{OD}| = ||V_{OD2}| - |V_{OD2}||$$

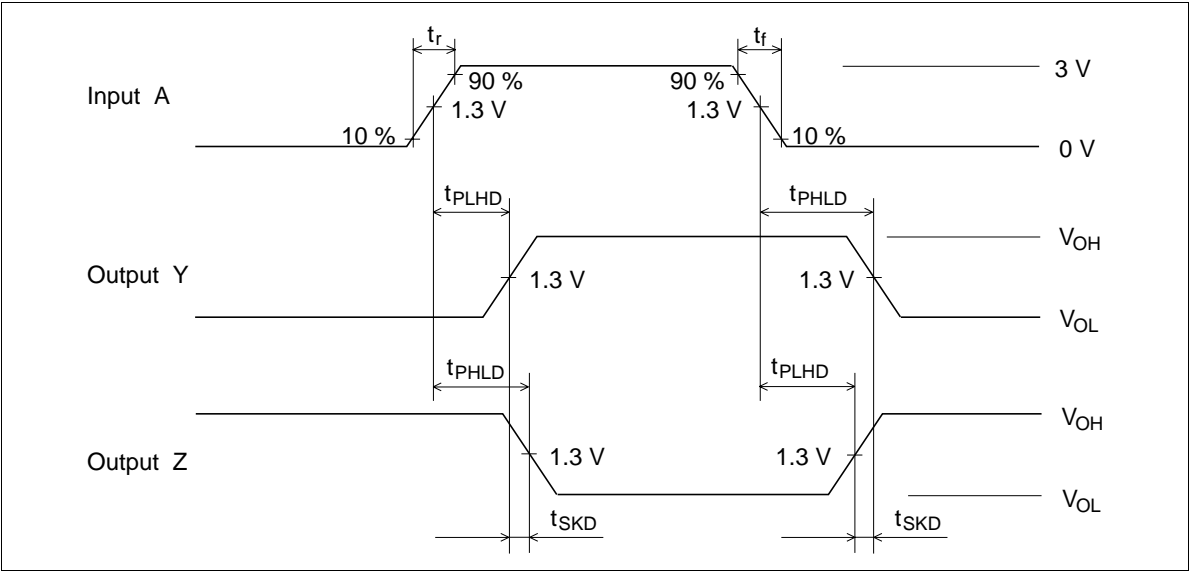
$$\Delta |V_{OC}| = |V_{OC} - V_{OC}|$$

1. t_{PLHD} , t_{PHLD}

Test circuit

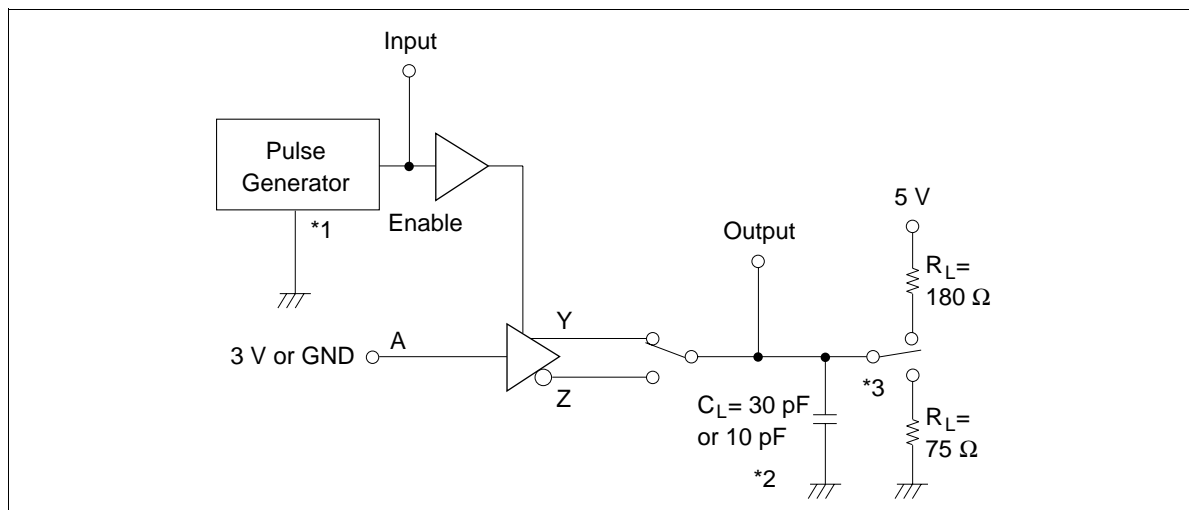


Waveforms

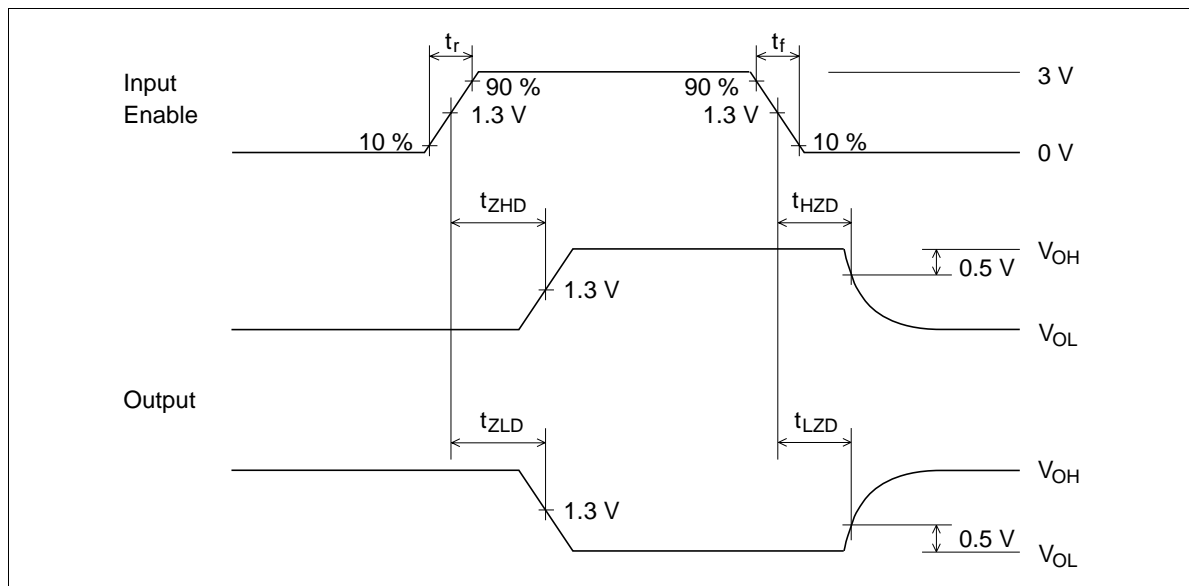


2. t_{ZHD} , t_{ZLD} , t_{HHD} , t_{LHD}

Test circuit

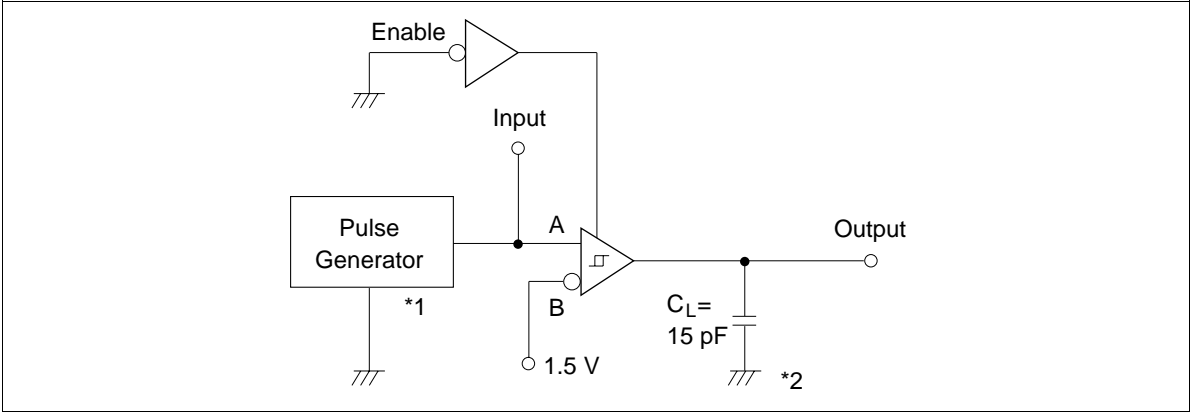


Waveforms

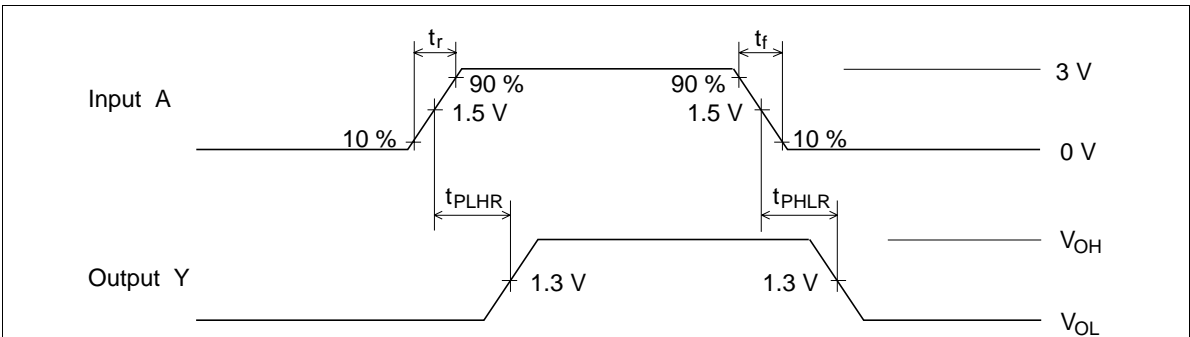


3. t_{PLHR} , t_{PHLR}

Test circuit



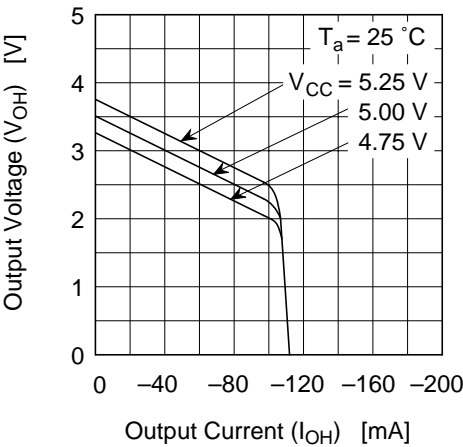
Waveforms



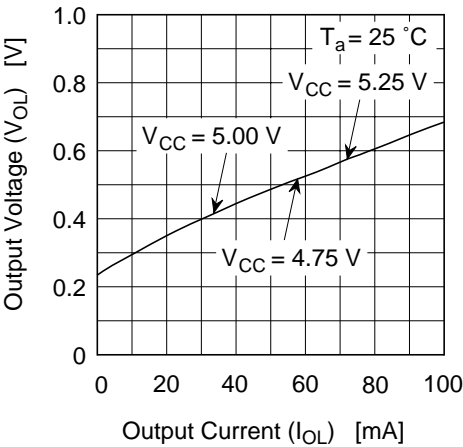
- Notes:
1. The pulse generator has the following characteristics:
 $PRR = 1 \text{ MHz}$, 50% duty cycle, $t_r = t_f = 6.0 \text{ ns}$.
 2. C_L includes probe and jig capacitance.
 3. 75Ω connected between the pin and GND at t_{ZHD} t_{HZD} test.
 180Ω connected between the pin and GND at t_{ZHD} t_{HZD} test.
 4. At t_{HZR} , t_{LZR} test, S_1 and S_2 are closed.
 At t_{ZHR} test, S_1 is open and S_2 is closed.
 At t_{ZLR} test, S_1 is closed and S_2 is open.

Main Characteristics

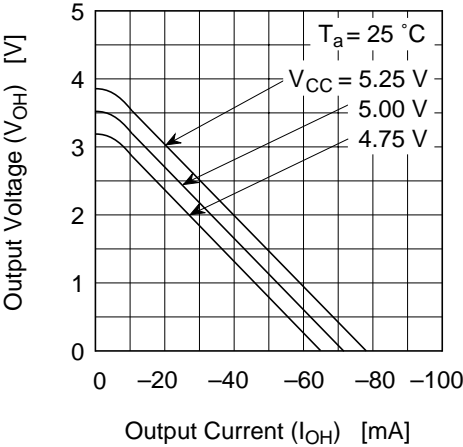
Output Characteristics (High level)
[Driver]



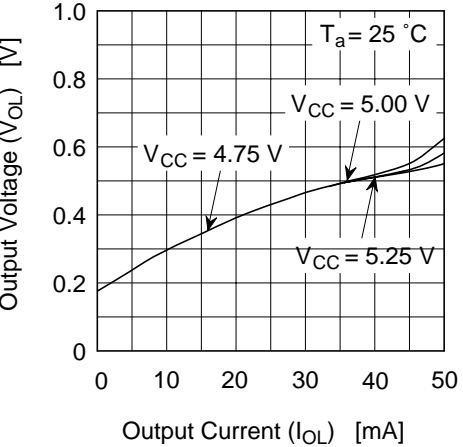
Output Characteristics (Low level)
[Driver]

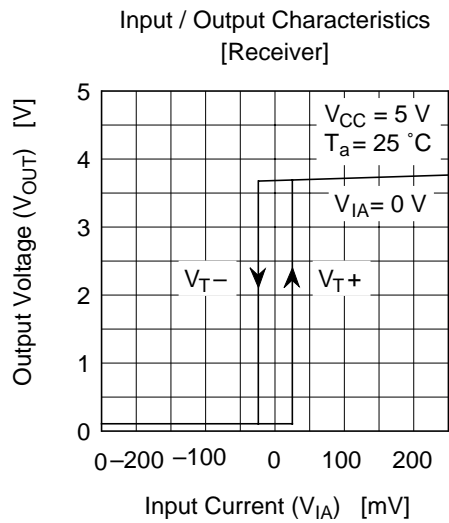


Output Characteristics (High level)
[Receiver]



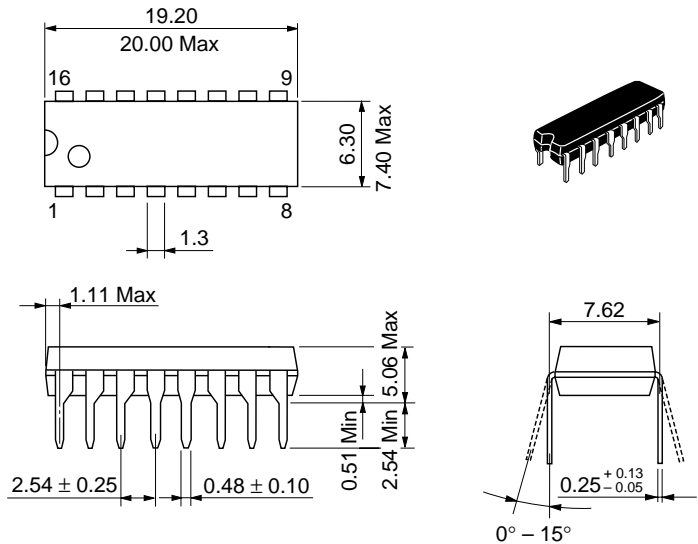
Output Characteristics (Low level)
[Driver]





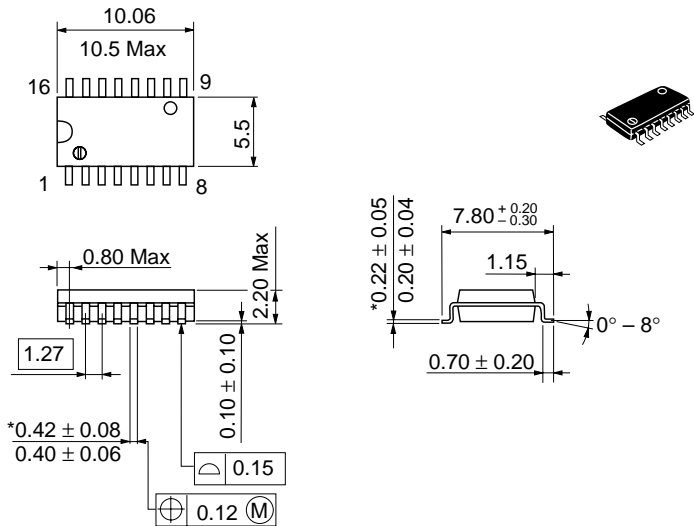
Package Dimensions

Unit: mm



Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	1.07 g

Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DA
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.24 g

Cautions

1. Hitachi neither warrants nor grants licenses of any rights of Hitachi's or any third party's patent, copyright, trademark, or other intellectual property rights for information contained in this document. Hitachi bears no responsibility for problems that may arise with third party's rights, including intellectual property rights, in connection with use of the information contained in this document.
2. Products and product specifications may be subject to change without notice. Confirm that you have received the latest product standards or specifications before final design, purchase or use.
3. Hitachi makes every attempt to ensure that its products are of high quality and reliability. However, contact Hitachi's sales office before using the product in an application that demands especially high quality and reliability or where its failure or malfunction may directly threaten human life or cause risk of bodily injury, such as aerospace, aeronautics, nuclear power, combustion control, transportation, traffic, safety equipment or medical equipment for life support.
4. Design your application so that the product is used within the ranges guaranteed by Hitachi particularly for maximum rating, operating supply voltage range, heat radiation characteristics, installation conditions and other characteristics. Hitachi bears no responsibility for failure or damage when used beyond the guaranteed ranges. Even within the guaranteed ranges, consider normally foreseeable failure rates or failure modes in semiconductor devices and employ systemic measures such as fail-safes, so that the equipment incorporating Hitachi product does not cause bodily injury, fire or other consequential damage due to operation of the Hitachi product.
5. This product is not designed to be radiation resistant.
6. No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without written approval from Hitachi.
7. Contact Hitachi's sales office for any questions regarding this document or Hitachi semiconductor products.

HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits.

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL	NorthAmerica	: http://semiconductor.hitachi.com/
	Europe	: http://www.hitachi-eu.com/hel/ecg
	Asia	: http://sicapac.hitachi-asia.com
	Japan	: http://www.hitachi.co.jp/Sicd/indx.htm

For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe GmbH
Electronic Components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 585160

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00,
Singapore 049318
Tel: <65>-538-6533/538-8577
Fax: <65>-538-6933/538-3877
URL: <http://www.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road,
Hung-Kuo Building,
Taipei (105), Taiwan
Tel: <886>-(2)-2718-3666
Fax: <886>-(2)-2718-8180
Telex: 23222 HAS-TP
URL: <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon,
Hong Kong
Tel: <852>-(2)-735-9218
Fax: <852>-(2)-730-0281
URL: <http://www.hitachi.com.hk>

Copyright © Hitachi, Ltd., 2000. All rights reserved. Printed in Japan.
Colophon 2.0