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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

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Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

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HVU363A

Variable Capacitance Diode for TV tuner

RENESAS

ADE-208-234B(Z)

Rev. 2
Nov. 1998

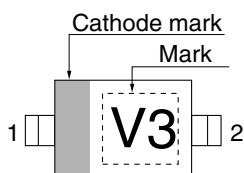
Features

- High capacitance ratio.(n=15.0Typ)
- Low series resistance ($r_s=0.75\Omega_{\text{max}}$) and good C-V linearity.
- Ultra small Resin Package (URP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVU363A	V3	URP

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}^{*1}	35	V
Reverse voltage	V_R	32	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes 1. RL=10kΩ

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V_R	32	—	—	V	$I_R = 1\mu A$
Reverse current	I_{R1}	—	—	10	nA	$V_R = 30V$
	I_{R2}	—	—	100		$V_R = 30V, Ta = 60^\circ C$
Capacitance	C_1	34.65	—	42.35	pF	$V_R = 1V, f = 1 MHz$
	C_{28}	2.361	—	2.754		$V_R = 28V, f = 1 MHz$
Capacitance ratio	n	13.5	15.0	—	—	C_1 / C_{28}
Series resistance	r_s	—	—	0.75	Ω	$C=14pF, f = 470 MHz$
Matching error	$\Delta C/C^{*1}$	—	—	2.0	%	$V_R = 1 \text{ to } 28V, f = 1 MHz$
Linealty factor ^{*2}	—	—	-1.2	—	—	$\Delta \log C / \Delta \log V$

Notes 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of $\Delta C/C$ continuous in a reel , expect extention to another group.
Calculate Matching Error,

(Cmax-Cmin)

$$\Delta C/C = \frac{\text{Cmax-Cmin}}{\text{Cmin}} \times 100 (\%)$$

Notes 2. Calculate LF ($\Delta \log C / \Delta \log V$) at VR = 1 through 28V , f = 1 MHz .(Reference Value)

Main Characteristic

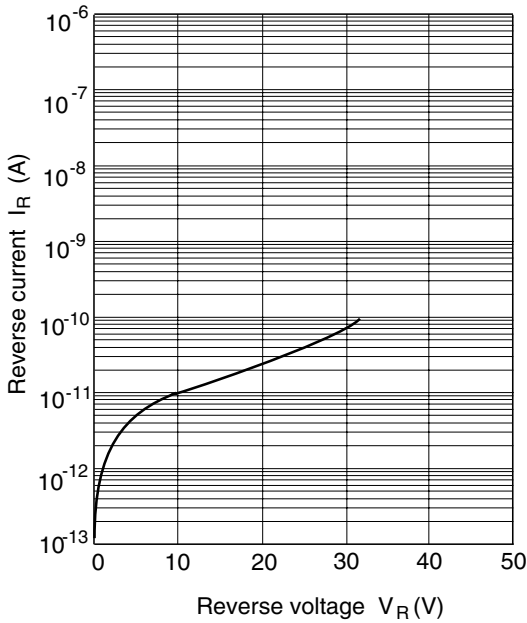


Fig.1 Reverse current Vs. Reverse voltage

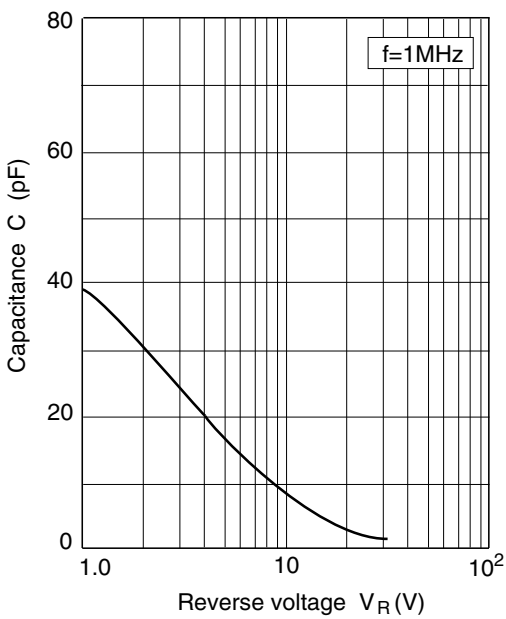


Fig.2 Capacitance Vs. Reverse voltage

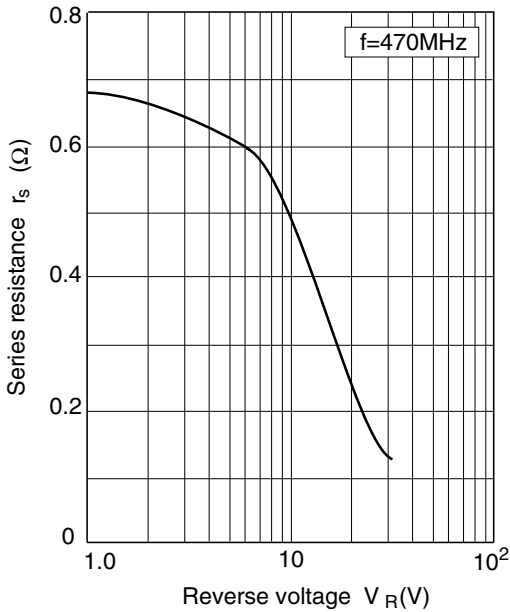


Fig.3 Series resistance Vs. Reverse voltage

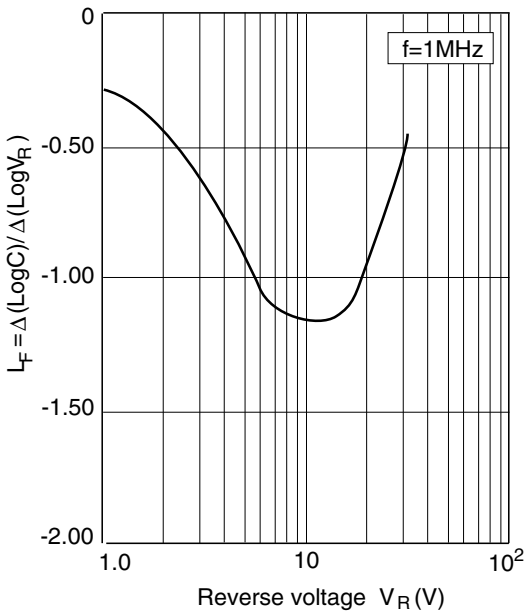
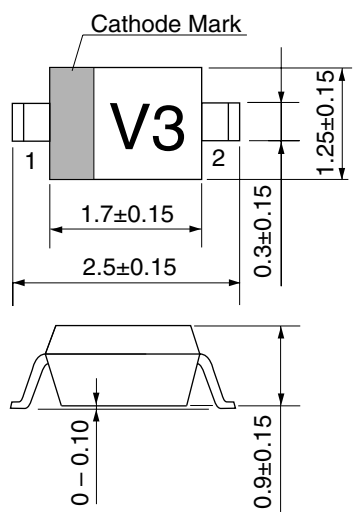


Fig.4 Linearity factor Vs. Reverse voltage

Package Dimensions

Unit : mm



- 1. Cathode
- 2. Anode

Hitachi Code	URP
JEDECCode	—
EIAJCode	—
Weight(g)	0.004

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Sales Offices

HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL	NorthAmerica	: http://semiconductor.hitachi.com/
	Europe	: http://www.hitachi-eu.com/hel/ecg
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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1> (408) 433-1990 Fax: <1> (408) 433-0223	Hitachi Europe Ltd. Electronic Components Group Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 585200
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Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00
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Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00 Singapore 049318 Tel: <65>-538-6533/538-8577 Fax: <65>-538-6933/538-3877 URL: http://www.hitachi.com.sg
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Hitachi Asia Ltd. (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP URL: http://www.hitachi.com.tw

Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: <852>-(2)-735-9218 Fax: <852>-(2)-730-0281 URL: http://semiconductor.hitachi.com.hk
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