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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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HVD365

Variable Capacitance Diode for VCO



ADE-208-951A (Z)

Rev.1 Mar. 2002

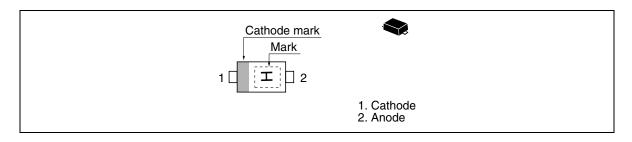
Features

- High capacitance ratio and good C-V linearity.
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVD365	Н	SFP

Pin Arrangement



HVD365

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	$V_{_{R}}$	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics

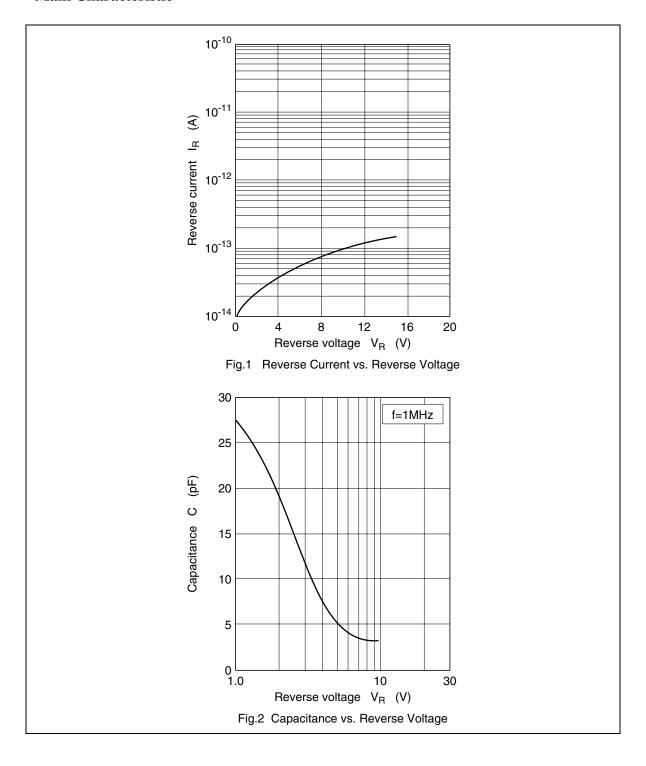
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	_	10	nA	V _R = 10 V
	I _{R2}	_	_	100	_	V _R = 10 V, Ta = 60°C
Capacitance	C ₁	27.05	_	28.55	pF	V _R = 1 V, f = 1 MHz
	C ₄	6.05	_	7.55	_	V _R = 4 V, f = 1 MHz
Capacitance ratio	n	3.0	_	_	_	$C_{_1}/C_{_4}$
Series resistance	r _s	_	_	1.5	Ω	V _R = 4 V, f = 100 MHz
ESD-Capability *1	_	200	_	_	V	$C=200~pF$, $R=0~\Omega$, Both forward and reverse direction 1 pulse.

Notes 1. Failure criterion; $I_R \ge 20 \text{ nA}$ at $V_R = 10 \text{ V}$

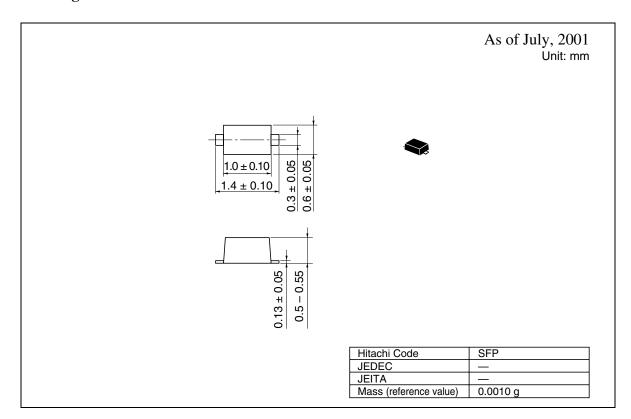
2. Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic



HVD365

Package Dimensions



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