
HD74LVC541

Octal Buffers / Line Drivers with 3-state Outputs

HITACHI

ADE-205-115(Z)

Rev.0

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Description

The HD74LVC541 has eight line drivers with three state outputs in a 20 pin package. When $\overline{G1}$ and $\overline{G2}$ is low level, this drivers set up output is enable. Low voltage and high speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 2.0\text{ V to }5.5\text{ V}$
- All inputs $V_{IH}(\text{Max.}) = 5.5\text{ V} (@V_{CC} = 0\text{ V to }5.5\text{ V})$
- Typical V_{OL} ground bounce $< 0.8\text{ V} (@V_{CC} = 3.3\text{ V}, T_a = 25^\circ\text{C})$
- Typical V_{OH} undershoot $> 2.0\text{ V} (@V_{CC} = 3.3\text{ V}, T_a = 25^\circ\text{C})$
- High output current $\pm 24\text{ mA} (@V_{CC} = 3.0\text{ V to }5.5\text{ V})$

Function Table

Inputs			Output Y
$\overline{G1}$	$\overline{G2}$	A	
L	L	L	L
L	L	H	H
H	X	X	Z
X	H	X	Z

H : High level

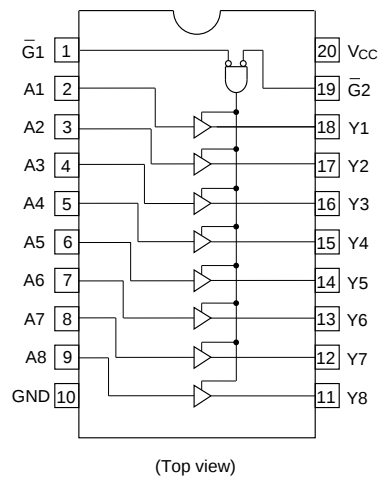
L : Low level

X : Immaterial

Z : High impedance

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Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	-0.5 to 6.0	V	
Input diode current	I_{IK}	-50	mA	$V_I = -0.5$ V
Input voltage	V_I	-0.5 to 6.0	V	
Output diode current	I_{OK}	-50	mA	$V_O = -0.5$ V
		50	mA	$V_O = V_{CC} + 0.5$ V
Output voltage	V_O	-0.5 to $V_{CC} + 0.5$	V	Output "H" or "L"
Output current	I_O	± 50	mA	
V_{CC} , GND current / pin	I_{CC} or I_{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	1.5 to 5.5	V	Data retention
		2.0 to 5.5	V	At operation
Input / output voltage	V_I	0 to 5.5	V	$\overline{G}1, \overline{G}2, A$
	V_O	0 to V_{CC}	V	Output "H" or "L"
Operating temperature	Ta	-40 to 85	°C	
Output current	I_{OH}	-12	mA	$V_{CC} = 2.7$ V
		-24 ²	mA	$V_{CC} = 3.0$ V to 5.5 V
	I_{OL}	12	mA	$V_{CC} = 2.7$ V
		24 ²	mA	$V_{CC} = 3.0$ V to 5.5 V
Input rise / fall time ^{*1}	t_r, t_f	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform : Refer to test circuit of switching characteristics.

2. duty cycle $\leq 50\%$

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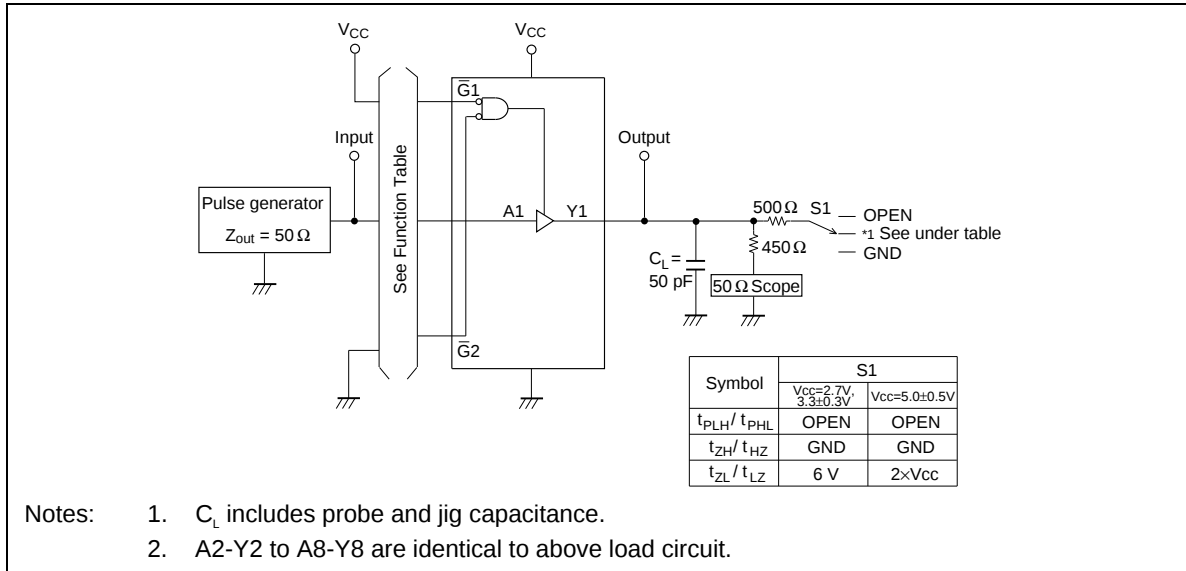
Electrical Characteristics

Item	Symbol	V _{CC} (V)	Ta = -40 to 85°C		Unit	Test Conditions
			Min	Max		
Input voltage	V _{IH}	2.7 to 3.6	2.0	—	V	
		4.5 to 5.5	V _{CC} ×0.7	—	V	
	V _{IL}	2.7 to 3.6	—	0.8	V	
		4.5 to 5.5	—	V _{CC} ×0.3	V	
Output voltage	V _{OH}	2.7 to 5.5	V _{CC} -0.2	—	V	I _{OH} = -100 μA
		2.7	2.2	—	V	I _{OH} = -12 mA
		3.0	2.4	—	V	
		3.0	2.0	—	V	I _{OH} = -24 mA
		4.5	3.8	—	V	
	V _{OL}	2.7 to 5.5	—	0.2	V	I _{OL} = 100 μA
		2.7	—	0.4	V	I _{OL} = 12 mA
		3.0	—	0.55	V	I _{OL} = 24 mA
		4.5	—	0.55	V	
Input current	I _{IN}	0 to 5.5	—	±5.0	μA	V _{IN} = 5.5 V or GND
Quiescent supply current	ΔI _{CC}	3.0 to 3.6	—	500	μA	V _{IN} = one input at (V _{CC} -0.6)V, other inputs at V _{CC} or GND
Off state output current	I _{OZ}	5.5	—	±10	μA	V _{IN} = V _{CC} , GND V _{OUT} = V _{CC} or GND
Quiescent supply current	I _{CC}	5.5	—	20	μA	V _{IN} = V _{CC} or GND

Switching Characteristics

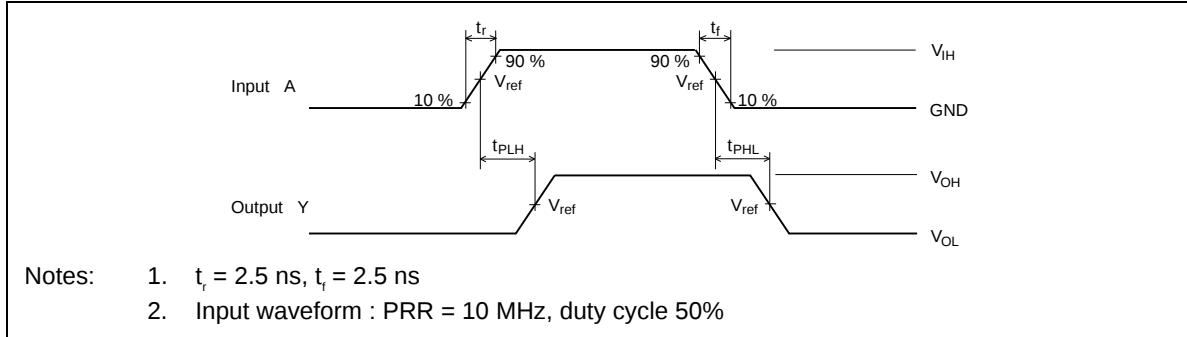
Item	Symbol	V_{CC} (V)	$T_a = -40 \text{ to } 85^\circ\text{C}$			Unit	From (Input)	To (Output)
			Min	Typ	Max			
Propagation delay time	t_{PLH}	2.7	—	6.0	8.0	ns	A	Y
	t_{PHL}	3.3 ± 0.3	1.5	4.5	7.0	ns		
		5.0 ± 0.5	—	3.5	6.0	ns		
Output enable time	t_{ZH}	2.7	—	8.5	11.0	ns	$\overline{G}1$ or $\overline{G}2$	Y
	t_{ZL}	3.3 ± 0.3	1.5	6.5	9.0	ns		
		5.0 ± 0.5	—	4.5	8.0	ns		
Output disable time	t_{HZ}	2.7	—	5.5	9.0	ns	$\overline{G}1$ or $\overline{G}2$	Y
	t_{LZ}	3.3 ± 0.3	1.5	4.5	8.0	ns		
		5.0 ± 0.5	—	3.5	6.5	ns		
Input capacitance	C_{IN}	2.7	—	3.0	—	pF		
Output capacitance	C_O	2.7	—	15.0	—	pF		

Test Circuit

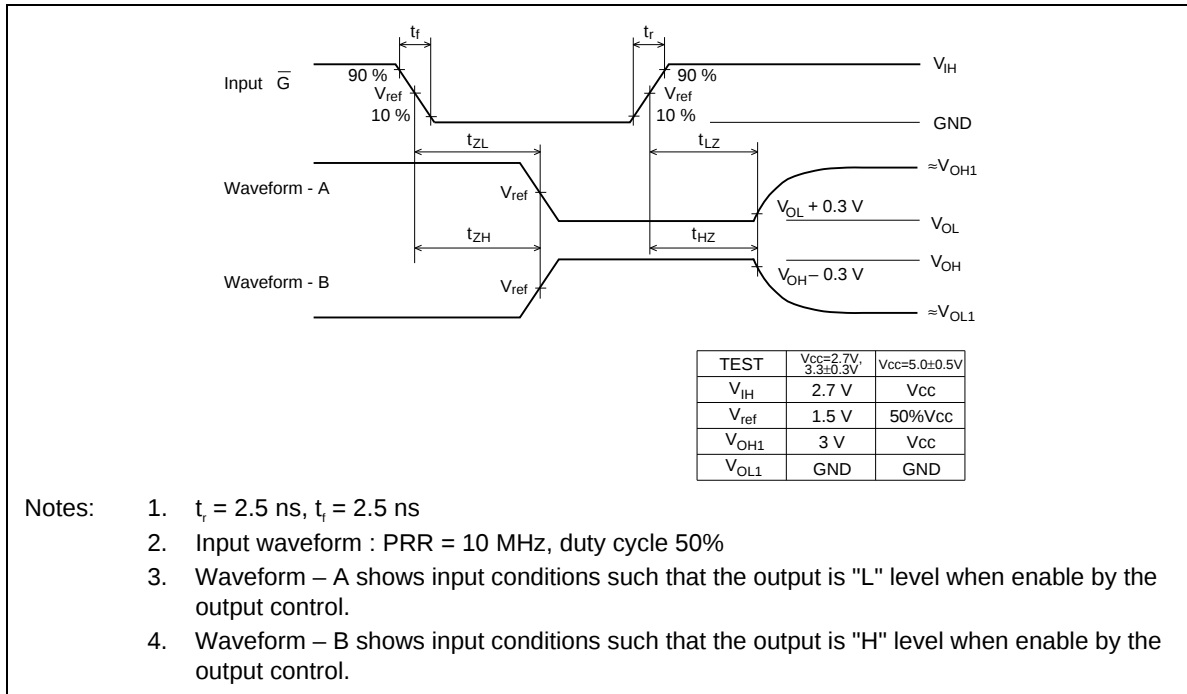


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Waveforms – 1



Waveforms – 2



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