
HD74ALVC162830

1-bit to 2-bit Address Driver with 3-state Outputs

HITACHI

ADE-205-199 (Z)

Preliminary

1st. Edition

November 1997

Description

This 1-bit to 2-bit address driver is designed for 2.3 V to 3.6 V V_{CC} operation. To ensure the high impedance state during power up or power down, OE should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current sinking capability of the driver. All outputs, which are designed to sink up to 12 mA, include equivalent 26 Ω resistors to reduce overshoot and undershoot.

Features

- $V_{CC} = 2.3 \text{ V to } 3.6 \text{ V}$
- Typical V_{OL} ground bounce $< 0.8 \text{ V}$ ($@V_{CC} = 3.3 \text{ V}$, $T_a = 25^\circ\text{C}$)
- Typical V_{OH} undershoot $> 2.0 \text{ V}$ ($@V_{CC} = 3.3 \text{ V}$, $T_a = 25^\circ\text{C}$)
- High output current $\pm 12 \text{ mA}$ ($@V_{CC} = 3.0 \text{ V}$)
- All outputs have equivalent 26 Ω series resistors, so no external resistors are required

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Function Table

Inputs			Outputs	
OE1	OE2	A	1Yn	2Yn
L	H	H	H	Z
L	H	L	L	Z
H	L	H	Z	H
H	L	L	Z	L
L	L	H	H	H
L	L	L	L	L
H	H	X	Z	Z

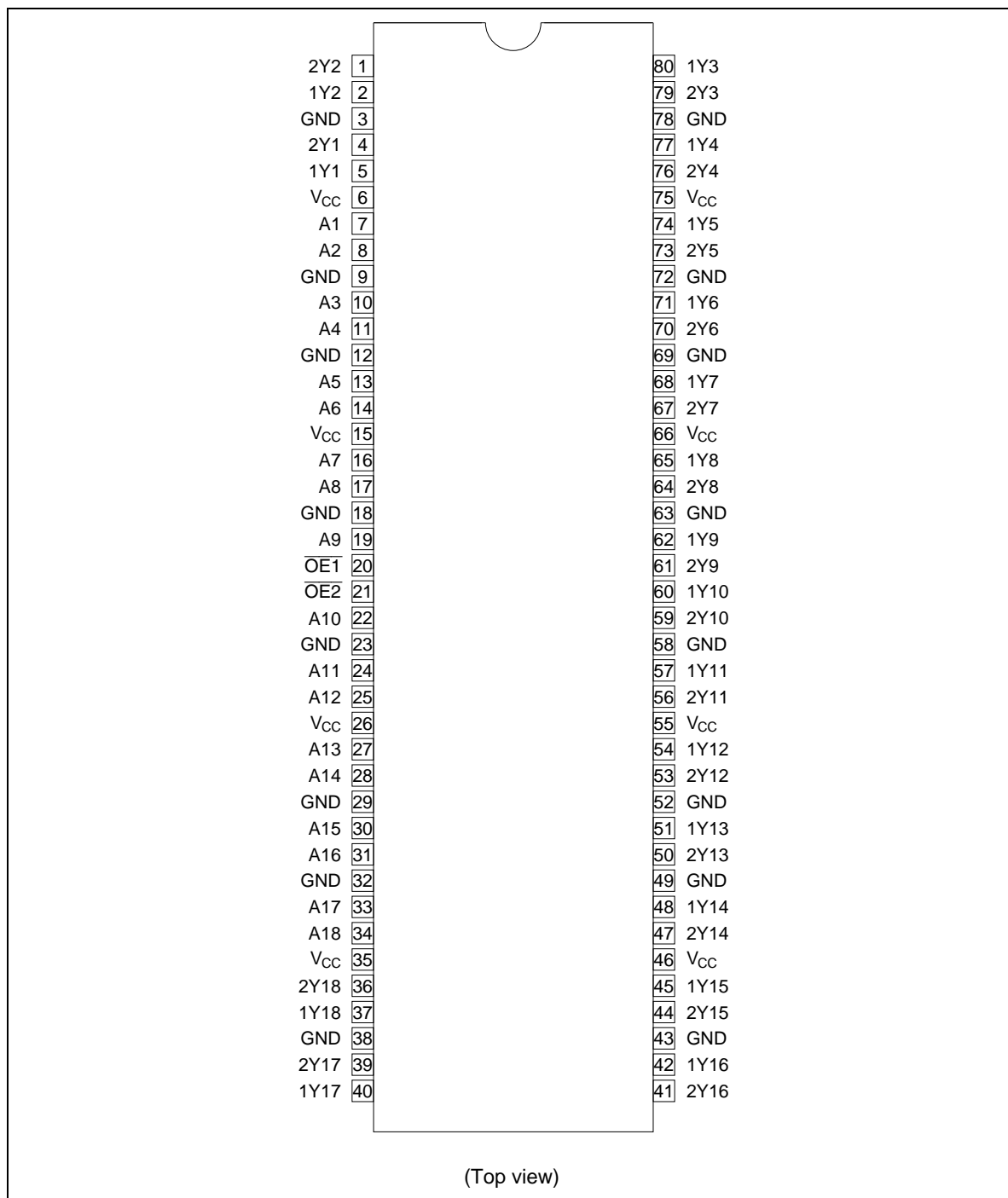
H : High level

L : Low level

X : Immaterial

Z : High impedance

Pin Arrangement



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Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	−0.5 to 4.6	V	
Input voltage ^{*1}	V_I	−0.5 to 4.6	V	
Output voltage ^{*1,2}	V_O	−0.5 to $V_{CC} + 0.5$	V	
Input clamp current	I_{IK}	−50	mA	$V_I < 0$
Output clamp current	I_{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	±50	mA	$V_O = 0$ to V_{CC}
V_{CC} , GND current / pin	I_{CC} or I_{GND}	±100	mA	
Maximum power dissipation at $T_a = 55^\circ\text{C}$ (in still air) ^{*3}	P_T	1	W	TVSOP
Storage temperature	T_{stg}	−65 to 150	°C	

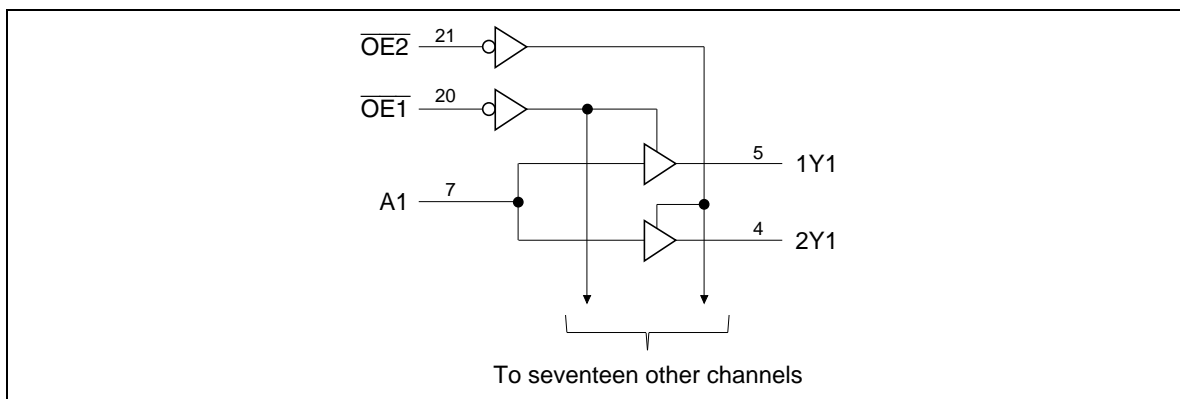
Notes: Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

1. The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.
2. This value is limited to 4.6 V maximum.
3. The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage	V_{CC}	2.3	3.6	V	
Input voltage	V_I	0	V_{CC}	V	
Output voltage	V_O	0	V_{CC}	V	
High level output current	I_{OH}	—	−6	mA	$V_{CC} = 2.3\text{ V}$
		—	−8		$V_{CC} = 2.7\text{ V}$
		—	−12		$V_{CC} = 3.0\text{ V}$
Low level output current	I_{OL}	—	6	mA	$V_{CC} = 2.3\text{ V}$
		—	8		$V_{CC} = 2.7\text{ V}$
		—	12		$V_{CC} = 3.0\text{ V}$
Input transition rise or fall rate	$\Delta E_t / \Delta E_v$	0	10	ns / V	
Operating temperature	T_a	−40	85	°C	

Note: Unused control inputs must be held high or low to prevent them from floating.

Logic Diagram

HD74ALVC162830

Electrical Characteristics (Ta = -40 to 85°C)

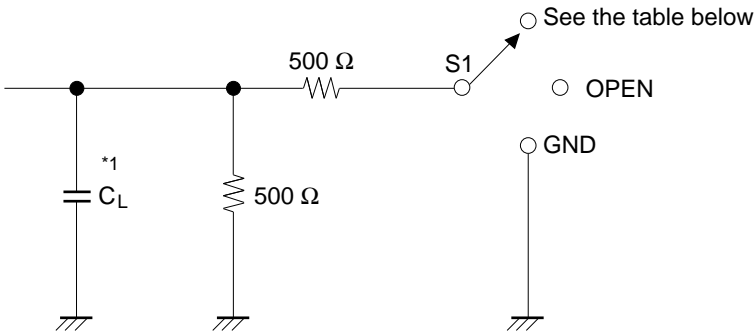
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions
Input voltage	V _{IH}	2.3 to 2.7	1.7	—	V	
		2.7 to 3.6	2.0	—		
	V _{IL}	2.3 to 2.7	—	0.7		
		2.7 to 3.6	—	0.8		
Output voltage	V _{OH}	2.3 to 3.6	V _{cc} -0.2	—	V	I _{OH} = -100 μA
		2.3	1.9	—		I _{OH} = -4 mA, V _{IH} = 1.7 V
		2.3	1.7	—		I _{OH} = -6 mA, V _{IH} = 1.7 V
		3.0	2.4	—		I _{OH} = -6 mA, V _{IH} = 2.0 V
		2.7	2.0	—		I _{OH} = -8 mA, V _{IH} = 2.0 V
		3.0	2.0	—		I _{OH} = -12 mA, V _{IH} = 2.0 V
	V _{OL}	2.3 to 3.6	—	0.2		I _{OL} = 100 μA
		2.3	—	0.4		I _{OL} = 4 mA, V _{IL} = 0.7 V
		2.3	—	0.55		I _{OL} = 6 mA, V _{IL} = 0.7 V
		3.0	—	0.55		I _{OL} = 6 mA, V _{IL} = 0.8 V
		2.7	—	0.6		I _{OL} = 8 mA, V _{IL} = 0.8 V
		3.0	—	0.8		I _{OL} = 12 mA, V _{IL} = 0.8 V
Input current	I _{IN}	3.6	—	±5	μA	V _{IN} = V _{cc} or GND
Off state output current	I _{OZ}	3.6	—	±10	μA	V _{OUT} = V _{cc} or GND
Quiescent supply current	I _{cc}	3.6	—	40	μA	V _{IN} = V _{cc} or GND
	ΔI _{cc}	3.0 to 3.6	—	750	μA	V _{IN} = one input at (V _{cc} -0.6) V, other inputs at V _{cc} or GND

Switching Characteristics (Ta = -40 to 85°C)

Item	Symbol	V _{cc} (V)	Min	Typ	Max	Unit	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH}	2.5±0.2	1.2	—	3.8	ns	A	Y
	t _{PHL}	2.7	—	—	4.0			
		3.3±0.3	1.7	—	3.5			
Output enable time	t _{ZH}	2.5±0.2	1.0	—	5.7	ns	OE	Y
	t _{ZL}	2.7	—	—	5.7			
		3.3±0.3	1.0	—	4.8			
Output disable time	t _{HZ}	2.5±0.2	1.5	—	6.2	ns	OE	Y
	t _{LZ}	2.7	—	—	5.4			
		3.3±0.3	1.7	—	5.2			
Input capacitance	C _{IN}	3.3	—	4.5	—	pF	Control inputs	
		3.3	—	5.0	—		Data inputs	
Output capacitance	C _O	3.3	—	7.5	—	pF		

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Test Circuit

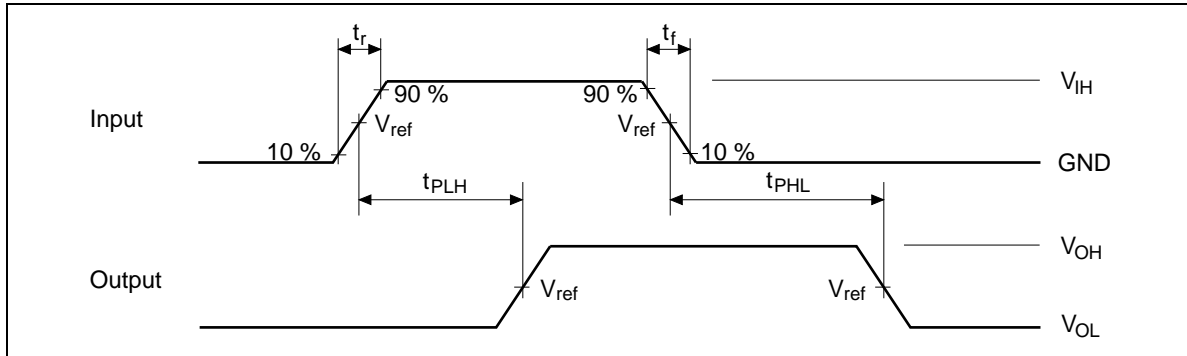


Load Circuit for Outputs

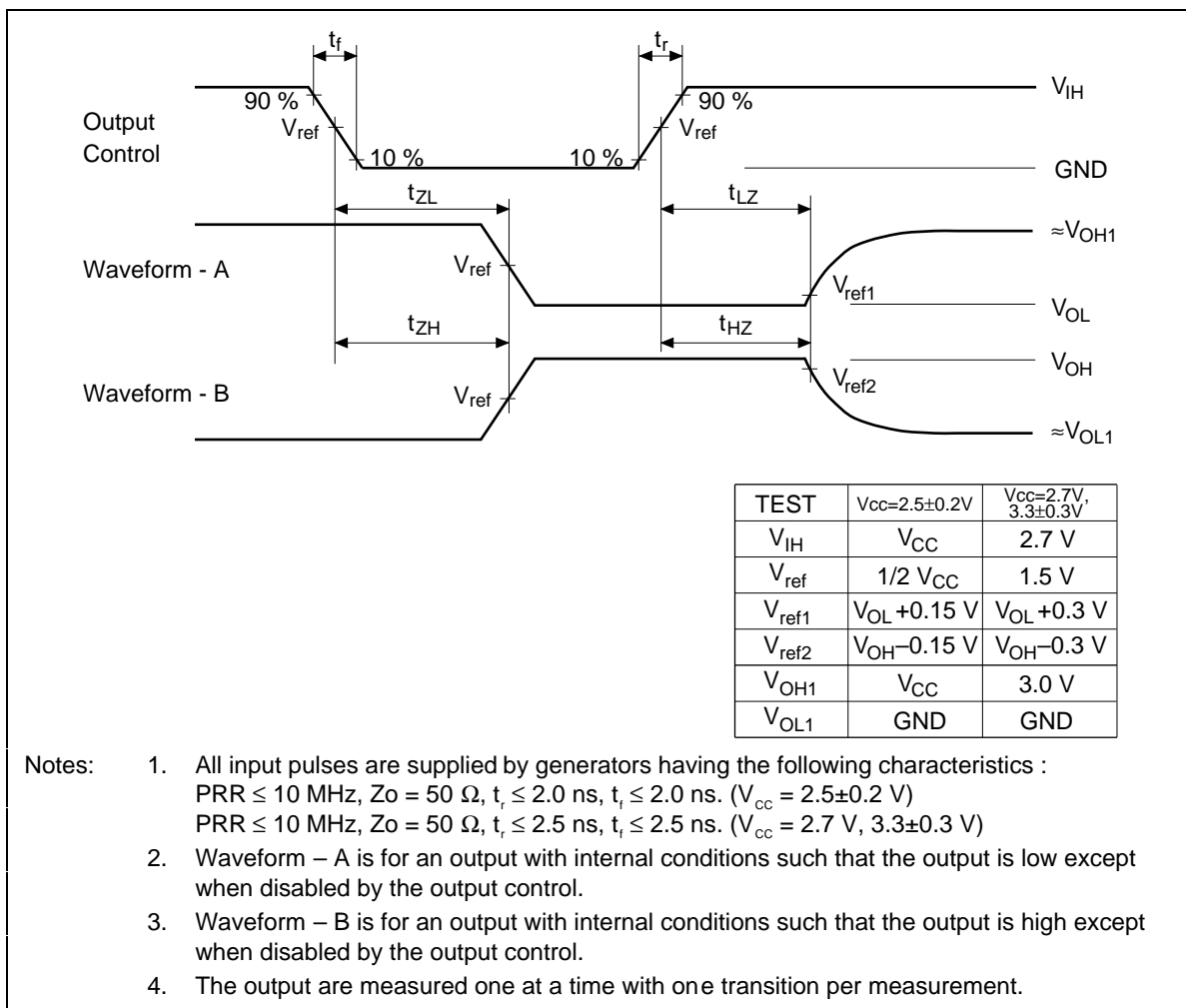
Symbol	$V_{CC}=2.5\pm0.2V$	$V_{CC}=2.7V, 3.3\pm0.3V$
t_{PLH}/t_{PHL}	OPEN	OPEN
t_{ZH}/t_{HZ}	GND	GND
t_{ZL}/t_{LZ}	$2 \times V_{CC}$	6.0 V
C_L	30 pF	50 pF

Note: 1. C_L includes probe and jig capacitance.

Waveforms – 1



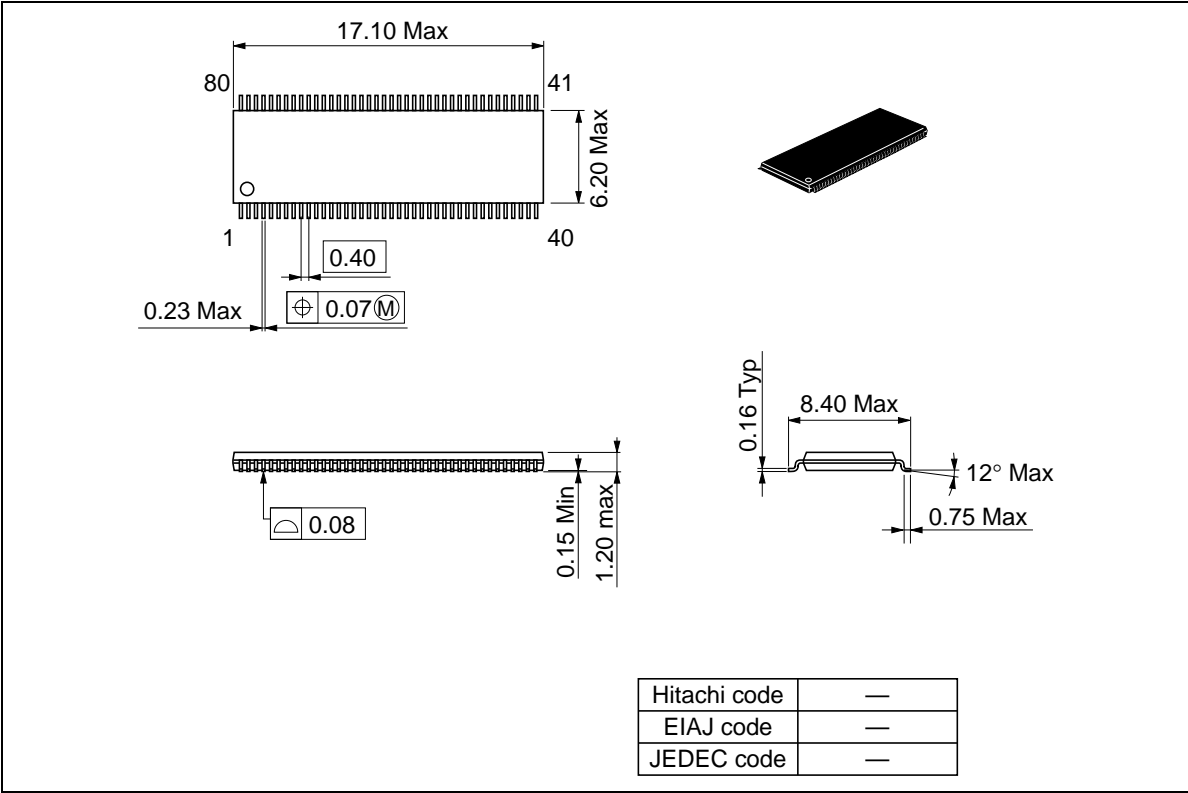
Waveforms – 2



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Package Dimensions

Unit : mm



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