# Fiber Optic LAN Components Connectorized PIN Plus Preamplifier

# Preliminary HFD3180-102

### **FEATURES**

- Prealigned LC SFF Connector sleeve
- Data rates from 622 Mbps to 2.5 Gbps
- PIN detector and preamplifier in a TO-46 heremtic package
- 3.3V operation
- GaAs PIN detector and BiCMOS preamplifier
- Differential Output for low noise
- 2.4 GHz typical Bandwidth



The HFD3180-102 is a high-performance 850nm GaAs detector and preamplifier packaged for high-speed data communications. The product is designed for ease of use by the module designer or manufacturer for data rates from 622 Mbps to 2.5 Gbps.

The HFD3180-102 converts optical power into an electrical signal that is used in fiber optic communications and other applications. As the light increases, the output voltage increases, limiting at input powers above –10dBm. The differential output is designed to be **AC** coupled into a data amplifier. The pre-aligned and lensed package with an industry standard LC SFF style connector sleeve, allows for "drop in" assembly to reduce manufacturing cost.

The Honeywell HFD3180-102 is designed to interface with 50/125 and 62.5/125mm multimode fiber.

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### ABSOLUTE MAXIMUM RATINGS

Parameter	Rating
Storage Temperature	-40 to +85°C
Case Operating Temperature	0 to +70°C
Lead Solder Temperature	260°C, 10 sec.
Power Supply Voltage	3.8 V
Incident Optical Power	0 dBm average, +4 dBm peak

## NOTICE

Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operations section for extended periods of time may affect reliability.

ELECTRO-OPTICAL CHARACTERISTICS (Vcc=3.3V, AC coupled to 50Q, 0°C<T<70°C unless otherwise specified)

Parameters	Test Condition	Symbol	Min.	Тур.	Max.	Units	Notes
Electrical Characteristics							
Supply Voltage	$P_{in} = 0\mu W$ , Rload= $50\Omega$	$V_{cc}$	3.0	3.3	3.8	Volts	1
Supply Current	$P_{in} = 0\mu W$ , Rload= $50\Omega$	$I_{cc}$		26	50	mA	1
Output Voltage	$P_{in} = 100 \mu W$ , Rload= $50 \Omega$	V <sub>out</sub>		200	500	mV	1
Opto-Electronic Characteristics							
Responsivity	$P_{in} = 20 \mu W$ peak,	R		1600		μV/μW	2,3
	Rload=50Ω						
Upper 3dB Bandwidth		$BW_{upper}$	2000	2400	2800	MHz	4
RMS Output Referred	$P_{in}=0\mu W, R_{load}=50\Omega$			500		nW	5
Noise	1875 MHz BT Filter						
Sensitivity	BER=10 <sup>-12</sup> , SNR=7	S	-17	-20		dBm	
Power Supply Rejection	$P_{in}=0\mu W, R_{load}=50\Omega$	PSRR	10	30		dB	6
Ratio							
Pulse Width Distortion	$P_{in}=20\mu W$ peak, $R_{load}=50\Omega$	PWD			40	ps	7
Rise/Fall Time	$P_{in}=20\mu W$ peak, $R_{load}=50\Omega$	$T_R/T_F$			250	ps	8
Wavelength Responsivity	$P_{in}=20\mu W$ peak, $R_{load}=50\Omega$	λ	760	850	860	nm	

### Notes:

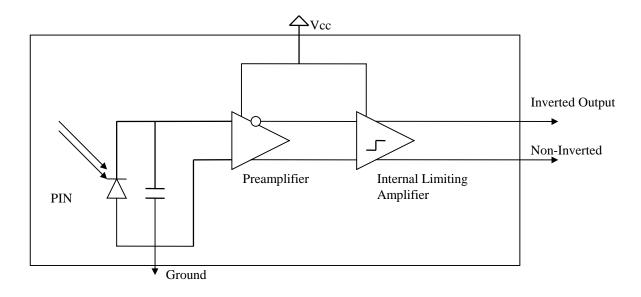
- Pin refers to the total optical power at the face of the fiber optic cable input to the HFD3180-102.
- 2. Responsivity measured with source wavelength of 850nm, 125MHz square wave,  $P_{in}$ =20 $\mu$ W peak,  $R_{load}$ =50 $\Omega$ .
- The output voltage increases as received light power increases, up to approximately -15dBm. The preamplifier is designed to limit the electrical output signal above this optical input level, and does not introduce signal distortion until the average input power exceeds 0dBm.
- 4. Bandwidth is measured with a small signal sinusoidal light source with 50  $\mu$ W average power,  $R_{load}$ =50 $\Omega$ .
- RMS input referred optical noise is obtained by measuring the RMS output referred noise, then dividing by the responsivity.
- 6. PSRR is measured from 300KHz to 1GHz by injecting a 20dB electrical signal on the  $V_{\rm cc}$  pin. The nominal value at 100MHz is recorded. No external bypass components are assumed. An external  $V_{\rm cc}$  filter network will greatly increase the PSRR.

- 7. Measured at the 50% level of output pulses using 0.5 GHz square wave with <200 ps rise time.
- 8. Rise and fall times are measured with source wavelength of 850nm, 125MHz square wave, with optical rise and fall times < 200ps,  $P_{in}$ =20 $\mu$ W peak,  $R_{load}$ =50 $\Omega$ .

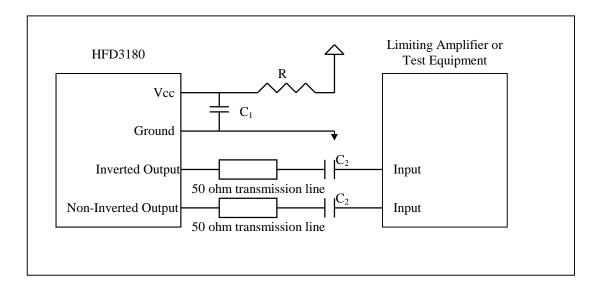
## **NOTICE**

The inherent design of this component causes it to be sensitive to electrostatic discharge (ESD). To prevent ESD-induced damage and/or degradation to equipment, take normal ESD precautions when handling this product

FIGURE 1: INTERNAL SCHEMATIC DIAGRAM OF THE HFD3180-102



### FIGURE 2: RECOMMENDED INTERFACE CIRCUIT FOR THE HFD3180-102



 $R=10 \Omega$ 

 $C_1 = 10 \text{ nF}$ 

 $C_2$  = Data rate dependant (22nF for rates > 1Gbps

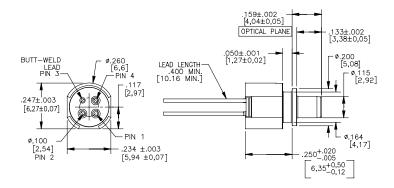
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### **ORDER GUIDE**

<b>Catalog Listing</b>	Description
HFD3180-102	Connectorized PIN Plus Preamplifier

### MOUNTING DIMENSIONS (for reference only) in./(mm)



## **PINOUT**

Number	Function
1	V <sub>CC</sub>
2	Inverted Output
3	Ground
4	Non Inverted Output

### WARRANTY/REMEDY

Honeywell warrants goods of its manufacture as being free of defective materials and faulty workmanship. Commencing with the date of shipment, Honeywell's warranty runs for 18 months. If warranted goods are returned to Honeywell during that period of coverage, Honeywell will repair or replace without charge those items it finds defective. The foregoing is Buyer's sole remedy and is in lieu of all other warranties, expressed or implied, including those of merchantability and fitness for a particular purpose.

While we provide application assistance, personally and through our literature, it is up to the customer to determine the suitability of the product in the application.

Specifications may change at any time without notice. The information we supply is believed to be accurate and reliable as of this printing. However, we assume no responsibility for its use.

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#### INTERNET

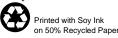
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