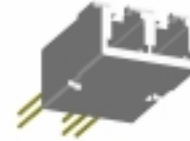


Fiber Optic LAN Components LC SFF Duplex OFE 2.5 Gbps

HFT219x-521

FEATURES

- Prealigned Fixed LC Duplex Optical Front End (OFE)
- VCSEL packaged with a back monitor photodiode
- Common anode and common cathode polarities available
- TO-46 hermetic package for VCSEL and Pin + Preamp
- 3.3 V operation
- GaAs PIN detector and BiCMOS preamplifier
- Differential output for low noise
- High Speed >1GHz
- Laser signal is attenuated
- Unattenuated versions available as well (HFT2193-522 and HFT2194-522)



The HFT219x-52x is a single package transmitter and receiver designed to interface with the LC style optical connectors.

The transmitter is a high performance 850nm VCSEL (Vertical Cavity Surface Emitting Laser) packaged for high speed data communications. This product combines all the performance advantages of VCSEL with a custom designed power monitor diode. The power monitor diode can be used with an appropriate feedback control circuitry to set a maximum power level for each VCSEL. Attenuating coatings are available on the Laser transmitter to simplify design and assist in meeting eye safety requirements.

The PIN + preamp converts optical power into a differential output electrical signal. As the light increases, the differential output voltage increases, limiting at input powers above -10dBm. The differential output is designed to be AC coupled into a data amplifier.

The Honeywell HFT219x-521 is designed to interface with 50/125 and 62.5/125µm multimode fiber within an LC style interface.

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VCSEL PARAMETERS

ELECTRO-OPTICAL CHARACTERISTICS (T_A=25 °C unless otherwise stated)

VCSEL Parameters	Test Condition	Symbol	Min.	Typ.	Max.	Units	Notes
Peak Fiber Coupled Optical Power (See threshold current and slope efficiency which control power output)	I _F =9 mA Peak 50/125 μm fiber NA=0.20	P _{OC}		350		μW	1
Threshold Current		I _{TH}	1	3	5	mA	
Threshold Current Temperature Variation	T _A = 0°C to 70°C	Δ I _{TH}	-1.5		1.5	mA	2
Slope Efficiency	P _{OC} =0.5 mW	η	0.028		0.14	mW/mA	3
Slope Efficiency Temperature Variation	T _A = 0°C to 70°C	Δη/ΔT		-6000		PPM/°C	
Peak Wavelength	I _F =9 mA	λ _p	830	850	860	nm	
λ _p Temp Coefficient	I _F =9 mA	Δλ _p /ΔT		0.06		nm/°C	
Spectral Bandwidth	I _F =9 mA, FWHM	Δλ			0.85	nm	
Laser Forward Voltage	I _F =9 mA	V _F		1.8	2.2	V	
Laser Reverse Voltage	I _R =10 μA	BVR _{LD}		-10		V	
Rise and Fall Time	Bias Above Threshold (20%-80%)	t _R t _F			130 150	ps	4
Relative Intensity Noise	1 GHz BW	RIN		-130	-122	dB/Hz	
Series Resistance	I _F =9 mA	R _S	18	25	40	Ohms	
Series Resistance Temperature Coefficient	I _F =9 mA, 0°C to 70°C	dR _S /dT		-3000		PPM/°C	

Photodiode Parameters	Test Condition	Symbol	Min.	Typ.	Max.	Units	Notes
Monitor Current	P _{OC} =0.5 mW	I _{PD}		0.42		mA	
Monitor Current Temperature Variation	P _{OC} =0.5 mW	ΔI _{PD} /ΔT		0.2		%/°C	
Dark Current	P _O =0 mW, V _R =3 V	I _D			20	nA	
PD Reverse Voltage	P _O =0 mW, I _R =10 μA	BVR _{PD}	30	115		V	5
PD Capacitance	V _R =0 V, Freq=1 MHz	C		75	100	pF	
	V _R =3 V, Freq=1 MHz			40	55		

Notes:

- Operating power is set by the peak operating current I_{PEAK}=I_{BIAS}+I_{MODULATION}.
- Operation at temperatures outside the specified range may result in the threshold current exceeding the maximums defined in the electro-optical characteristics table.
- Slope efficiency is defined as ΔP_O/ΔI_F at a total power output of 0.5 mW. Slope efficiency is intentionally lowered to the values shown by optical attenuation.
- Rise and fall times are sensitive to drive electronics.
- To safeguard the VCSEL from current spike damage, short the VCSEL anode and cathode to each other during photodiode BVR verification testing. Additionally to safeguard the PIN photodiode, limit the photodiode reverse voltage in accordance with the absolute maximum rating

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RECEIVER PARAMETERS

ELECTRO-OPTICAL CHARACTERISTICS ($V_{cc}=3.3V$, AC coupled to 50Ω , $0^\circ C < T < 70^\circ C$ unless otherwise specified)

Parameters	Test Condition	Symbol	Min.	Typ.	Max.	Units	Notes
Electrical Characteristics							
Supply Voltage	$P_{in} = 0\mu W$, $R_{load}=50\Omega$	V_{cc}	3.0	3.3	3.8	Volts	1
Supply Current	$P_{in} = 0\mu W$, $R_{load}=50\Omega$	I_{cc}		26	50	mA	1
Output Voltage	$P_{in} = 100\mu W$, $R_{load}=50\Omega$	V_{out}		200	500	mV	1
Opto-Electronic Characteristics							
Responsivity	$P_{in} = 20\mu W$ peak, $R_{load}=50\Omega$	R		1600		$\mu V/\mu W$	2,3
Upper 3dB Bandwidth		BW_{upper}	2000	2400	2800	MHz	4
RMS Output Referred Noise	$P_{in}=0\mu W$, $R_{load}=50\Omega$ 1875 MHz BT Filter			500		nW	5
Sensitivity	$BER=10^{-12}$, $SNR=7$	S	-20	-24		dBm	
Power Supply Rejection Ratio	$P_{in}=0\mu W$, $R_{load}=50\Omega$	PSRR	10	30		dB	6
Pulse Width Distortion	$P_{in}=20\mu W$ peak, $R_{load}=50\Omega$	PWD			40	ps	7
Rise/Fall Time	$P_{in}=20\mu W$ peak, $R_{load}=50\Omega$	T_R/T_F			250	ps	8
Wavelength Responsivity	$P_{in}=20\mu W$ peak, $R_{load}=50\Omega$	λ	760	850	860	nm	

Notes:

1. Pin refers to the total optical power at the face of the fiber optic cable input.
2. Responsivity measured with source wavelength of 850nm, 125MHz square wave, $P_{in}=20\mu W$ peak, $R_{load}=50\Omega$.
3. The output voltage increases as received light power increases, up to approximately -15dBm. The preamplifier is designed to limit the electrical output signal above this optical input level, and does not introduce signal distortion until the average input power exceeds 0dBm.
4. Bandwidth is measured with a small signal sinusoidal light source with 50 μW average power, $R_{load}=50\Omega$.
5. RMS input referred optical noise is obtained by measuring the RMS output referred noise, then dividing by the responsivity.
6. PSRR is measured from 300KHz to 1GHz by injecting a -20dB electrical signal on the V_{cc} pin. The nominal value at 100MHz is recorded. No external bypass components are assumed. An external V_{cc} filter network will greatly increase the PSRR.
7. Measured at the 50% level of output pulses using 0.5 GHz square wave with <200 ps rise time.
8. Rise and fall times are measured with source wavelength of 850nm, 125MHz square wave, with optical rise and fall times < 200ps, $P_{in}=20\mu W$ peak, $R_{load}=50\Omega$.

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Absolute Maximum Ratings

Parameter	Rating
Storage Temperature	-40 to +85°C
Operating Temperature	0 to +70°C
Lead Solder Temperature	260°C, 10 sec.
Continuous Optical Output Power (Any Current)	5 mW
Laser Diode Reverse Voltage ($I_R=10\ \mu A$)	5 V
Laser Continuous Forward Current, Heat-Sinked	15 mA
PIN Photodiode Forward Current	10 mA
Power Supply Voltage (PIN + Preamp)	3.8 V
Incident Optical Power	0 dBm average, +4 dBm peak

NOTICE

Stresses greater than those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operations section for extended periods of time may affect reliability.

NOTICE

The inherent design of this component causes it to be sensitive to electrostatic discharge (ESD). To prevent ESD-induced damage and/or degradation to equipment, take normal ESD precautions when handling this product

ORDER GUIDE

Catalog Listing	Description
HFT2190-521	Attenuated, Common Anode
HFT2191-521	Attenuated, Common Cathode

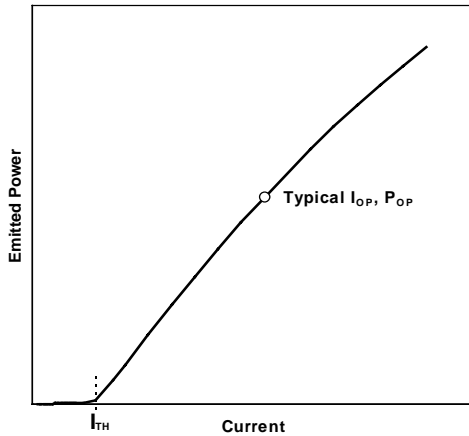
Fiber Optic LAN Components

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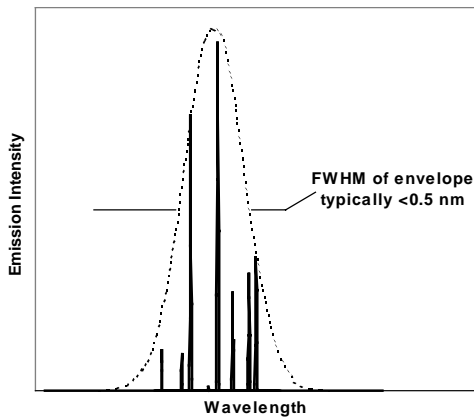
HFT219x-521

TYPICAL PERFORMANCE CURVES

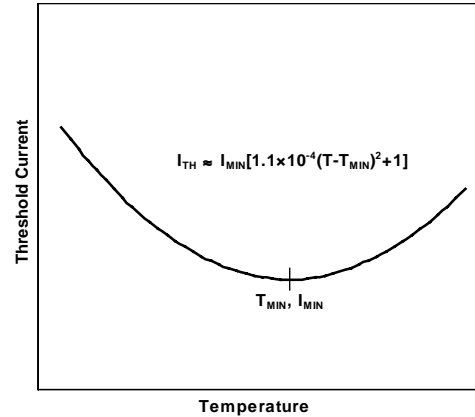
Emitted Power vs. Current: Power varies approximately linearly with current above threshold.



Emission Intensity vs. Wavelength: Typical 10 mA spectrum comprises multiple lines corresponding to multiple transverse modes.



Threshold Current vs. Temperature: Threshold current varies parabolically with temperature; thus it can be nearly constant for a limited temperature range.



NOTICE

The inherent design of this component causes it to be sensitive to electrostatic discharge (ESD). To prevent ESD-induced damage and/or degradation to equipment, take normal ESD precautions when handling this product.



The VCSEL is a class IIIb laser and should be treated as a potential eye hazard. Due to the size of the component, the applicable warning logotype, aperture label, and certification/identification label cannot be placed on the component itself. These labels can be found on the individual envelope in which the VCSEL unit is packaged, or attached to the shipping package.

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FIGURE 1: INTERNAL SCHEMATIC DIAGRAM OF THE HFD3180-102

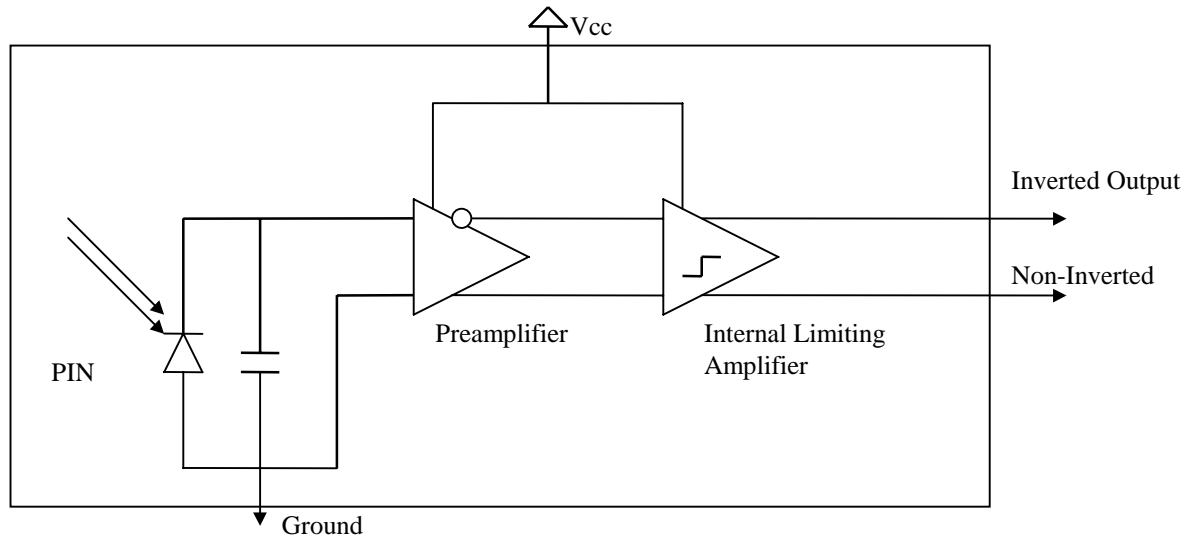
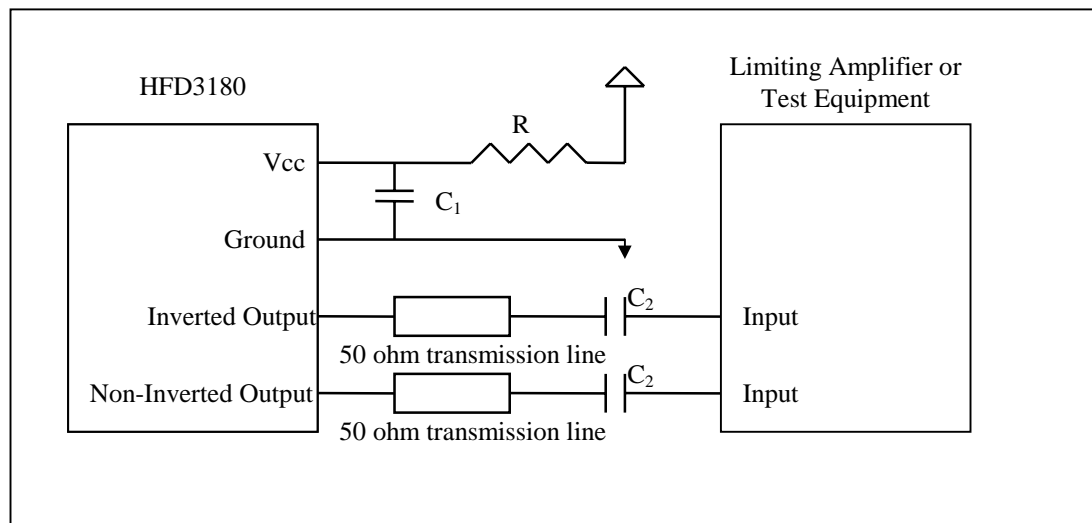


FIGURE 2: RECOMMENDED INTERFACE CIRCUIT FOR THE HFD3180-102



$R=10\ \Omega$

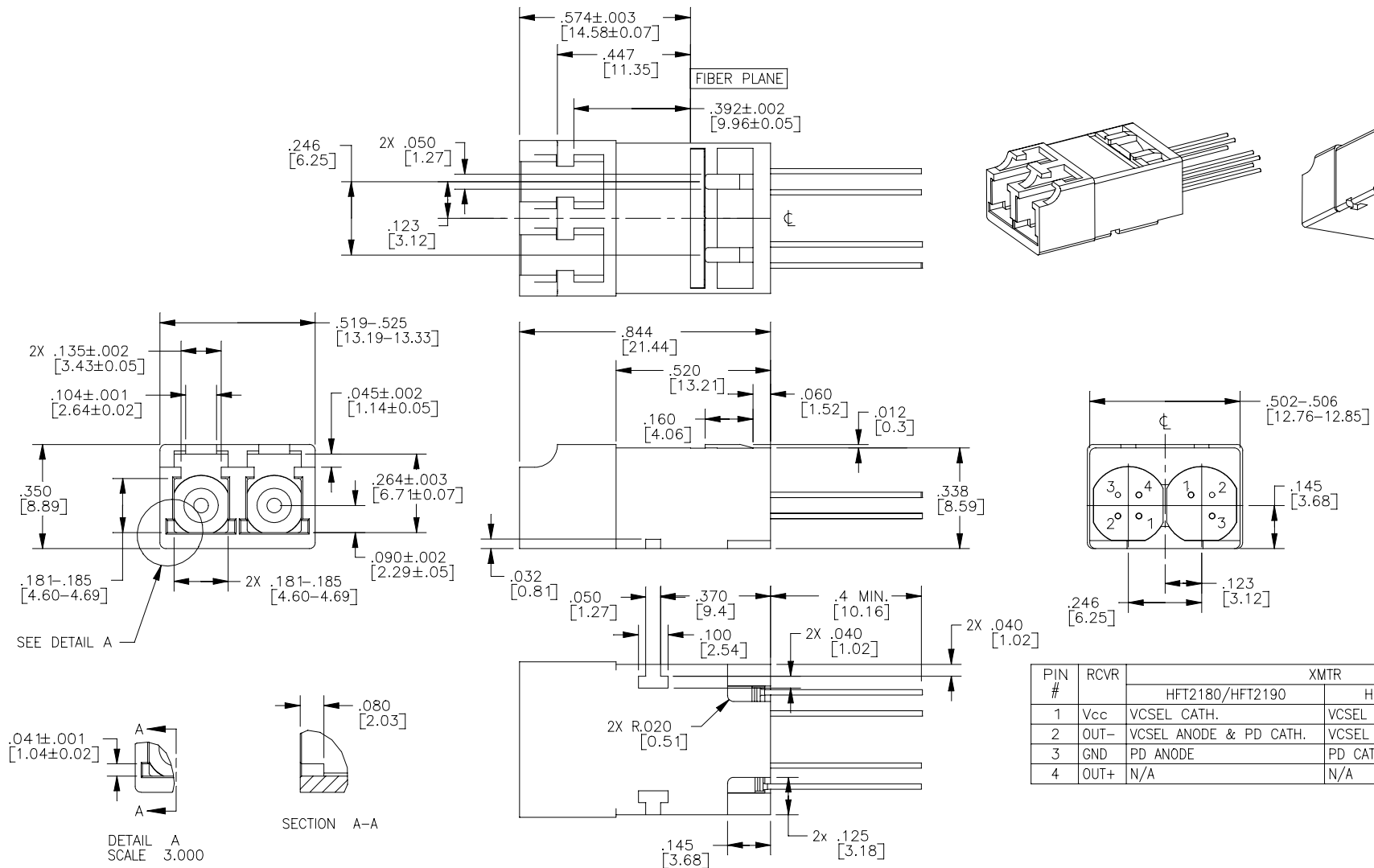
$C_1 = 10\ \text{nF}$

$C_2 = \text{DATA RATE DEPENDANT (22NF FOR RATES > 1GB)}$

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PIN #	RCVR	XMTR	
		HFT2180/HFT2190	HFT2181/HFT2191
1	V _{CC}	VCSEL CATH.	VCSEL ANODE
2	OUT-	VCSEL ANODE & PD CATH.	VCSEL CATH. & PD ANODE
3	GND	PD ANODE	PD CATHODE
4	OUT+	N/A	N/A

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While we provide application assistance, personally and through our literature, it is up to the customer to determine the suitability of the product in the application.

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TELEPHONE

1-800-367-6786 (USA)
1-800-737-3360 (Canada)
+49 (0) 89 35813310 (Germany)
+65-580-3312 (Singapore)
+44 (0) 118 981 9511 (UK)

INTERNET

<http://www.honeywell.com/sensing/VCSEL>
info.sc@honeywell.com

FAX

1-972-470-4326 (Customer Response Center)
1-972-470-4549 (Fax on demand)
+49 (0) 89 3599971 (Germany)
+65 445 3033 (Singapore)
+44 (0) 118 981 7513 (UK)

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Honeywell

Honeywell Inc.
11 West Spring Street
Freeport, Illinois 61032



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Honeywell Inc.
Optoelectronics Facility
830 East Arapaho Road
Richardson, Texas 75081

**Honeywell Control
Systems Ltd.**
Zodiac House
Calleva Park
Aldermaston, Berkshire
RG7 8HW England

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