Octal Buffers/Line Drivers With 3 State Outputs

HITACHI

ADE-205-596 (Z) 1st. Edition Dec. 2000

Description

The HD151244 using CMOS process, provides high speed and high drivability equivalent to HD74LS240 with super low power dissipation. The device has eight inverter drivers (three state outputs) in 20 pin package. Each four drivers construct a group and the two groups are controlled separately by $1\overline{G}$ and $2\overline{G}$ which enable the output at low level. Power up down protection function keeps the output in high impedance state at low V_{CC} regardless of the state in enable inputs.

Features

• High speed tpd = 10 ns (Typ)

• High output current $I_{OH} = -15 \text{ mA}$

 $I_{OL} = 24 \text{ mA}$

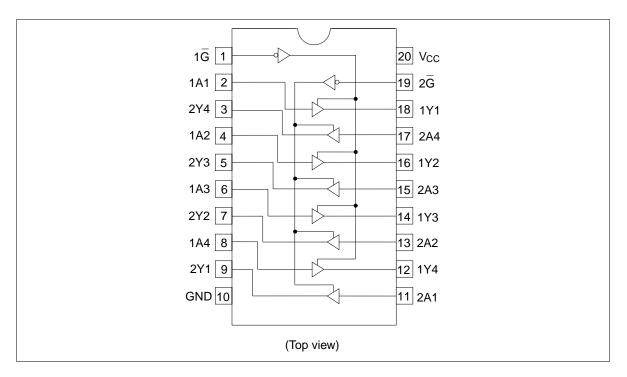
Both input and output is TTL level

• Wide operating temperature: $Ta = -40 \text{ to } +85^{\circ}\text{C}$

- Both input and output is high impedance state at supply off
- The function provides power up down protection



Pin Arrangement



Function Table

Inputs

X :

G	A	Output Y
Н	X	Z
L	Н	Н
L	L	L

H : High levelL : Low levelZ : High impedance

Irrelevant

Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item	Symbol	Rating	Unit	
Supply Voltage	V _{cc}	-0.5 to +7.0	V	
Input Voltage (G, A)	V _{IN}	-0.5 to +7.0	V	
Output Voltage	V _{out}	-0.5 to +5.5	V	
Power Dissipation	P _T	500	mW	
Storage Temperature	Tstg	-65 to +150	°C	

Note: 1. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit	
Supply Voltage	V _{cc}	4.5	5.0	5.5	V	
Output Current	I _{OH}	_	_	-15	mA	
	I _{OL}	_	_	24	mA	
Operating Temperature	Topr	-40	25	85	°C	
Input Rise and Fall Time*1	t _r , t _f	0	_	250	ns/V	

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics (Ta = -40° C to $+85^{\circ}$ C)

Item	Symbol	V _{cc} (V)	Min	Max	Unit Conditions		
Input Voltage	V _{IH}	_	2.0	_	V		
	V _{IL}	_	_	0.8	V		
Output Voltage	V_{OH}	4.5	2.4	_	V	$I_{OH} = -3$ mA, $V_{IN} = V_{CC} - 2.1$ V or 0.5 V	
		4.5	2.0	_		$I_{OH} = -15 \text{ mA}, V_{IN} = V_{CC} - 2.1 \text{V or } 0.5 \text{ V}$	
	V _{OL}	4.5	_	0.4	V	$I_{OL} = 12 \text{ mA}, V_{IN} = V_{CC} - 2.1 \text{ V or } 0.5 \text{ V}$	
		4.5	_	0.5		I_{OL} = 24 mA, V_{IN} = V_{CC} – 2.1 V or 0.5 V	
Off State Output Current	I _{oz}	5.5	_	±5.0	μΑ	$V_{OUT} = V_{CC}$ or GND	
	I _{OZ(off)}	0	_	±5.0		$V_{OUT} = 5.5 \text{ V}$	
Input Current	I _{IN}	5.5	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND	
	I _{IN(off)}	0	_	±1.0		V _{IN} = 5.5 V	
Output Short current*1	I _{os}	5.5	-40	-225	mΑ		
Power Supply Current	I _{cc}	5.5	_	0.5	mΑ	$I_{\text{OUT}} = 0~\mu\text{A},~V_{\text{IN}} = V_{\text{CC}}~\text{or}~0.5~\text{V}$	
	I _{CCT} *2	5.5	_	1.5		$V_{IN} = V_{CC} - 2.1 \text{ V or } 0.5 \text{ V}$	

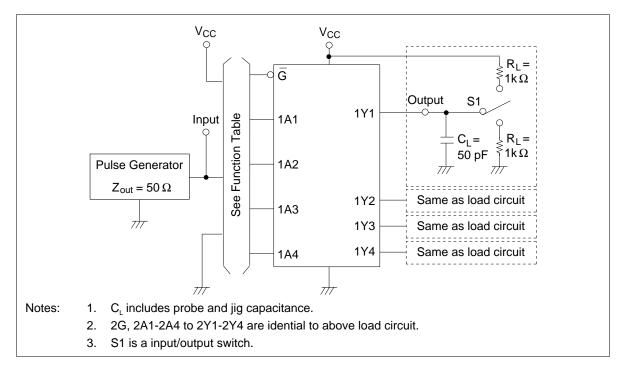
Notes: 1. Not more than one output should be shorted at a time and duration of the short circuit should not exceed one second.

2. When input by the TTL level, it shows $I_{\text{\tiny CC}}$ increase at per 1 pin.

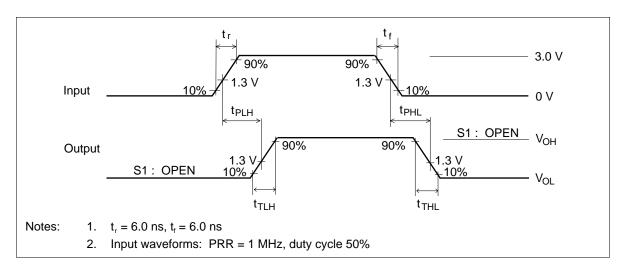
Switching Characteristics $(C_L = 50 \text{ pF})$

		Ta = 25°C V _{cc} = 5 V		Ta = -40° C to 85°C V _{cc} = 5 V ±10%			
Item	Symbol	Min	Max	Min	Max	Unit	Conditions
Propagation Delay Time	t _{PLH}	3.0	15.0	3.0	18.0	ns	See Next Page
	t _{PHL}	3.0	15.0	3.0	18.0	-	
Output Rise Time	t _{TLH}	0.0	10.0	0.0	10.0	ns	
Output Fall Time	t_{THL}	0.0	10.0	0.0	10.0	-	
Output Enable Time	t_{ZH}	3.0	25.0	3.0	30.0	ns	_
	t _{ZL}	3.0	25.0	3.0	30.0		
Output Disable Time	t _{HZ}	3.0	25.0	3.0	30.0	ns	_
	t _{LZ}	3.0	25.0	3.0	30.0	-	
Input Capacitance	C _{IN}	_	5 (Typ)	_	_	pF	$V_{IN} = V_{CC}$ or GND
Output Capacitance	C _o	_	12 (Typ)	_	_	pF	$V_{o} = V_{cc}$ or GND

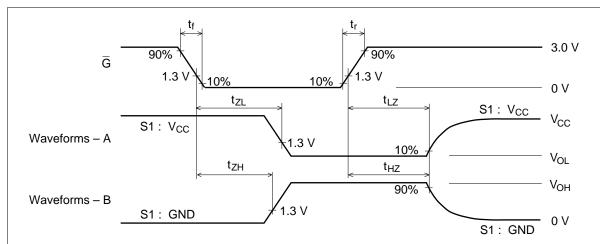
Test Circuit 1



Waveforms 1



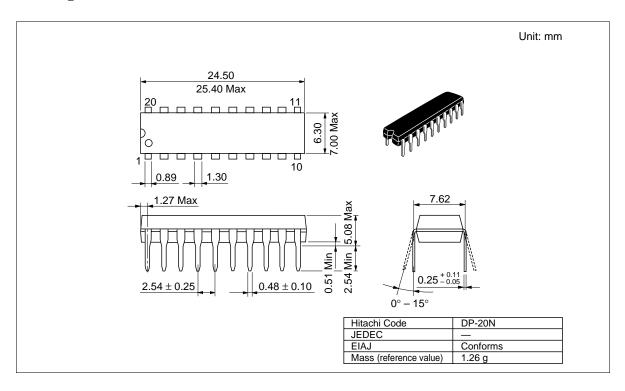
Waveforms 2

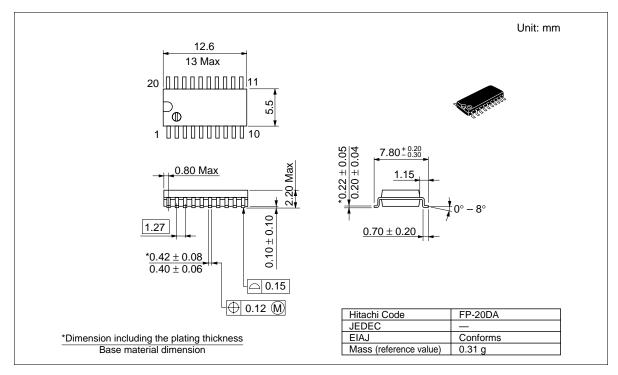


Notes:

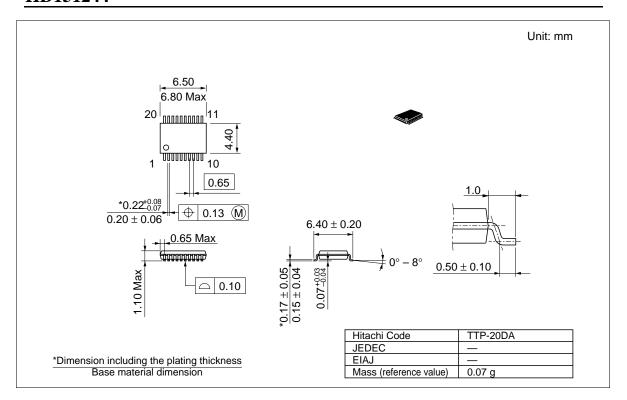
- 1. $t_r = 6.0 \text{ ns}, t_f = 6.0 \text{ ns}$
- 2. Input waveforms: PRR = 1 MHz, duty cycle 50%
- 3. Waveform A shows input conditions such that the output is low level when enable by the output control.
- 4. Waveform B shows input conditions such that the output is high level when enable by the output control.

Package Dimensions





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