# HA17474/P

# **Quad Operational Amplifier**

# **HITACHI**

ADE-204-041 (Z) Rev. 0 Dec. 2000

### **Description**

HA17474/P is a quad operational amplifier with provided internal frequency compensation and high performance. It can be applied widely to measuring control equipment and to general use.

#### **Features**

- High speed: 1.6 V/μs
- Continuous short-circuit protection
- Low-noise operational amplifiers
- Internal frequency compensation
- Wide operating power supply voltage range:  $V = \pm 2 \text{ V}$  to  $\pm 20 \text{ V}$
- Pin compatible with HA17324, HA17902

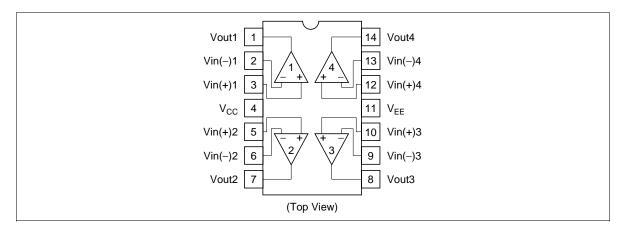
### **Ordering Information**

Type No.	Application	Package	
HA17474P	Industrial use	DP-14	
HA17474	Commercial use		

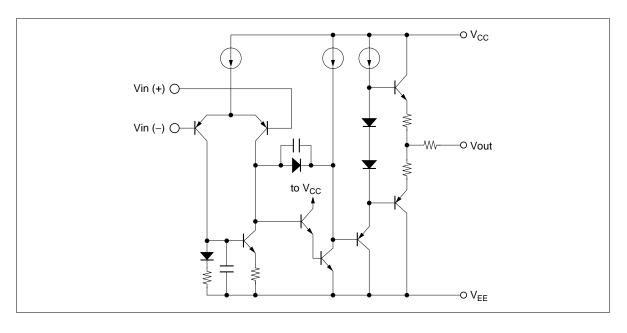


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## **Pin Arrangement**



### **Circuit Schematic**



#### **Absolute Maximum Ratings** (Ta = 25°C)

Rati	ngs
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Item	Symbol	HA17474	HA17474P	HA17474RP	Unit
Power supply	V <sub>cc</sub>	+20	+20	+20	V
	V <sub>EE</sub>	-20	-20	-20	V
Common-mode differential voltage	Vin(diff)	±30	±30	±30	V
Common-mode input voltage	V <sub>CM</sub>	±15 *1	±15 *1	±15 *1	V
Power dissipation	P <sub>T</sub>	670 *²	670 *²	670 *²	mW
Operating temperature range	Topr	-20 to +75	-20 to +75	-20 to +75	°C
Storage temperature range	Tstg	-55 to +125	-55 to +125	-55 to +125	°C

Notes: 1. For supply voltage less than  $\pm 15$  V, the absolute maximum input voltage is equal to the supply voltage.

2. Value under Ta  $\leq$  35°C. In case of more than it, 8.3 mW/°C derating shall be done.

## **Electrical Characteristics** (Ta = 25°C, $V_{CC} = +15$ V, $V_{EE} = -15$ V)

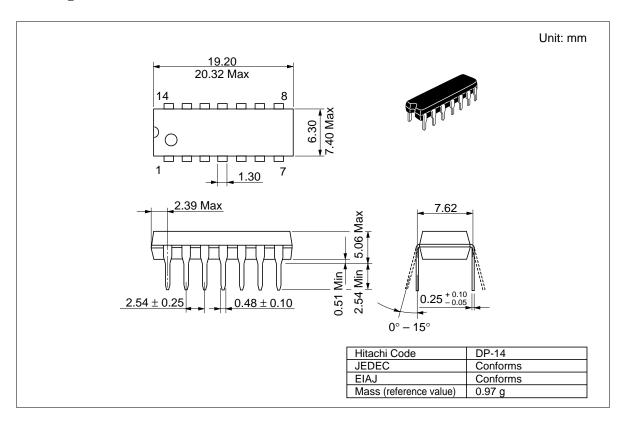
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Input offset voltage	V <sub>IO</sub>	_	1.0	5.0	mV	$R_s \le 10 \text{ k}\Omega$
Input offset current	I <sub>IO</sub>	_	30	50	nA	
Input bias current	I <sub>IB</sub>	_	100	300	nA	
Voltage gain	$A_{VD}$	88	94	_	dB	$R_L \ge 2 \text{ k}\Omega, V_O = \pm 10 \text{ V}$
Maximum output	Vop-p	±12	±13.7	_	V	$R_L \ge 10 \text{ k}\Omega$
voltage		±10	±12.5	_	V	$R_L \ge 2 k\Omega$
Common-mode input voltage range	V <sub>CM</sub>	±12	±14	_	V	
Common-mode rejection ratio	CMR	80	90	_	dB	$R_s \le 10 \text{ k}\Omega$
Supply voltage rejection ratio	PSRR	_	50	100	μV/V	$R_s \le 10 \text{ k}\Omega$
Power dessipation	Pd	_	150	210	mW	4-channel, No load
Slew rate	SR	_	1.6	_	V/μs	A <sub>VD</sub> = 1
Equivalent input noise voltage	V <sub>NI</sub>	_	9	_	nV/√ <del>Hz</del>	$R_s = 1 k\Omega$ , f = 1 Hz to 1 kHz
Channel separation	CS	_	108	_	dB	f = 1 kHz

Note: Since these products provide a high slew rate, oscillation may occur due to load capacitance. An allowable capacitor value is minimum at voltage follower.

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# **Package Dimensions**



#### **Cautions**

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