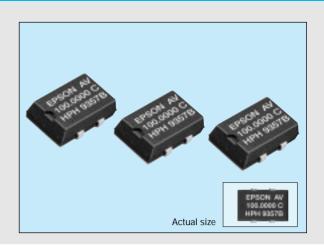
PROGRAMMABLE HIGH-STABILITY HIGH-FREQUENCY CRYSTAL OSCILLATOR

HG-8002JA series

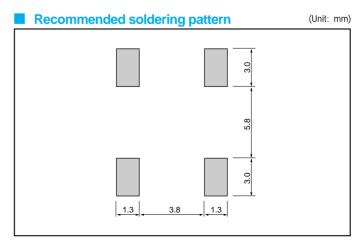
- Wide frequency output by PLL technology.
- Low current consumption by output enable function (OE) or standby function (ST).
- Package and pin compatible with SG-615.
- Low current consumption due of C-MOS technology.
- · Excellent shock resistance and environmental capability.



■ Specifications (characteristics)

Item		Symbol	PT/ST	PH/SH	PC/SC	Remarks	
			Specifications			Kernarks	
Output frequency range		fo		1.0000 MHz to 125.0000 MHz			
Power source voltage	Max. supply voltage	V _{DD} -GND		-0.5V to +7.0V			
	Operating voltage	V _{DD}	5.0V±0.25V		3.3V ± 0.165V		
Temperature range	Storage temperature	Tstg	-55°C to +125°C				
	Operating temperature	Topr	-20°C to +70°C (-40°C to +85°C)		Refer to page 4."Frequency range"		
Soldering condition		Tsol	Twice at under 260°C within 10 sec. or under 230°C within 3 min.				
Frequency stability		∆f/fo	AV: ±20ppm BV:	AV: ±20ppm BV: ±25ppm CX: ± 30ppm(-40°C to +85°C)		-20°C to +70°C	
Current consumption		lop	45mA max. 28mA max.		No load condition, Max. frequency range		
Output disable current		loe	30mA max.		16mA max.	OE=GND(PT, PH, PC)	
Standby current		I st		50 μA max.		ST=GND(ST, SH, SC)	
Duty		tw/ t	40% to 60%		C-MOS load: 1/2VDD level, Max. load condition		
			40% to 60%		_	TTL load: 1.4V level, Max. load condition	
High output voltage		Vон		V _{DD} -0.4V min.		I _{OH} =-16mA(PT/ST, PH/SH),-8mA(PC/SC)	
Low output voltage		Vol		0.4V max.		IoL= 16mA(PT/ST, PH/SH), 8mA(PC/SC)	
Output load condition (fan out)	TTL	N	2TTL max.	_		Max. frequency and max. operating voltage range	
	C-MOS	CL		15pF max.		iviax. Trequericy and max. operating voltage range	
Output enable/disable input voltage		VIH	2.0V r	nin.	0.7 × V _{DD} min.	ST. OE terminal	
		VIL	0.8V m	nax. $0.2 \times V_{DD}$ max.		S1, OE terriiriai	
Output rise time	C-MOS level	tтьн	_	3ns max.		C-MOS load: 20%→80% Vpb level	
	TTL level	LILI	4ns max.	_		TTL load: 0.4V→2.4V level	
Output fall time	C-MOS level	tтнL	_	3ns max.		C-MOS load: 80%→20% Vpp level	
	TTL level	LIHL	4ns max.	_		TTL load: 2.4V→0.4V level	
Oscillation start up time		tosc		10ms max.		Time at minimum operating voltage to be 0 sec.	
Aging		fa		±2ppm/year max.		Ta= 25°C, V _{DD} = 5.0V/3.3V(PC/SC)	
Shock resistance		S.R.		±2ppm max.		Three drops on a hard board from 75 cm or excitation test with 3000G x 0.3ms x 1/2sine wave in 3 directions	

Note: • Please contact us for inquiries about operating temperature(-40°C to +85°C), usable frequencies, duty and output load conditions.



THE CRYSTALMASTER

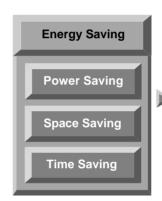


EPSON offers effective savings to its customers through a wide range of electronic devices, such as semiconductors, liquid crystal display (LCD) modules, and crystal devices. These savings are achieved through a sophisticated melding of three different efficiency technologies.

Power saving technology provides low power consumption at low voltages.

Space saving technology provides further reductions in product size and weight through super-precise processing and high-density assembly technology.

Time saving technology shortens the time required for design and development on the customer side and shortens delivery times.



Our concept of Energy Saving technology conserves resources by blending the essence of these three efficiency technologies. The essence of these technologies is represented in each of the products that we provide to our cus-

In the industrial sector, leading priorities include measures to counter the greenhouse effect by reducing CO2,

measures to preserve the global environ-

ment, and the development of energyefficient products. Environmental problems are of global concern, and although the contribution of energysaving technology developed by EPSON may appear insignificant, we seek to contribute to the develop-

ment of energy-saving products by our

customers through the utilization of our electronic devices. EPSON is committed to the conservation of energy, both for the sake of people and of the planet on which we live.





Resource

Saving



SEIKO EPSON CORP. QUARTZ DEVICE DIVISION acquired ISO9001 and ISO14001 certification by B.V.Q.I. (Bureau Veritas Quality International) .

> ISO9001 in October, 1992. ISO14001 in November, 1997.

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