

TOSHIBA DIODE SILICON EPITAXIAL PLANAR TYPE

HN2D02FU

ULTRA HIGH SPEED SWITCHING APPLICATION.

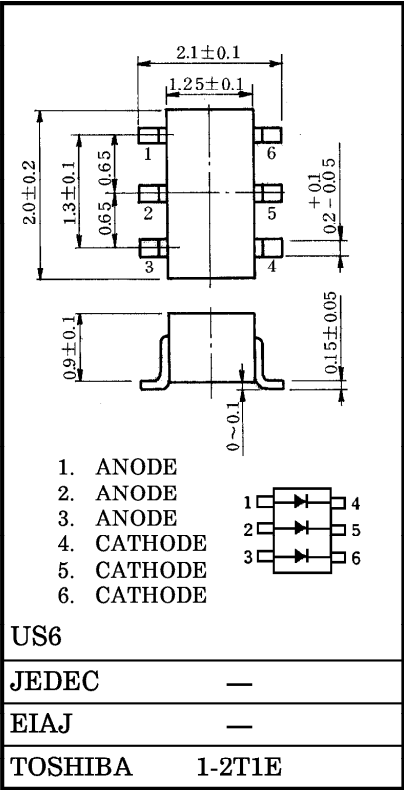
Unit in mm

- HN2D02FU is composed of 3 independent diodes.
- Low Forward Voltage : $V_F(3)=0.98V$ (Typ.)
- Fast Reverse Recovery Time : $t_{rr}=1.6ns$ (Typ.)
- Small Total Capacitance : $C_T=0.5pF$ (Typ.)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Maximum (Peak) Reverse Voltage	V_{RM}	85	V
Reverse Voltage	V_R	80	V
Maximum (Peak) Forward Current	I_{FM}	240*	mA
Average Forward Current	I_O	80*	mA
Surge Current (10ms)	I_{FSM}	1*	A
Power Dissipation	P	200	mW
Junction Temperature	T_j	125	°C
Storage Temperature	T_{stg}	-55~125	°C

* : This is the Maximum Ratings of single diode (Q1 or Q2 or Q3). In the case of using 2 or 3 diodes, the Maximum Ratings per diode is 75% of the single diode one.

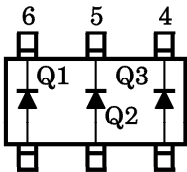
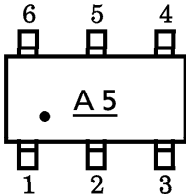


ELECTRICAL CHARACTERISTICS (Q1, Q2, Q3 COMMON, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Forward Voltage	$V_F(1)$	$I_F=1mA$	—	0.62	—	V
	$V_F(2)$	$I_F=10mA$	—	0.75	—	
	$V_F(3)$	$I_F=100mA$	—	0.98	1.20	
Reverse Currunt	$I_R(1)$	$V_R=30V$	—	—	0.1	μA
	$I_R(2)$	$V_R=80V$	—	—	0.5	
Total Capacitance	C_T	$V_R=0, f=1MHz$	—	0.5	3.0	pF
Reverse Recovery Time	t_{rr}	$I_F=10mA$ (Fig. 1)	—	1.6	4.0	ns

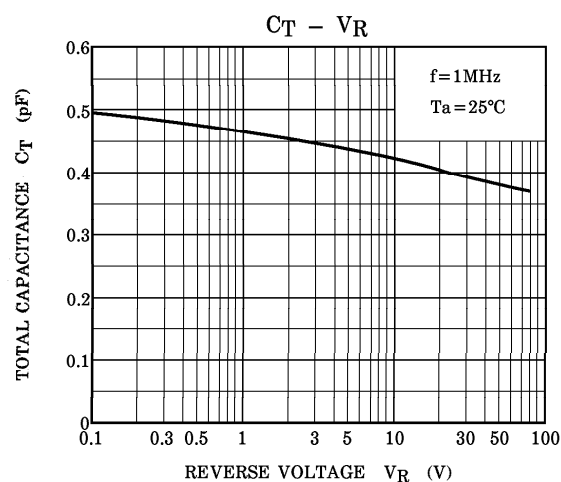
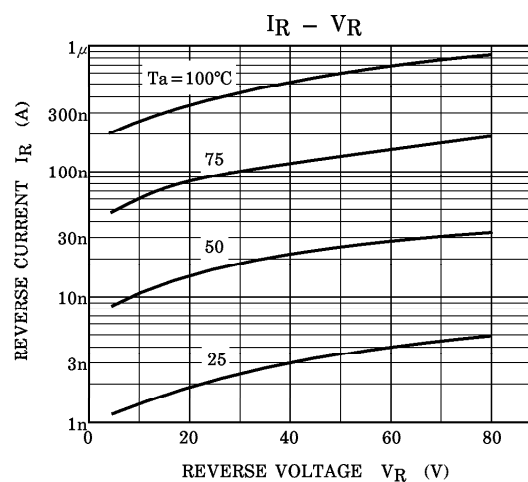
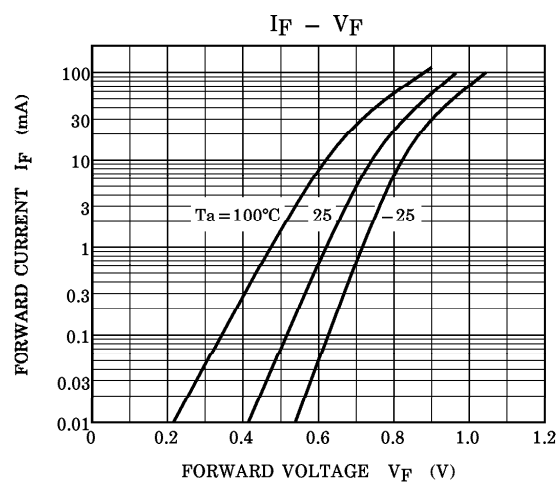
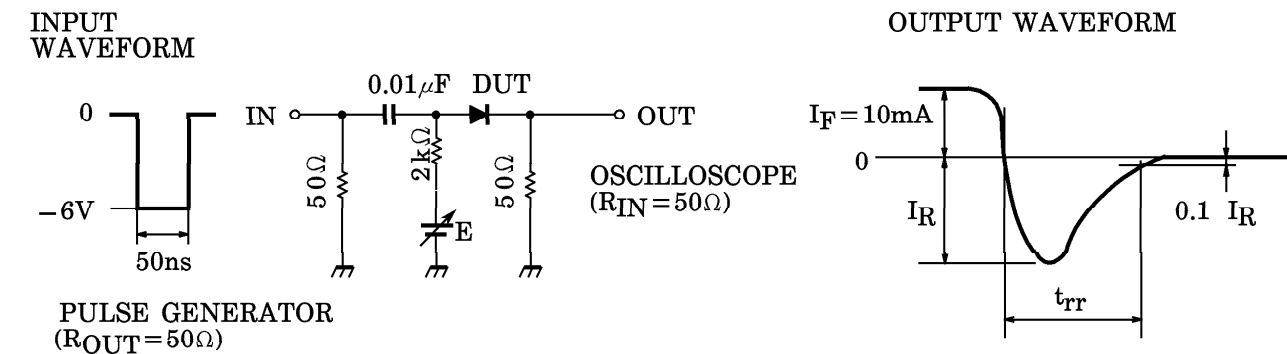
Marking

PIN ASSIGNMENT (TOP VIEW)



961001EAA2

● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

Fig. 1 : REVERSE RECOVERY TIME (t_{rr}) TEST CIRCUIT

961001EAA2'

- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.