
HAT2085R

Silicon N Channel MOS FET
High Speed Power Switching

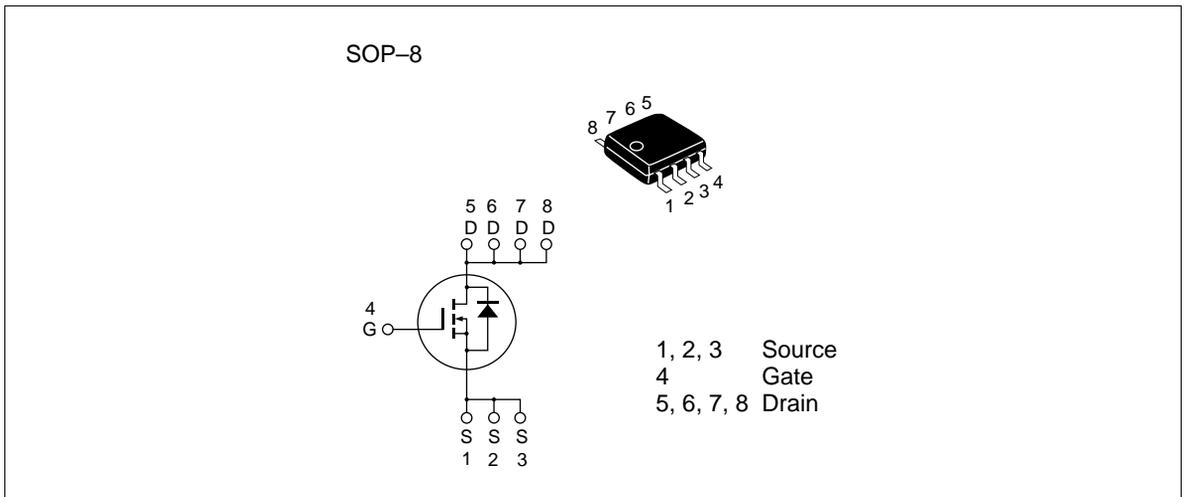
HITACHI

ADE-208-1232 (Z)
Target Specification 1st. Edition
Dec. 2000

Features

- Low on-resistance
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	200	V
Gate to source voltage	V_{GSS}	± 30	V
Drain current	I_D	(2)	A
Drain peak current	$I_{D(\text{pulse})}$ ^{Note1}	(16)	A
Body-drain diode reverse drain current	I_{DR}	(2)	A
Channel dissipation	P_{ch} ^{Note2}	2.5	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Note: 1. $PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$

2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), $PW \leq 10\text{s}$

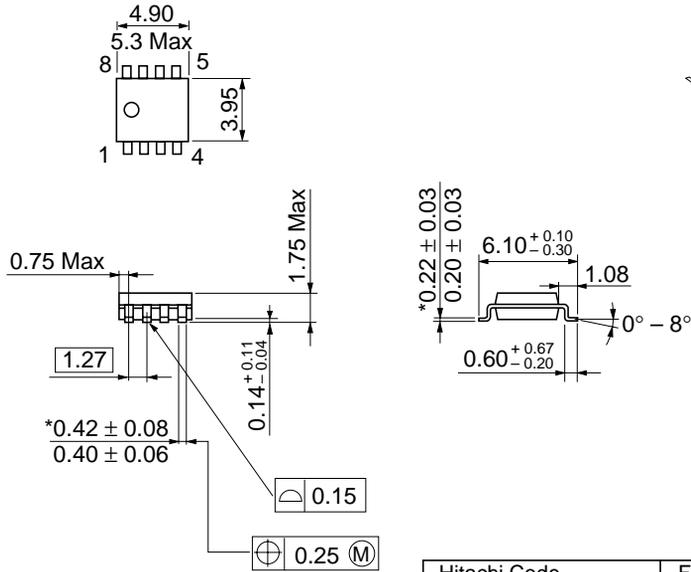
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	$I_D = 10\text{mA}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 200\text{V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(\text{off})}$	(3.0)	—	(4.5)	V	$I_D = 1\text{mA}$, $V_{DS} = 10\text{V}$
Static drain to source on state resistance	$R_{DS(\text{on})}$	—	(0.49)	(0.64)	Ω	$I_D = 1\text{A}$, $V_{GS} = 10\text{V}$ ^{Note3}
Forward transfer admittance	$ y_{fs} $	(1.2)	(2.0)	—	S	$I_D = 1\text{A}$, $V_{DS} = 10\text{V}$ ^{Note3}
Input capacitance	C_{iss}	—	(300)	—	pF	$V_{DS} = 25\text{V}$
Output capacitance	C_{oss}	—	(43)	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	(12)	—	pF	$f = 1\text{MHz}$
Turn-on delay time	$t_{d(\text{on})}$	—	(20)	—	ns	$V_{DD} \cong 100\text{V}$, $I_D = 1\text{A}$
Rise time	t_r	—	(10)	—	ns	$V_{GS} = 10\text{V}$
Turn-off delay time	$t_{d(\text{off})}$	—	(50)	—	ns	$R_L = 100\Omega$
Fall time	t_f	—	(10)	—	ns	$R_g = 10\Omega$
Total gate charge	Q_g	—	(10)	—	nC	$V_{DD} = 160\text{V}$
Gate to source charge	Q_{gs}	—	(2)	—	nC	$V_{GS} = 10\text{V}$
Gate to drain charge	Q_{gd}	—	(5)	—	nC	$I_D = 2\text{A}$
Body-drain diode forward voltage	V_{DF}	—	(0.8)	(1.2)	V	$I_F = 2\text{A}$, $V_{GS} = 0$ ^{Note3}
Body-drain diode reverse recovery time	t_{rr}	—	(75)	—	ns	$I_F = 2\text{A}$, $V_{GS} = 0$ $di_F/dt = 100\text{A}/\mu\text{s}$

Note: 3. Pulse test

Package Dimensions

As of January, 2001
Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-8DA
JEDEC	Conforms
EIAJ	—
Mass (reference value)	0.085 g

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