

H5N2005DL, H5N2005DS

Silicon N Channel MOS FET
High Speed Power Switching

HITACHI

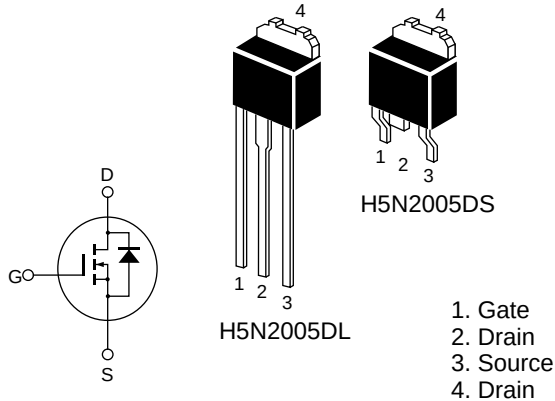
ADE-208-1373 (Z)
Target Specification 1st. Edition
Mar. 2001

Features

- Low on-resistance
- Low drive current
- High speed switching

Outline

DPAK-2



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	200	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	(6)	A
Drain peak current	$I_{D (pulse)}$ ^{Note 1}	(24)	A
Body-drain diode reverse drain current	I_{DR}	(6)	A
Body-drain diode reverse drain peak current	$I_{DR (pulse)}$ ^{Note 1}	(24)	A
Channel dissipation	P_{ch} ^{Note 2}	25	W
Channel to case thermal impedance	θ_{ch-c}	5	°C/W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	–55 to +150	°C

Notes: 1. $PW = 10 \mu s$, duty cycle = 1%2. Value at $T_c = 25^\circ C$

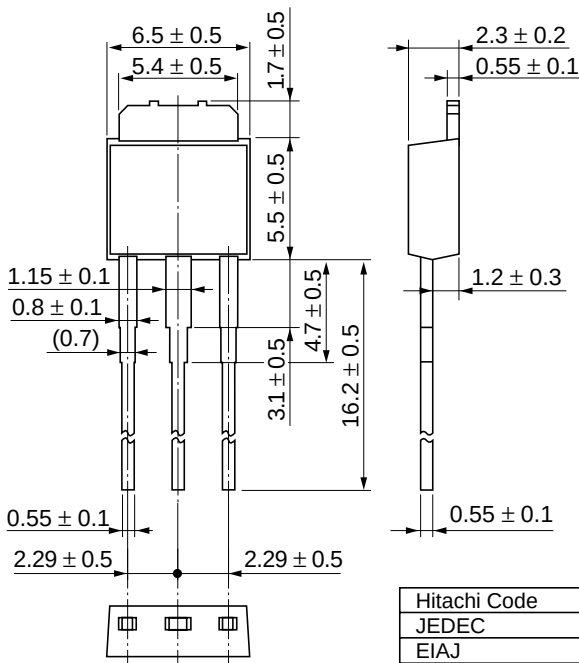
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 200 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	(3.0)	—	(4.5)	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	(0.52)	(0.65)		$I_D = 3 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note 4}
Forward transfer admittance	$ y_{fs} $	(2.0)	(3.4)	—	S	$I_D = 3 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note 4}
Input capacitance	C_{iss}	—	(300)	—	pF	$V_{DS} = 25 \text{ V}$
Output capacitance	C_{oss}	—	(50)	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	(14)	—	pF	$f = 1 \text{ MHz}$
Total Gate charge	Q_g	—	(9.5)	—	nC	$V_{DD} = 160 \text{ V}$
Gate to source charge	Q_{gs}	—	(1.8)	—	nC	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Q_{gd}	—	(5.2)	—	nC	$I_D = 6 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	(19)	—	ns	$I_D = 3 \text{ A}$
Rise time	t_r	—	(16)	—	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	(44)	—	ns	$R_L = 33.3$
Fall time	t_f	—	(12)	—	ns	$R_g = 10$
Body-drain diode forward voltage	V_{DF}	—	(1.0)	(1.5)	V	$I_F = 6 \text{ A}$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	(90)	—	ns	$I_F = 6 \text{ A}$, $V_{GS} = 0$
Body-drain diode reverse recovery charge	Q_{rr}	—	(300)	—	nC	$diF/dt = 100 \text{ A/us}$

Note: 4. Pulse test

Package Dimensions

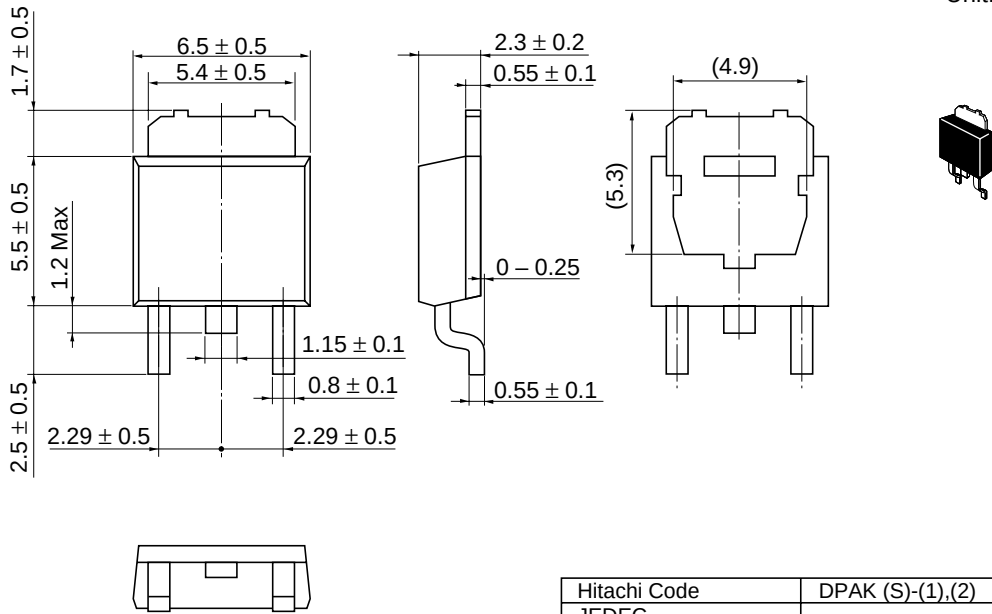
As of January, 2001
Unit: mm



Hitachi Code	DPAK (L)-(2)
JEDEC	—
EIAJ	—
Mass (reference value)	0.42 g

As of January, 2001

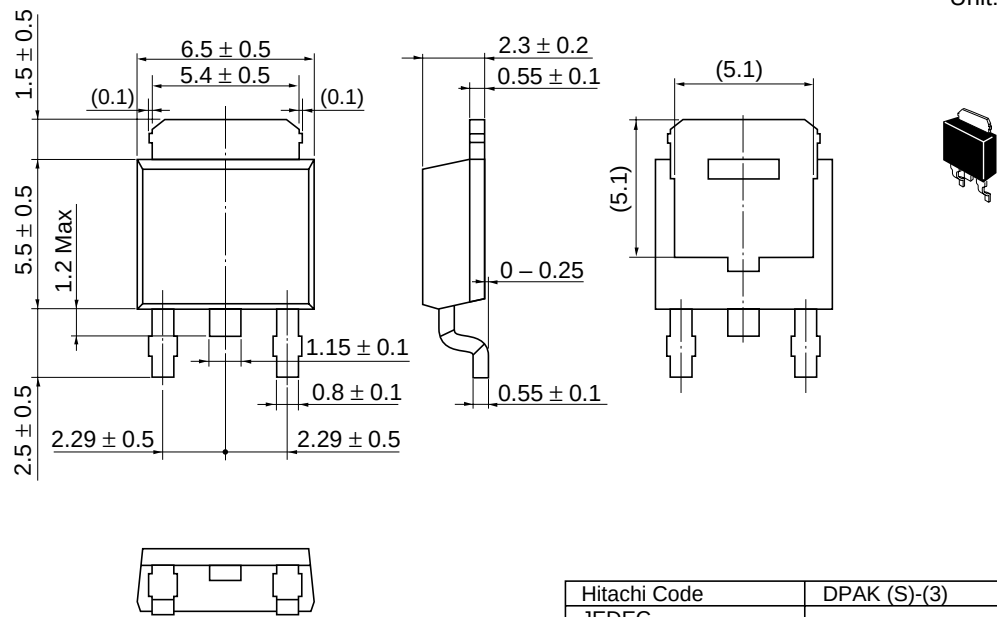
Unit: mm



Hitachi Code	DPAK (S)-(1),(2)
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.28 g

H5N2005DL, H5N2005DS

As of January, 2001
Unit: mm



Hitachi Code	DPAK (S)-(3)
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.28 g

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HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL	NorthAmerica	:	http://semiconductor.hitachi.com/
	Europe	:	http://www.hitachi-eu.com/hel/ecg
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For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe GmbH
Electronic Components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 585160

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00,
Singapore 049318
Tel: <65>-538-6533/538-8577
Fax: <65>-538-6933/538-3877
URL: <http://www.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road,
Hung-Kuo Building,
Taipei (105), Taiwan
Tel: <886>-(2)-2718-3666
Fax: <886>-(2)-2718-8180
Telex: 23222 HAS-TP
URL: <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon,
Hong Kong
Tel: <852>-(2)-735-9218
Fax: <852>-(2)-730-0281
URL: <http://www.hitachi.com.hk>

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