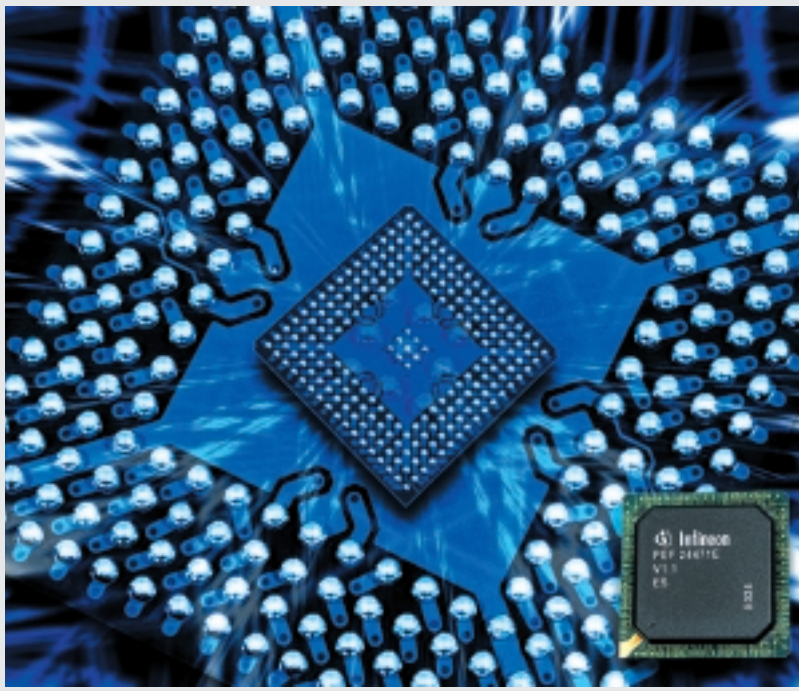
The evolution in switching applications requires new functionalities. Switching in CTI (Computer Telephony Integration) systems, switching of wide-band data or switching in systems supporting voice over IP (VoIP) become more and more important.

The SWITI family offers with its HTSI devices complete and cost-efficient solutions to cover all needs of modern switching applications.

It provides a full feature H.100/H.110 interface and features like programmable minimum or constant delay to support voice switching as well as wide-band data switching.

It offers subchannel switching for applications requiring 1, 2 and 4-Bit switching (e.g. mobile base stations).

The HTSI devices offer switching capacities of 512, 1024 and 2048 time-slots. They are fully compatible with respect to package, pinning and programming. Thus they allow easy performance adaptations just by selecting a device with higher or lower switching capacity.



SWITI

Applications

- H.100/H.110 [1] interface in
  - Computer telephony systems
  - Multi service access platforms
  - Gateways
  - Integrated access devices
- TDM switch, concentrator or multiplexer with up to 32 local I/Os in all kind of telecommunication systems (e.g. PBXs, COs or mobile base stations) handling data rates up to 16 Mbit/s per stream.

[1] H.100/H.110 is also downward compatible to SC-Bus™, MVIP-90™ and H-MVIP™

Features

- Fully compliant H.100/H.110 interface
- Switching capacity of 512, 1024 or up to 2048 time-slots
- Programmable data rates on a per line basis
- 16 local I/Os and 32 bidirectional H.100/H.110 lines in H-mode
- Maximum 32 local I/Os in M-mode (no support of H-bus)
- Minimum or constant delay
- 1, 2, 4-Bit subchannel switching
- Broadcast capabilities
- Multipoint switching
- 8 channel stream to stream switching interoperable to SC-Bus™, MVIP-90™ and H-MVIP™ devices
- Optional 8-Bit parallel input and 8-Bit parallel output for local bus
- Data rates of 2, 4, 8 and 16 Mbit/s with automatic data rate adaption
- GPIO ports
- Full read and write access to all time-slots
- 8/16-Bit µP-Interface in Intel/Infineon and Motorola mode
- On-chip PLL
- JTAG IEEE1149.1
- 3.3 V local I/O with 5 V tolerant inputs and TTL-compatible outputs
- P-BGA-217 package

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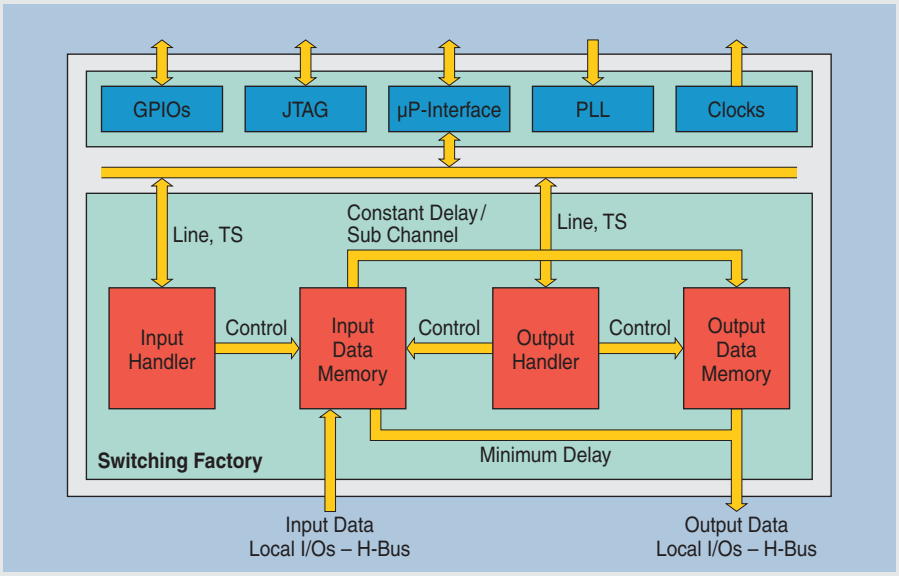
SWITI

H.100/H.110 Time-Slot Interchangers
HTSI PEF 20451
HTSI-L PEF 20471
HTSI-XL PEF 24471

Infineon technologies

Never stop thinking.

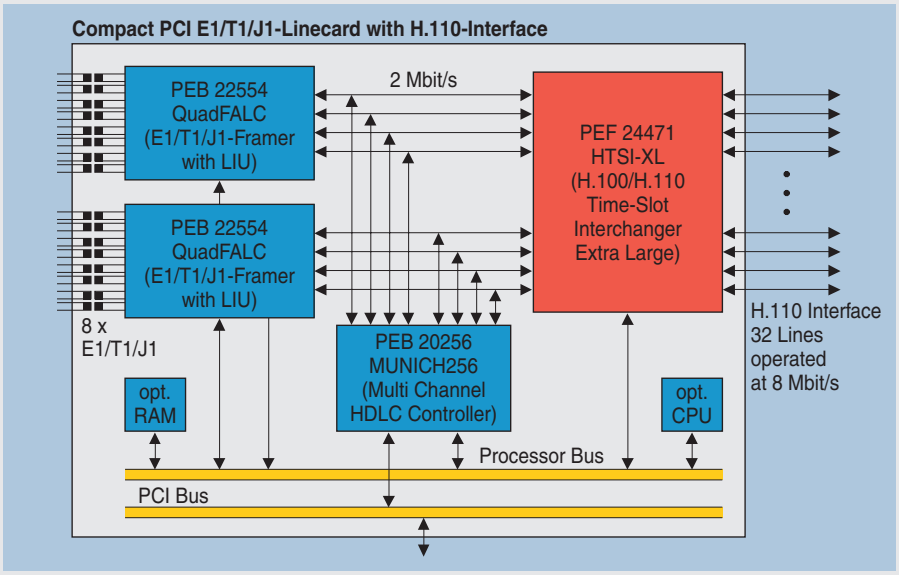


HTSI Block Diagram

The consistent architecture of the SWITI family offers the highest grade of flexibility. Switching performance adaptations in the system can be done only by choosing a device with higher or lower switching capacity.

Beneath the HTSI devices there are also three standard MTSI (Memory Time-Slot Interchangers) devices part of the SWITI family, which can be programmed in the same way as the HTSI devices. Thus all devices of the SWITI family can be operated using one standard SW driver.

Type	Sales Code	Package
HTSI	PEF 20451	P-BGA-217
HTSI-L	PEF 20471	P-BGA-217
HTSI-XL	PEF 24471	P-BGA-217
HTSI-S	Product Idea	P-BGA-217



HTSI Application Example

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