
HD29468

Triple Line Drivers/Receivers

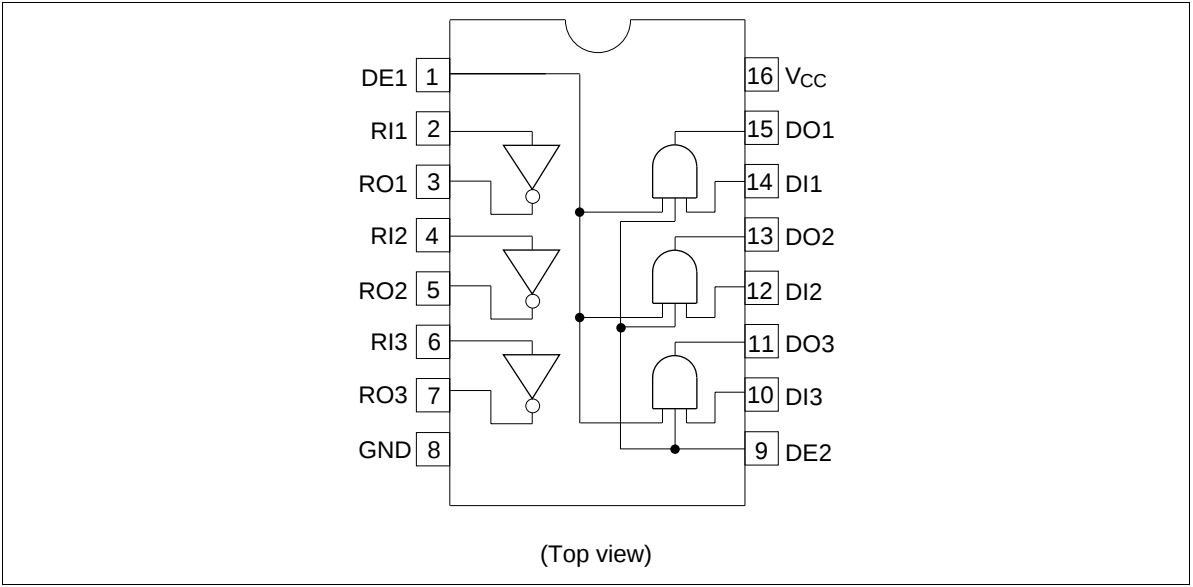
HITACHI

ADE-205-583 (Z)
1st. Edition
Dec. 2000

Description

The HD29468 features line drivers and receivers for unbalanced transmissions, which meet the specs of IBM 360 and 370. This device has three drivers and receivers in one package. Input of driver and output of receiver are compatible with low power schottky TTL circuit and operates from a single 5 V power supply. The driver has two types of enable inputs. Spurious noise can be prevented by grounding either input when power supply is thrown or cut off. The outputs are protected from short circuit and the wired logic is available due to emitter follower from for parity line data bus applications. The device operates at high speed. Low to high level and high to low level propagation delay times difference are 10 ns max.

Pin Arrangement



Function Table

Driver				Receiver	
Input			Output	Input	Output
DI	DE1	DE2	DO	RI	RO
L	X	X	L	L	H
X	L	X	L	H	L
X	X	L	L		
H	H	H	H		

H : High level
L : Low level
X : Immaterial

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply Voltage	V_{CC}	+7	V
Driver Input Voltage	V_{ID}	-0.5 to +7	V
Driver Output Voltage	V_{OD}	-0.5 to +7	V
Receiver Input Voltage	V_{IR}	-0.5 to +7	V
Power Dissipation ($T_a = 25^{\circ}\text{C}$)* ¹	DP	1000	mW
	FP	785	
Operating Temperature	T_a	0 to +75	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-65 to +150	$^{\circ}\text{C}$

Notes: 1. The above data were taken by the ΔV_{BE} method, mounting on a glass epoxy board ($40 \times 40 \times 1.6$ mm) of 10% wiring density.

2. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	V_{CC}	4.75	5.00	5.25	V
Operating Temperature	T_a	0	—	75	$^{\circ}\text{C}$

Electrical Characteristics

Driver (V_{CC} = 5.0 V ± 5 %, Ta = 0 to +75°C)

Item	Symbol	Min	Max	Unit	Conditions
High Level Input Voltage	V _{IH}	2.0	—	V	
Low Level Input Voltage	V _{IL}	—	0.8	V	
Input Clamp Voltage	V _{IK}	—	−1.5	V	V _{CC} = 4.75 V, I _{IN} = −18 mA
High Level Output Voltage	V _{OH}	3.11	—	V	V _{CC} = 4.75 V, V _{IH} = 2.0 V I _{OH} = −59.3 mA (Ta = 25°C)
		—	4.1		V _{CC} = 5.25 V, V _{IH} = 2.0 V I _{OH} = −78.1 mA
Low Level Output Voltage	V _{OL}	—	0.15	V	V _{CC} = 5.25 V, V _{IL} = 0.8 V I _{OL} = −0.24 mA, V _{IH} = 4.5 V
High Level Input Current	DI I _{IH}	—	20	μA	V _{CC} = 5.25 V, V _{IH} = 2.7 V
	DE	—	60		V _{CC} = 5.25 V, V _{IH} = 2.7 V
Low Level Input Current	DI I _{IL}	—	−400	μA	V _{CC} = 5.25 V, V _{IL} = 0.4 V
	DE	—	−1200		V _{CC} = 5.25 V, V _{IL} = 0.4 V
High Level Output Current	I _{OH}	—	100	μA	V _{CC} = 4.75 V, V _{IL} = 0 V, V _{OH} = 5.0 V
		—	100		V _{CC} = 4.75 V, V _{IH} = 4.5 V, V _{OH} = 5.0 V
Short Circuit Output Current	I _{OS}	—	−30	mA	V _{CC} = 5.25 V, V _{IH} = 4.5 V

Receiver (Ta = 0 to +75°C)

Item	Symbol	Min	Max	Unit	Conditions
High Level Output Threshold Voltage	V _{OTH}	2.7	—	V	V _{CC} = 4.75 V, V _{IL} = 1.15 V I _{OH} = −400 μA
Low Level Output Threshold Voltage	V _{OTL}	—	0.5	V	V _{CC} = 5.25 V, V _{IH} = 1.55 V I _{OL} = 8 mA
High Level Output Voltage	V _{OH}	2.7	—	V	V _{CC} = 4.75 V, V _{IN} : Open I _{OH} = −400 μA
Low Level Output Voltage	V _{OL}	—	0.5	V	V _{CC} = 4.75 V I _{OL} = 8 mA
		—	0.4		V _{IH} = 1.55 V I _{OL} = 4 mA
Input Resistance	R _{IN}	7.4	0	KΩ	V _{CC} = 0 V
High Level Input Current	I _{IH}	—	0.42	mA	V _{CC} = 4.75 V, V _{IH} = 3.11 V
Low Level Input Current	I _{IL}	0.04	−0.24	mA	V _{CC} = 5.25 V, V _{IL} = 0.15 V
Short Circuit Output Current	I _{OS}	−20	−100	mA	V _{CC} = 5.25 V, V _{IL} = 0 V

Driver/Receiver ($T_a = 0$ to $+75^{\circ}\text{C}$)

Item	Symbol	Min	Max	Unit	Conditions
Supply Voltage	I_{CCH}	—	37	mA	$V_{\text{CC}} = 5.25 \text{ V}$, $V_{\text{IH}} = 4.5 \text{ V}$
	I_{CCL}	—	55		$V_{\text{CC}} = 5.25 \text{ V}$, $V_{\text{IL}} = 0 \text{ V}$

Switching Characteristics**Driver** ($V_{\text{CC}} = 5.0 \text{ V}$, $T_a = 25^{\circ}\text{C}$)

Item	Symbol	Min	Max	Unit	Conditions
Rise Propagation Delay Time	t_{PLH}	6.5	18.5	ns	$R_L = 47.5 \Omega$
Fall Propagation Delay Time	t_{PHL}	6.5	18.5	ns	
Propagation Delay Time Difference* ¹	Δt_{PD}	—	10	ns	

Note: 1. $\Delta t_{\text{PD}} = |t_{\text{PLH}} - t_{\text{PHL}}|$

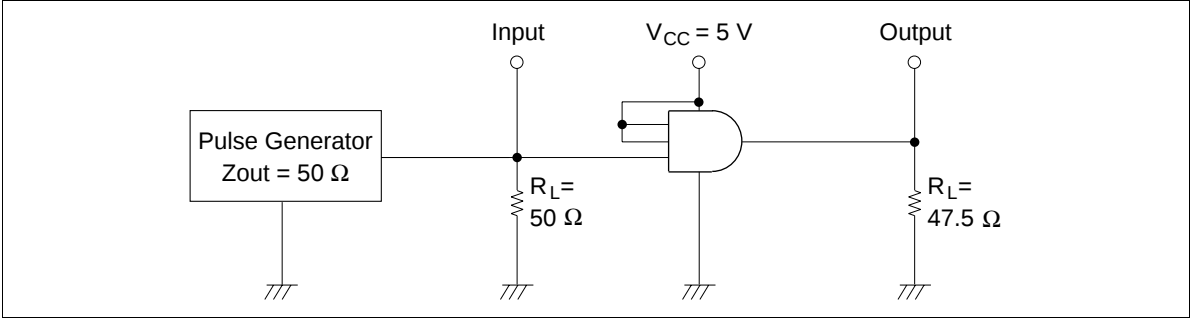
Receiver ($V_{\text{CC}} = 5.0 \text{ V}$, $T_a = 25^{\circ}\text{C}$)

Item	Symbol	Min	Max	Unit	Conditions
Rise Propagation Delay Time	t_{PLH}	7.5	19.5	ns	$R_L = 2 \text{ K}\Omega$, $C_L = 15\text{pF}$
Fall Propagation Delay Time	t_{PHL}	7.5	19.5	ns	
Propagation Delay Time Difference* ¹	Δt_{PD}	10	ns		$R_L = 2 \text{ K}\Omega$, $C_L = 15\text{pF}$

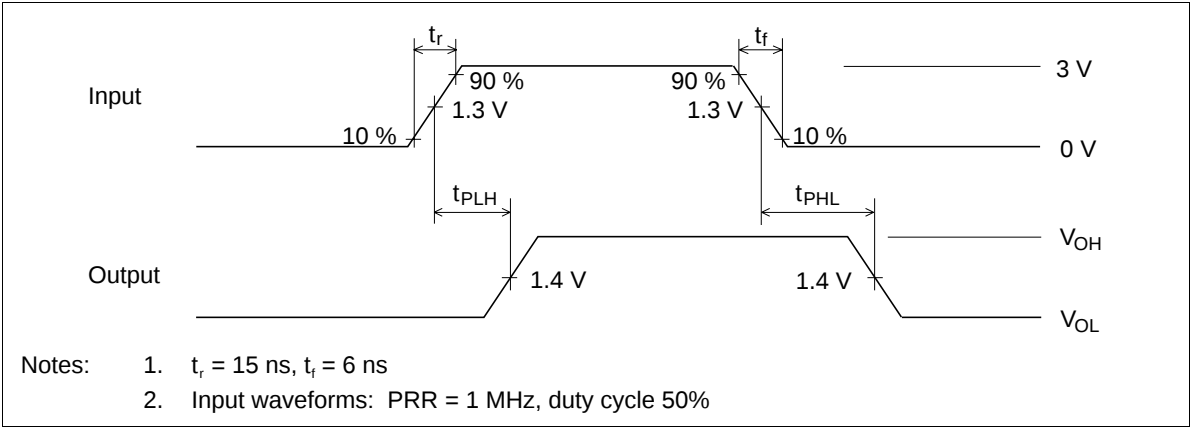
Note: 1. $\Delta t_{\text{PD}} = |t_{\text{PLH}} - t_{\text{PHL}}|$

Driver

Test Circuit

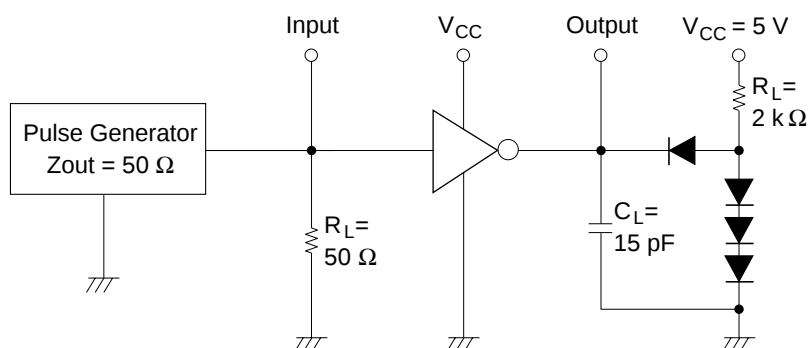


Waveforms



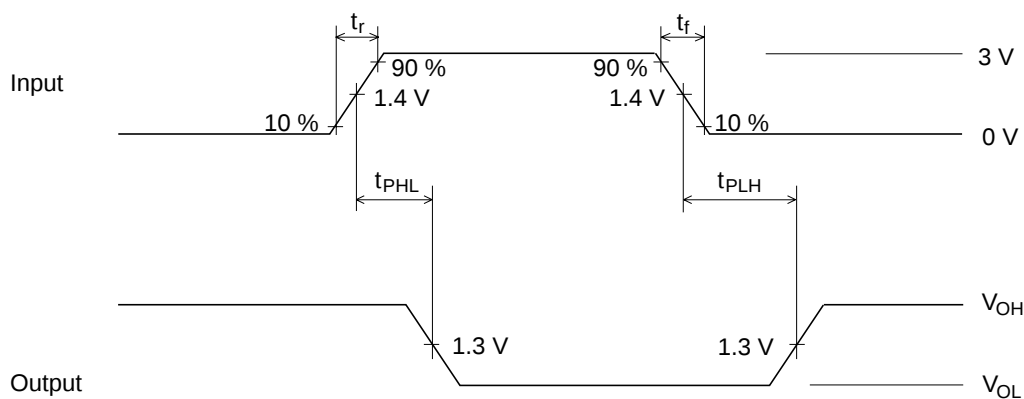
Driver

Test Circuit



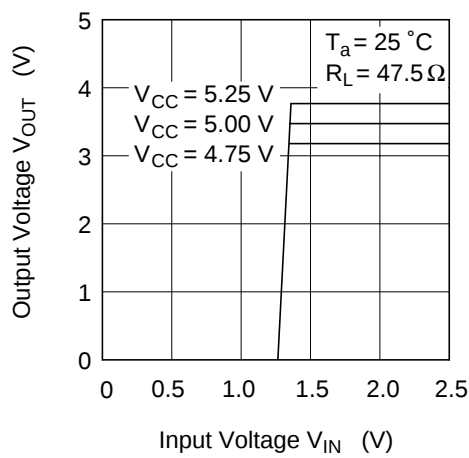
Note: 1. C_L includes probe and jig capacitance.

Waveforms

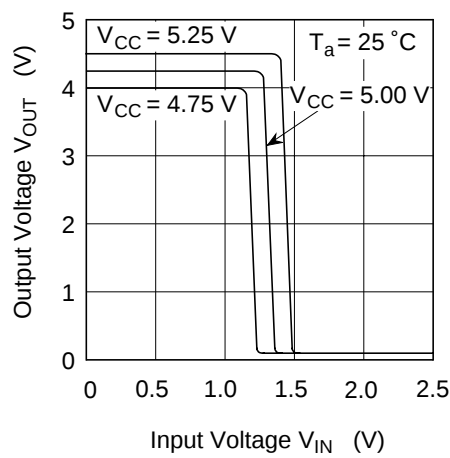


Notes: 1. $t_r = t_f = 10\ \text{ns}$
 2. Input waveforms: PRR = 1 MHz, duty cycle 50%

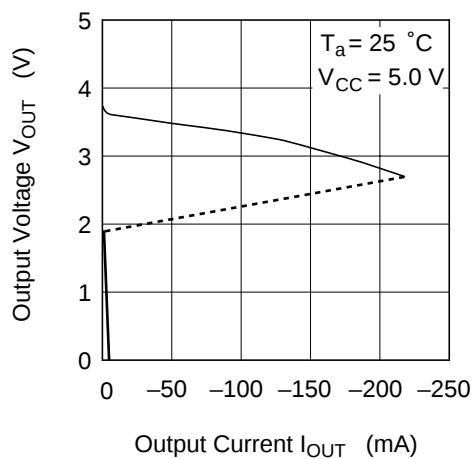
Driver $V_{IN} - V_{OUT}$



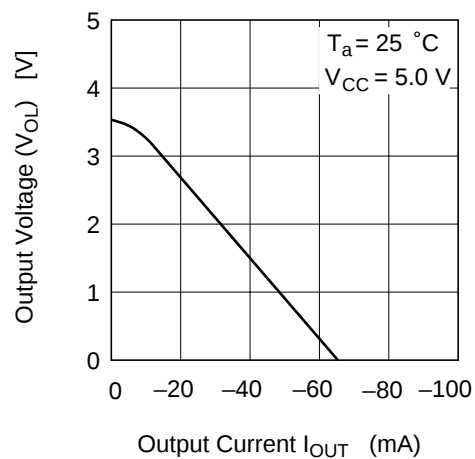
Receiver $V_{IN} - V_{OUT}$



Driver $I_{OUT} - V_{OUT}$

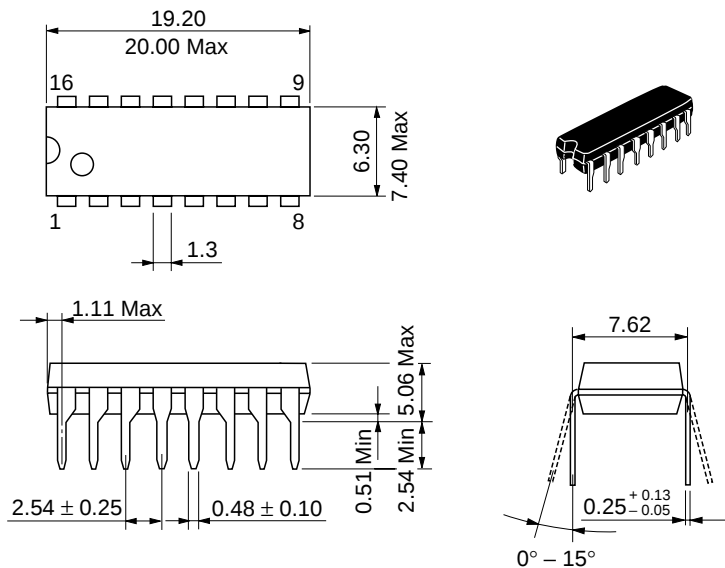


Receiver $I_{OUT} - V_{OUT}$



Package Dimensions

Unit: mm



Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	1.07 g

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