

FEATURES

- **direct coupled telecoil preamplifier**
- **natural low frequency rolloff to reduce 50/60 Hz hum pickup**
- **low current consumption (typ 65 μ A)**
- **34 dB maximum gain**

STANDARD PACKAGING

- Chip (28 x 56 mils)
Au Bump

DESCRIPTION

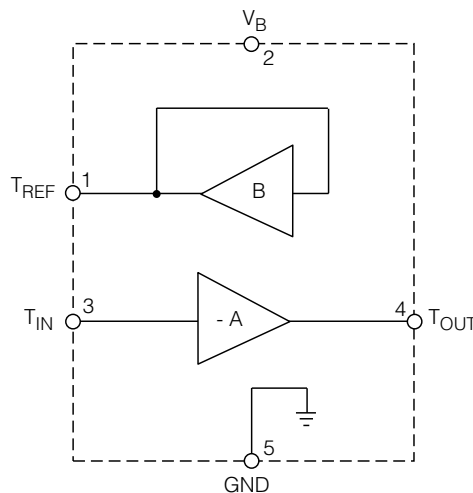
The GC562 was designed to allow direct coupling of the telecoil onto the preamplifier without the need of a capacitor.

The DC bias voltage at the output of the unity gain, inverting preamplifier B, precisely matches that of the input of preamp A. When the telecoil is placed between these two pins, no DC current will flow through the coil as the potential at both ends is equal. With stage B configured as unity gain, its output appears as a virtual ground to AC signals. The lower -3 dB corner frequency of the amplifier is set by the ratio of the telecoil resistance to the inductance, given by the equation:

$$f_L = \frac{R_{COIL}}{(2\pi L_{COIL})}$$

With a gain set resistor from the input to output of preamplifier A, a signal inductively coupled into the telecoil via the telephone will produce a signal current to flow through the resistor, thereby producing a voltage at the output of the amplifier.

For stability reasons it is recommended that the telecoil amplifier not be configured for more than 34 dB gain, or with a feedback resistor larger than 100 k Ω .



FUNCTIONAL BLOCK DIAGRAM

ABSOLUTE MAXIMUM RATINGS

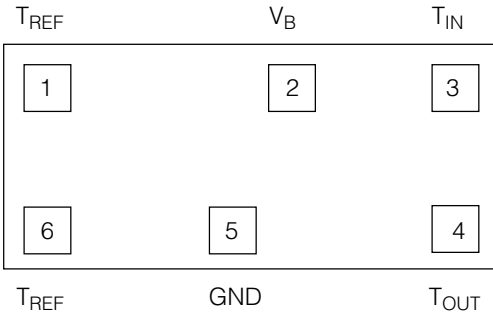
PARAMETER	VALUE / UNITS
Supply Voltage	5 VDC
Power Dissipation	25 mW
Operating Temperature Range	-10° C to 40° C
Storage Temperature Range	-20° C to 70° C

CAUTION

CLASS 1 ESD SENSITIVITY



CHIP PIN CONNECTION



ELECTRICAL CHARACTERISTICS

Conditions: Frequency = 1 kHz, Temperature = 25°C, Supply Voltage V_B = 1.3 V

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC SPECIFICATIONS						
Amplifier Current	I _{AMP}		40	65	100	μA
Telecoil Offset Voltage	T _{VOFF}	V _{P1} - V _{P3}	-4	0	4	mV
Input Bias Current	I _{BIAS}		10	100	200	nA
Stage A Source Current	I _{SRC_A}		15	30	-	μA
Stage B Source Current	I _{SRC_B}		7	15	-	μA
Output Voltage Swing-Low (Stage A)	V _{OL}		50	300	-	mV
AC SPECIFICATIONS						
T _{REF} Output Impedance	Z _{OUT}	at < 5kHz	-	200	-	Ω
Input Referred Noise	IRN	NFB 0.2 to 10kHz at 12dB/oct	-	1.5	-	μVRMS

All parameters and switches remain as shown in the Test Circuit unless otherwise stated in CONDITIONS column

V_{PX} - actual voltage measured on the pin at given condition (X is pin number).

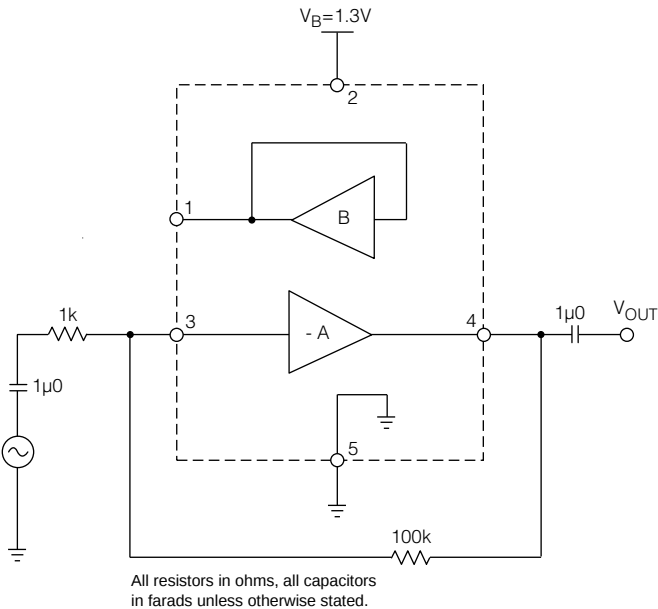


Fig. 1 Test Circuit

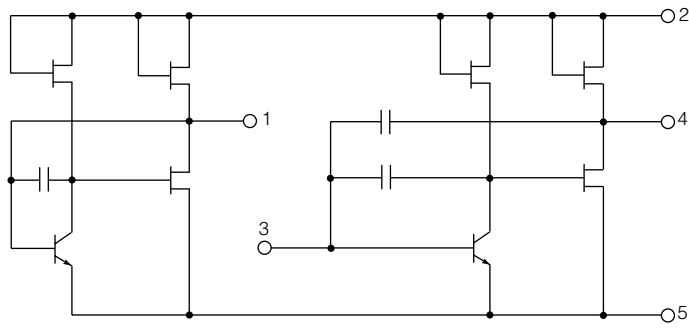


Fig. 2 Functional Schematic Diagram

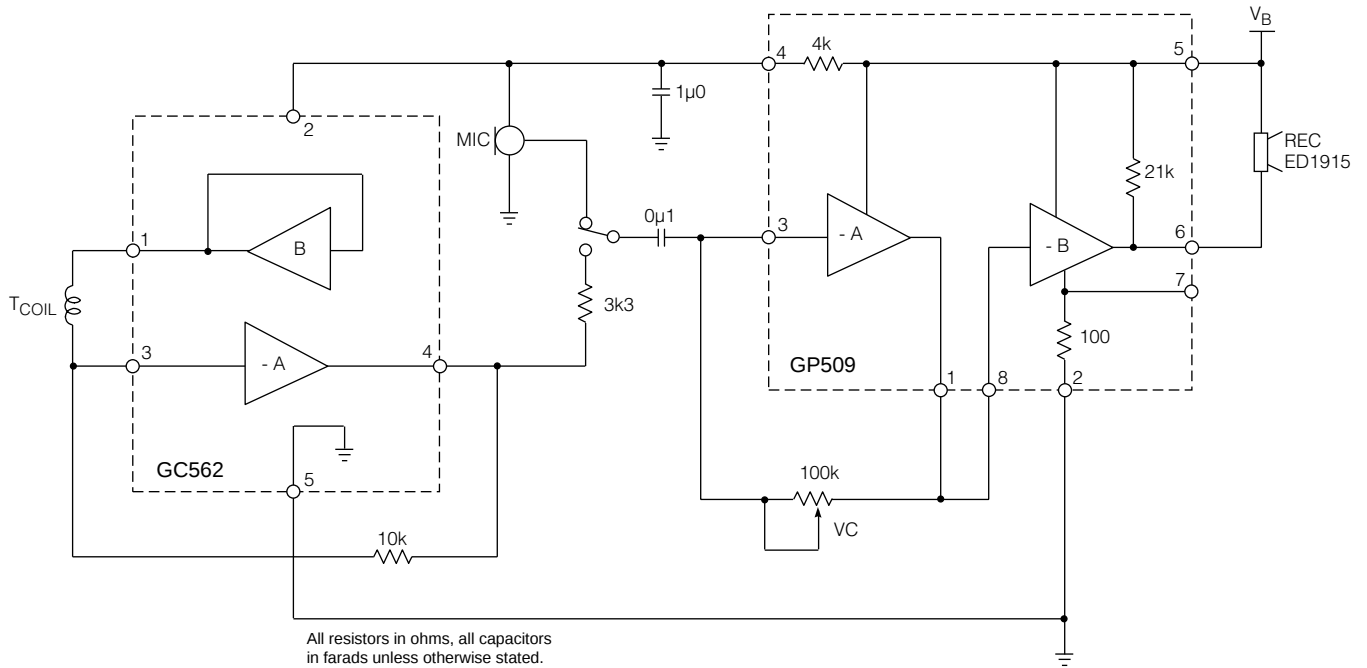


Fig. 3 Typical Hearing Instrument Application

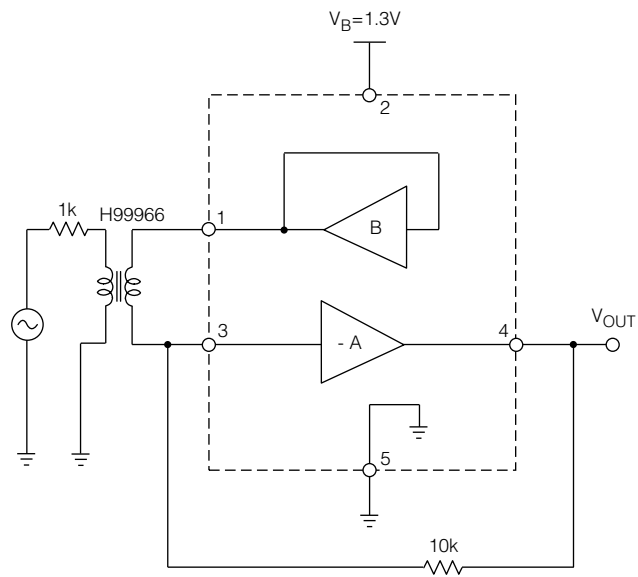


Fig. 4 Characterization Circuit

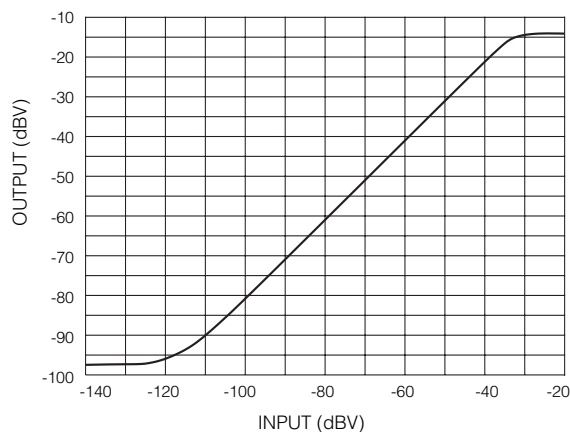


Fig. 5 I/O Curve

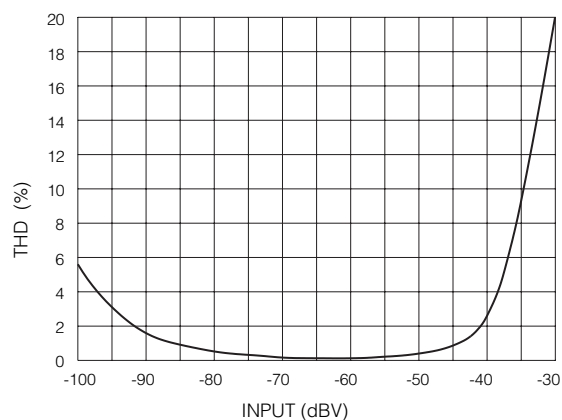


Fig. 7 THD vs Input

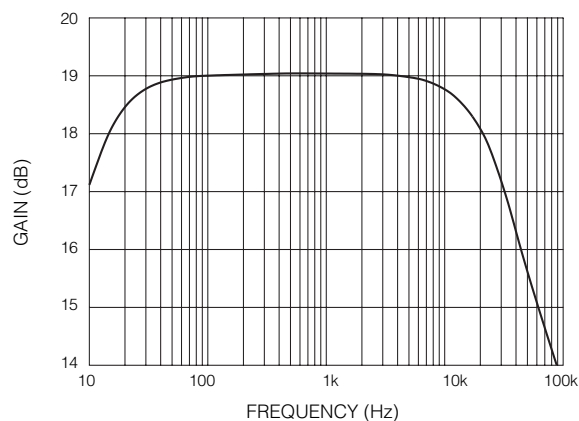


Fig. 6 Frequency Response

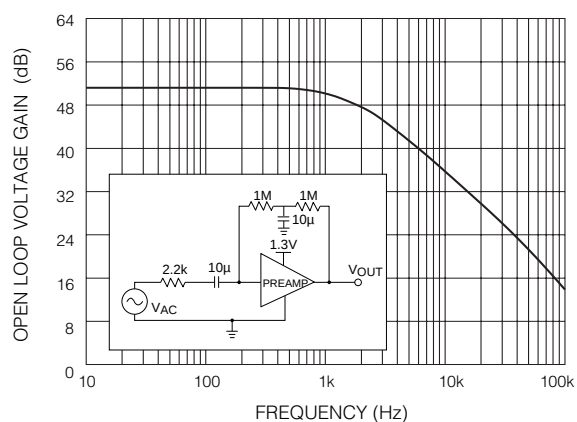
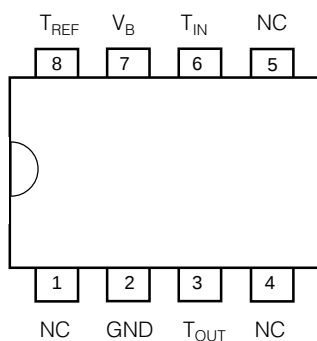


Fig. 8 Stage A Open Loop Voltage Gain

8 PIN DIP PINOUT

(For Evaluation Purposes)



GENNUM CORPORATION

MAILING ADDRESS:

P.O. Box 489, Stn. A, Burlington, Ontario, Canada L7R 3Y3
Tel. +1 (905) 632-2996 Fax +1 (905) 632-2814

SHIPPING ADDRESS:

970 Fraser Drive, Burlington, Ontario, Canada L7L 5P5

GENNUM JAPAN CORPORATION

C-101, Miyamae Village, 2-10-42 Miyamae, Suginami-ku, Tokyo 168-0081,
Japan Tel. +81 (3) 3334-7700 Fax: +81 (3) 3247-8839

DOCUMENT IDENTIFICATION: DATA SHEET

The product is in production. Gennum reserves the right to make changes at any time to improve reliability, function or design, in order to provide the best product possible.

REVISION NOTES:

Added Au Bump.