CMOS ASYNCHRONOUS COMMUNICATIONS INTERFACE ADAPTER

Features

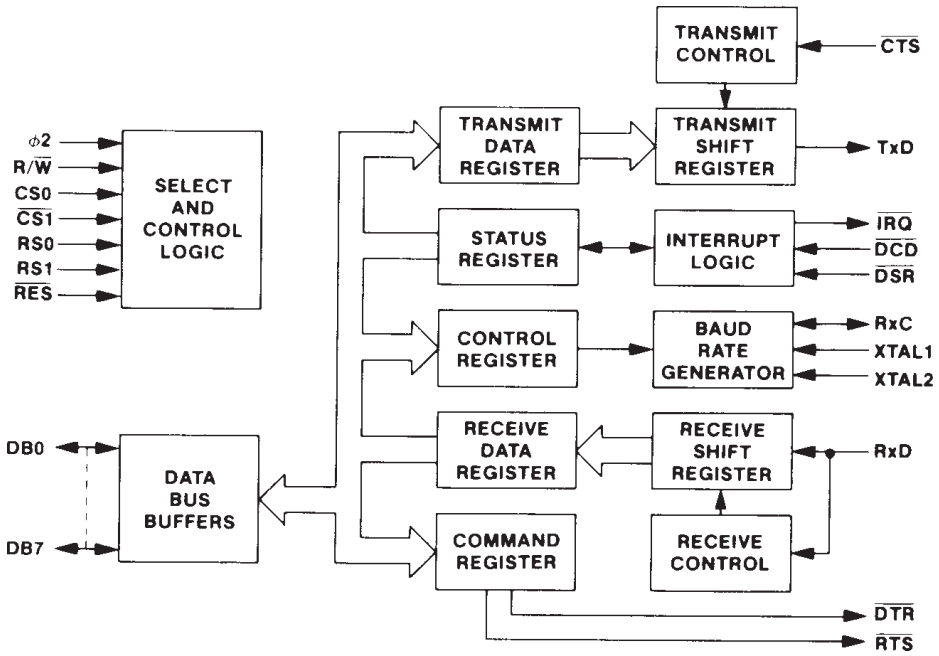
- CMOS process technology for low power consumption
- 15 programmable baud rates (50 to 19,200 baud)
- External 16X clock input for nonstandard baud rates to 125,000 baud
- Programmable interrupt and status registers
- Full-duplex or half-duplex operation modes
- Selectable 5, 6, 7, 8, or 9 bit transmission rates
- Programmable word length, parity generation and detection, and number of stop bits
- Programmable parity options — odd, even, none, mark or space
- Includes data set and modem control signals
- False start bit detection
- Serial echo mode
- Four operating frequencies - 1, 2, 3, and 4 MHz
- 28-pin DIP, 28-pin PLCC

Contact factory for complete data sheet.

Product Description

The CMD G65SC51 is an Asynchronous Communications Interface Adapter which offers many versatile features for interfacing 6500/6800 microprocessors to serial communication data sets and modems. The G65SC51's most significant feature is its internal baud rate generator, allowing programmable baud rate selection from 50 to 19,200 baud. This full range of baud rates is derived from a single standard 1.8432 MHz external crystal. For non-standard baud rates up to 125,000 baud, an external 16X clock input is provided. In addition to its powerful communications control features, the G65SC51 offers the advantages of CMD's leading edge CMOS technology, i.e., increased noise immunity, higher reliability, and greatly reduced power consumption.

Block Diagram



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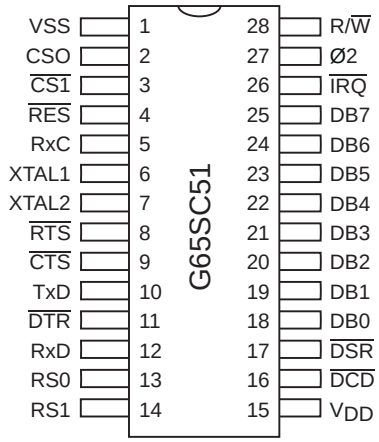
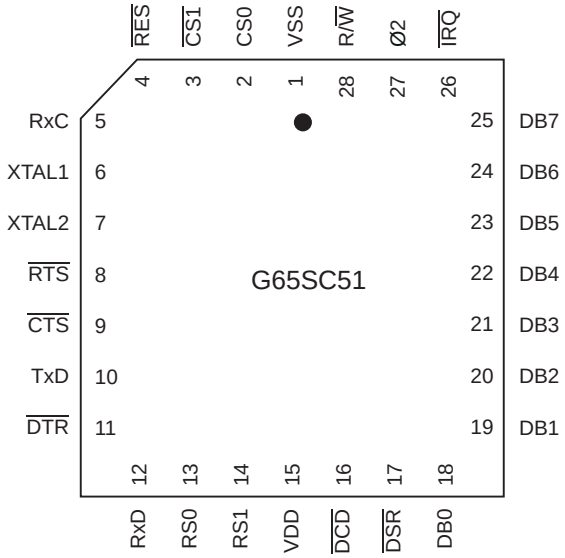
Pin Assignments





Ordering Information

Example: **G65SC51** **P** **I** **-2**

Product Identification Number

Package
- P — Plastic DIP (28)
- PE — PLCC (28)

Temperature/Processing
- I — -40°C to +85°C

Performance Designator
Designators selected for speed and power specifications
- —1 1MHz
- —2 2MHz
- —3 3MHz
- —4 4MHz