

Titanium™ Plus PCI



ADSL CPE Integrated PCI

G7000 Family

The highly integrated G7000 PCI ADSL chip set offers OEMs a complete high speed and full rate two-chip solution for ADSL PCI customer premises applications. This cost effective solution is based on GlobespanVirata's fully programmable multi-mode chip set that supports all standard compliant versions of ADSL including ITU G.992.1 (G.DMT), ITU G.992.2 (G.Lite), ITU G.992 Annex A, Annex B, Annex C (FBM and DBM), and ANSI T1.413 Issue 2.

A fully programmable solution ensures upgradeability to future versions of ADSL without hardware modifications and delivers complete interoperability with other chip sets and system vendors. The digital chip integrates a PCI 2.2 compliant interface and Digital Signal Processor (DSP). The analog chip includes a fully integrated Analog Front End (AFE). The combined integration in this two-chip solution significantly reduces the overall system cost. The G7000 PCI chip set provides industry-leading performance with data rates up to 10.5 Mb/s high-speed or 8 Mb/s full rate downstream and up to 1 Mb/s upstream. The multiple line code capability provides a single ADSL solution for Service Providers regardless of regional DSLAM deployments.

As a comprehensive ADSL solutions provider, GlobespanVirata offers detailed turn key reference designs, chip set data sheets, complete hardware specifications, schematics, Gerber files, Microsoft® Windows®, and Linux software drivers. In addition, GlobespanVirata offers dedicated technical support to ensure ease of design and early time to market.



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* Chips as depicted are for representation only.
Actual chips may vary from depiction.

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Features and Benefits

- High speed and full rate DMT ADSL solution
- Data rates up to 10.5 Mb/s high speed, 8 Mb/s full rate, 4 Mb/s for G.Lite downstream and up to 1 Mb/s upstream
- Fully integrated PCI 2.2 compliant interface and DSP
- Highly integrated AFE
- Standards compliant: ITU G.992.1 (G.DMT), ITU G.992.2 (G.Lite), ITU G.992 Annex A, Annex B, Annex C (FBM and DBM), and ANSI T1.413 Issue 2
- Support for PPP over ATM, PPP over Ethernet, Ethernet over ATM, and IP over ATM
- Support for Microsoft® Windows® 95, 98, Millennium Edition (ME), 2000, XP, NT, and Linux operating systems
- Interoperable with other ADSL chip sets and DSLAM system vendors
- Supports splitterless consumer installable DSL (CiDSL™) (Splitter required for Annex B)
- Tested for: CSA, CE, FCC Part 68 and part 15 Class B, UL1950, CISPR Class B, EN60950, VCCI Class B
- Visual LED indicates network status
- Turn-key PCI reference design



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