TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

# GT80J101A

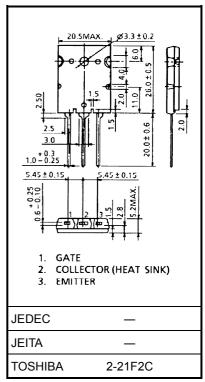
## **High Power Switching Applications**

Unit: mm

- Enhancement-Mode
- High Speed:  $t_f = 0.40 \mu s \text{ (max) (IC} = 80 \text{ A)}$
- Low Saturation Voltage: VCE (sat) = 3.0 V (max) (IC = 80 A)

### **Maximum Ratings (Ta = 25°C)**

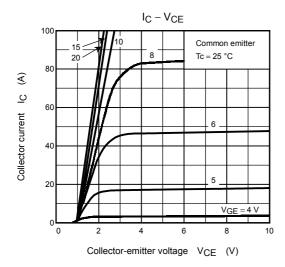
Characteristic	Symbol	Rating	Unit		
Collector-emitter voltage	V <sub>CES</sub>	600	V		
Gate-emitter voltage		V <sub>GES</sub>	±20	V	
Collector current	DC	IC	80	А	
	1ms	I <sub>CP</sub>	160		
Collector power dissipation (Tc = 25°C)		PC	200	W	
Junction temperature		Tj	150	°C	
Storage temperature		T <sub>stg</sub>	-55~150	°C	
Screw torque		_	0.8	N⋅m	

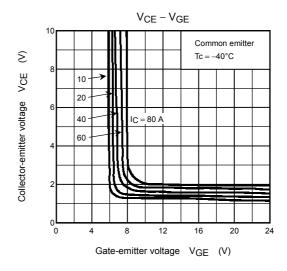


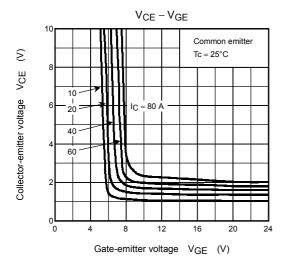
#### Weight: 9.75 g (typ.)

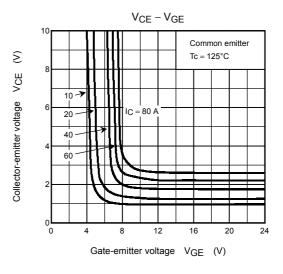
## **Electrical Characteristics (Ta = 25°C)**

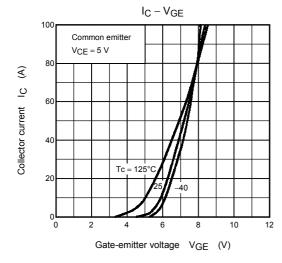
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I <sub>GES</sub>	$V_{GE} = \pm 25 \text{ V}, V_{CE} = 0$	_	_	±500	nA	
Collector cut-off current		I <sub>CES</sub>	V <sub>CE</sub> = 600 V, V <sub>GE</sub> = 0	_	_	1.0	mA	
Gate-emitter cut-off voltage		V <sub>GE</sub> (OFF)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 80 mA	3.0	_	6.0	V	
Collector-emitter saturation voltage		V <sub>CE</sub> (sat) (1)	$I_C = 10 \text{ A}, V_{GE} = 15 \text{ V}$	_	_	2.0	V	
		V <sub>CE</sub> (sat) (2)	$I_C = 80 \text{ A}, V_{GE} = 15 \text{ V}$	_	2.4	3.0	, v	
Input capacitance		C <sub>ies</sub>	$V_{CE} = 10 \text{ V}, V_{GE} = 0, f = 1 \text{ MHz}$	_	5500	_	pF	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 33 \Omega \\ V_{\text{IN}} & & & & & \\ \hline 15 V & & & & \\ \hline -15 V & & & & \\ \end{array}$	_	0.3	0.6	- μs	
	Turn-on time	t <sub>on</sub>		_	0.5	0.8		
	Fall time	t <sub>f</sub>		_	0.25	0.40		
	Turn-off time	t <sub>off</sub>		_	0.7	1.0		
Thermal resistance		R <sub>th (j-c)</sub>	_	_	_	0.625	°C/W	

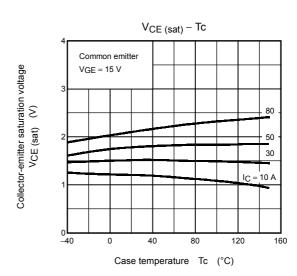




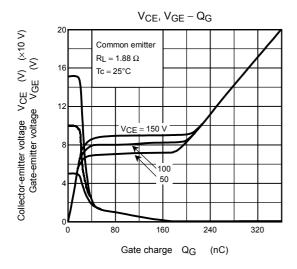


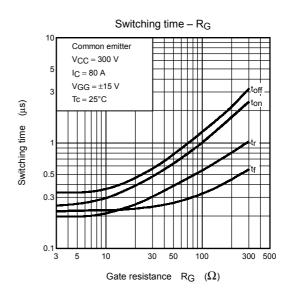


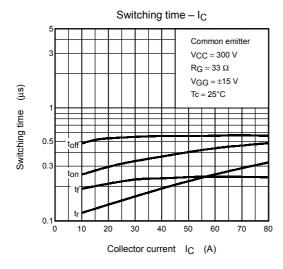


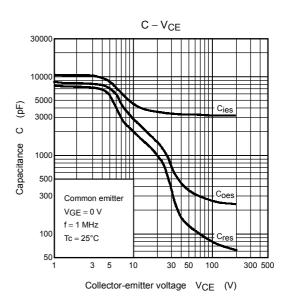


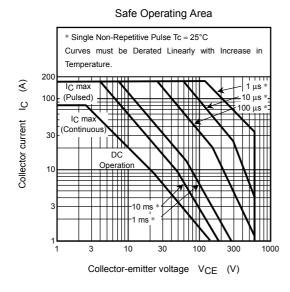
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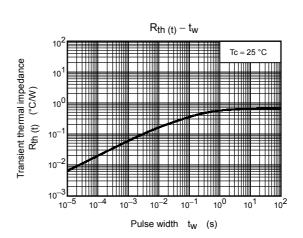




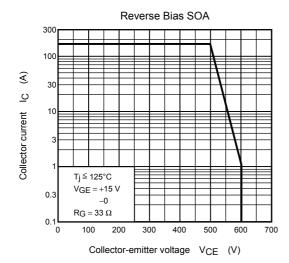








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