

Data Sheet No. GSDS50018 & GSDS50020

### **Generic Part Number:**

GSDS50018 & GSDS50020

**Request Quotation** 

# High Power NPN Transistors 180 V & 200 V, 50 Amp Switching TO-3 (TO-2004AE) Case

#### Features:

- High Voltage
- High Gain
- High Current
- Low Saturation Voltages
- Fast Switching
- Radiation Resistant

### **Applications:**

- High Speed Switching
- Power Conversion
- Converters
- Inverters
- Class D Amplifiers
- Class C Amplifiers

emicoa's GSD series is a family of NPN silicon transistors designed for high speed switching systems. This unique series utilizes technology that provides surface stabilization for high voltage operation and enhances long term reliability. A design feature is the use of an interdigital emitter providing a periphery greater than 7.0 inches. This improves both the gain and current handling capability.

These transistors have been specifically designed and engineered for high speed, high voltage switching applications where the designer is concerned with optimizing power conversion efficiency.



## **Maximum Ratings**

Ratings	Symbol	GSDS 50018	GSDS 50020	Unit
Collector-Base Voltage	$V_{CBO}$	180	200	V
Emitter-Base Voltage	V <sub>EBO</sub>	7	7	V
Collector Current Continuous	I <sub>C</sub>	50	50	А
Collector Current Peak	I <sub>CM</sub>	75	75	Α
Base Current Continuous	I <sub>B</sub>	20	20	А
Total Power Dissipation @ T <sub>C</sub> = 25°C	P <sub>D</sub>	100	100	W
Junction to Case Thermal Resistance	R <sub>J-C</sub>	1	1	°C/W
Operating Junction Temperature	T <sub>J(oper)</sub>	-65 to +200	-65 to +200	°C
Storage Temperature	T <sub>(stg)</sub>	-65 to +200	-65 to +200	°C

### **Electrical Characteristics**

 $T_C = 25^{\circ}C$  unless otherwise specified

GSDS50018 GSDS50020								
Test Conditions	Symbol	Min	Max	Min	Max	Unit		
I <sub>C</sub> = 1.0 mA	V <sub>CBO</sub>	180		200		V		
$I_C = 50 \text{ mA}$	$V_{CEO}$	180		200		V		
I <sub>E</sub> = 1.0 mA	$V_{EBO}$	7		7		V		
I <sub>E</sub> = V <sub>EB</sub> = 6.0 V	I <sub>EBO</sub>		100		100	μA		
V <sub>CE</sub> = 80% Rated	I <sub>CEO</sub>		50		50	μA		
$V_{CE} = 80\%$ Rated, $V_{BE} = -1.5$ V	I <sub>CEX</sub>		10		10	μA		
$L = 50 \mu H$ , $V_{BE(off)} = -1 V R_{BB} = 47 Ohms$	E <sub>S/B</sub>	750		750		μJ		
$V_{CE} = 4.0 \text{ V}, I_{C} = 50 \text{ mA}$	h <sub>FE</sub>	8		8				
I <sub>C</sub> = 50 A, I <sub>B</sub> = 10 A	V <sub>CE(sat)</sub>		1		1	V		
I <sub>C</sub> = 50 A, I <sub>B</sub> = 10 A	V <sub>BE(sat)</sub>		2		2	V		
V <sub>CE</sub> = 10 V, I <sub>C</sub> = 1.0 A, f = 10 MHz	h <sub>FE</sub>	3		3				
V <sub>CB</sub> = 10 V, f = 1 MHz	C <sub>OBO</sub>		350		350	pF		
Resistive Load $V_{CC} = 100 \text{ V}$ $I_C = 50 \text{ A}$ $I_{B1} = I_{B2} = 10 \text{ A}$ $I_P = 10 \mu\text{S}$ duty cycle	t <sub>D</sub> t <sub>R</sub> t <sub>S</sub> t <sub>F</sub>	  	0.04 0.2 0.75 0.18	  	0.04 0.2 0.75 0.18	μs μs μs μs		