

QS20S/SL20S SERIES



- ECP/EPP High Speed Parallel Port Applications
- Available in QSOP and SOIC Packages
- Proven TaNCap™ Thin Film Technology
- Highly Integrated - replaces Up to 27 Discretes

The IRC TaNCap IEEE 1284 parallel printer interface networks are designed for use in printer, motherboard, disk drives, and other high speed digital interface applications. These highly integrated TaNSil® technology thin film on silicon networks offer three different functions in a single 20-pin QSOP or SOIC package. R1 is a pull-up resistor for use with open collector and open drain drivers, R2 is a series termination resistor and C acts as a low pass filter.

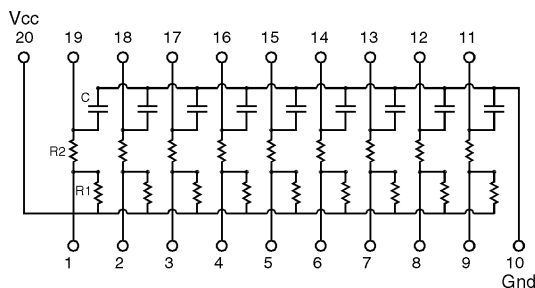
The QSOP package offers a high level of integration in a single surface mount device. Up to 27 discrete components are replaced by one IEEE1284 termination network.

The TaNCap series of resistor-capacitor networks are manufactured using IRC's military and space proven tantalum nitride technology. For high reliability and superior performance, use IEEE 1284 filter networks for your digital interface applications.

SPECIFICATIONS

	Range	Tolerance (%)	TCR (ppm/°C)	Operating Temp. Range (°C)	Breakdown Voltage (volts)	Max. Power Dissipation (watts)
Resistors	10Ω to 100Ω	±10	±100	-55 to +125	N/A	0.1 per resistor
Capacitors	10pF to 200pF	±20	N/A	-55 to +125	25	N/A

SCHEMATIC



Resistor-Capacitor Code Table

Code	R1	R2	C
1	1.0KΩ	10Ω	180pF
2	2.2KΩ	27Ω	220pF
3	4.7KΩ	33Ω	TBA
4	10KΩ	TBA	TBA

Example

332: R1 = 4.7KΩ, R2 = 33Ω, C = 220pF

HOW TO ORDER

Sample Part Number:

GUS - QS20 S - 3 3 2 - K M

Family

Model

QS20 = 20 pin QSOP

SL20 = 20 pin 0.300" SOIC

Schematic:

S = 20 pin IEEE 1284 Terminator

Resistor-Capacitor Code (R1 R2 C)

(See Table above for values)

Packaging Available
Tubes, Tape & Reel

Capacitor Tolerance
M = ±20%

Resistor Tolerance
K = ±10%; M = ±20%