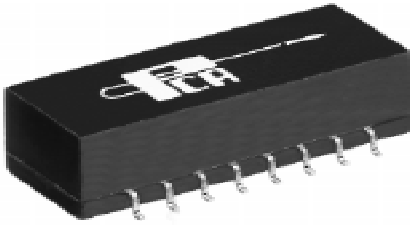


10/100 LAN Interface Module with Enhanced Common Mode Attenuation

EPF8017GM



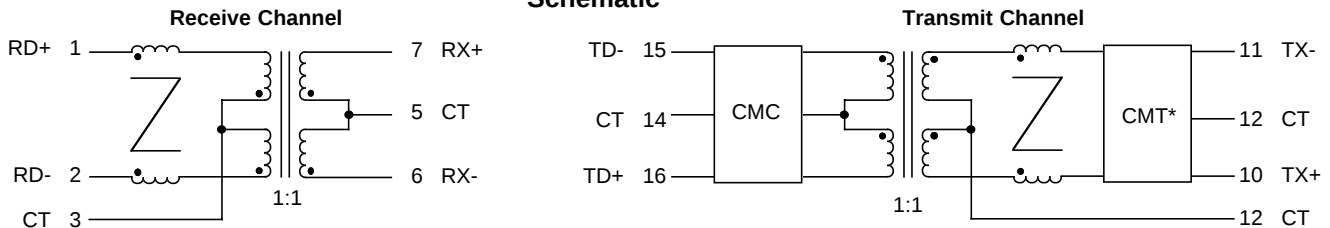
- Optimized for DP83840A/DP83223 Chip Set •
- Recommended for use with ICS 1890 Series and SS578Q2120 •
when connected per appropriate schematic
- Guaranteed to operate with 8 mA DC bias at 70°C •
- Complies with or exceeds IEEE 802.3, 10 BT/100 BX Standards •

Electrical Parameters @ 25° C

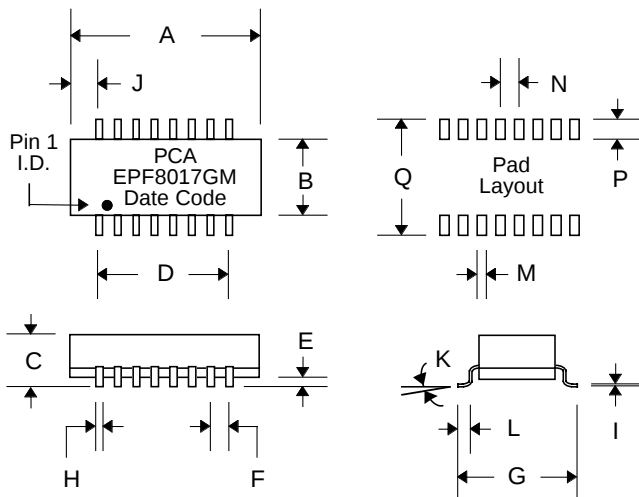
OCL @ 70°C	Insertion Loss (dB Max.)						Return Loss (dB Min.)						Common Mode Rejection (dB Min.)						Crosstalk (dB Min.) [Between Channels]	
100 KHz, 0.1 Vrms 8 mA DC Bias	1-80 MHz		80-100 MHz		100-150 MHz		1-30 MHz		30-60 MHz		60-100 MHz		1-30 MHz		30-100 MHz		100-500 MHz		5-10 MHz	10-100 MHz
Cable Side	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv		
350μH	-1	-1	-2	-2	-3.5	-3	-18	-18	-12	-12	-10	-10	-40	-40	-35	-30	-10	-10	-35	-35

• Isolation : 1500 Vrms • Impedance : 100 Ω • Rise Time : 3.0 nS Max. • * CMT : Optional •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.970	.990		24.64	25.15	
B	.380	.400		9.65	10.16	
C	.223	.243		5.66	6.17	
D	.700	Typ.		17.78	Typ.	
E	.010	.015		.254	.381	
F	.100	Typ.		2.54	Typ.	
G	.500	.520		12.7	13.20	
H	.016	.022		.406	.559	
I	.008	.012		.203	.305	
J	.090	Typ.		2.28	Typ.	
K	0°	8°		0°	8°	
L	.025	.045		.635	1.14	
M			.030			.762
N			.100			2.54
P			.092			2.34
Q			.560			14.22



For best results, PCB designer should design the outgoing traces preferably to be 50 Ω , balanced and well coupled to achieve minimum radiation from these traces.

Other pull down/up resistors not shown, for clarification please refer to National's application notes.

Notes : 1. See text above for clarification.
2. *NIC Side is shown. Hub side connections will have crossover swapping pins 3-6 & 1-2.