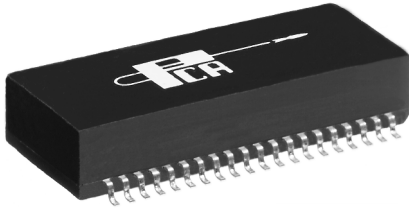


10 Base-T Multiport Module for CS8904 Quad PHY

EPE6320S



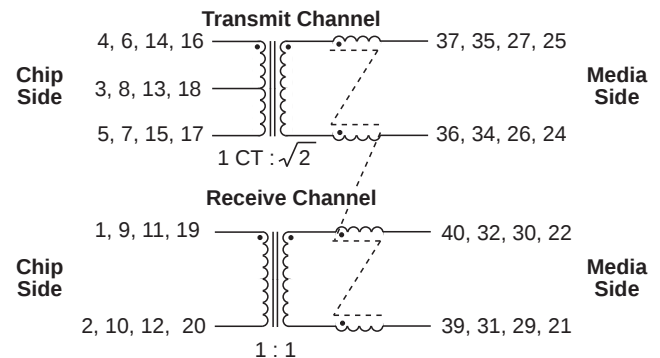
- Cost competitive solution for 4 port 10 Base-T switches •
- Also recommended for TI Quad PHY and similar quad chips •
- Robust design allows for most severe soldering processes •
- Complies with or exceeds IEEE Requirements •
- Isolation : 1500 Vrms •

Electrical Parameters @ 25°C

Inductance (μ H Min.)(1)	Leakage Inductance (μ H Max.)(1)		Return Loss (dB Min.)	Common Mode Rejection (dB Min.)				DCR (Ω Max.)		DM-CM (dB Min.)	CM-DM (dB Min.)	DM-CM (dB Min.)	CM-DM (dB Min.)	Crosstalk (dB Min.)	Insertion Loss (dB Max.)(2)		Interwinding Capacitance (pF Min.)
@ 100 KHz, 20 mVrms	@1MHz, 20mVrms		5 MHz - 10 MHz @ 85 Ω to110 Ω	@ 1 - 100 MHz		@ 400 MHz		chip side		@ 1-100 MHz	@ 300 MHz		2.5-25 MHz		5-20 MHz		
	Xmit	Rcv		Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv				
100	0.5	0.4	-18	-40	-20	-15	-10	0.8	0.5	-40	-30	-30	-15	-45	-1	-1	15

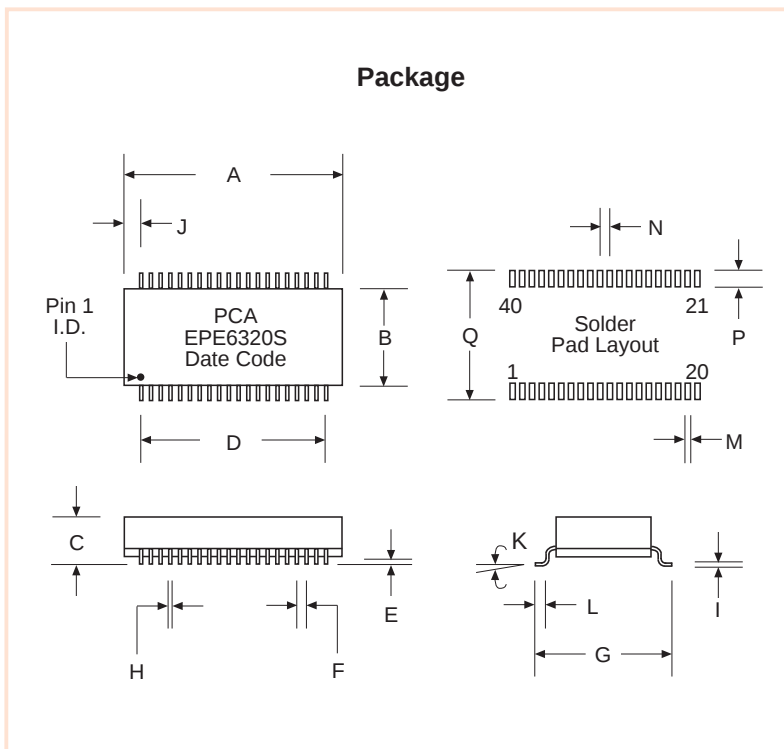
Notes: 1) Measured from chip side. 2) Insertion Loss @ 5MHz deviates @ 10 MHz by 0.2 dB Max.

Schematic



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	1.110	1.130	1.120	28.19	28.70	28.45
B	.470	.490	.480	11.94	12.45	12.19
C	.250	.270	.260	6.35	6.86	6.60
D	---	---	.950	---	---	24.13
E	.010	.015	.0125	.254	.381	.317
F	---	---	.050	---	---	1.27
G	.590	.610	.600	14.99	15.49	15.24
H	.016	.022	.019	.406	.559	.483
I	.008	.012	.010	.203	.305	.254
J	---	---	.085	---	---	2.16
K	0°	8°	---	0°	8°	---
L	.025	.045	.035	.635	1.14	.889
M	---	---	.030	---	---	.762
N	---	---	.050	---	---	1.27
P	---	---	.090	---	---	2.29
Q	---	---	.670	---	---	17.02



Documentor/Date

✓d By

Project Engineer/Date

Engineering/Date

Quality Assurance/Date