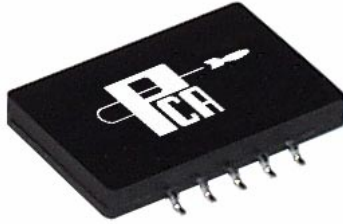


EPB5048G



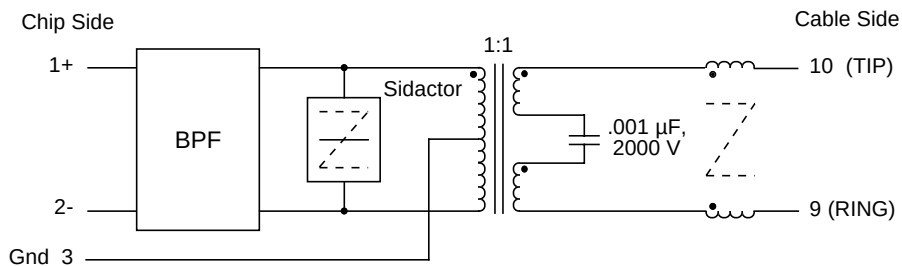
- Optimized for AMD79C978, Conexant CN 7221 •
& National DP83851 Chips
- UL/IEC 950 Approved File #132261 •
- Supports TUT Systems HPN Architecture •
- Robust construction allows for reflow processes •
- Temperature Range -40°C to +85°C •

Electrical Parameters @ 25° C

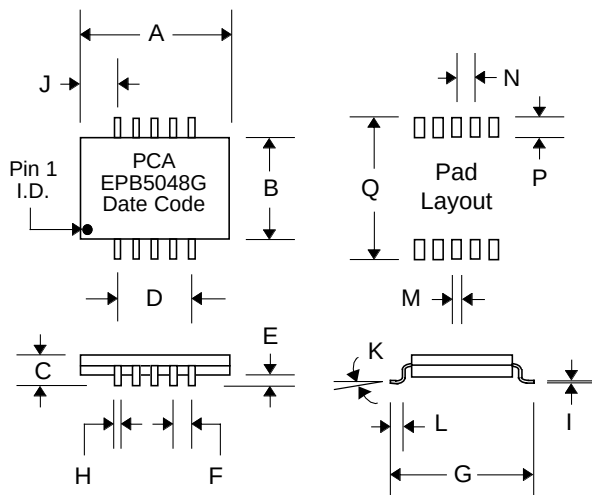
Cut-off Frequency		Insertion Loss (dB Max.)	Return Loss (dB Min.)	Attenuation (dB Min.)	
Lower Band MHz	Upper Band MHz	6.0-9.0 MHz	6.0-9.0 MHz	@ 1 MHz	@ 27 MHz
4.0-5.8	9-11	-3	-12	-60	-55

• Filter Characteristic Impedance : 100 Ω • Isolation: 1500 Vrms •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.760	.780	.770	19.30	19.81	19.56
B	.510	.530	.520	12.96	13.46	13.21
C	.110	.120	.115	2.79	3.05	2.92
D	---	---	.400	---	---	10.16
E	.003	.010	.005	.076	.254	.127
F	.097	.103	.100	2.46	2.62	2.54
G	.650	.670	.660	16.51	17.02	16.76
H	.017	.022	.020	.432	.559	.508
I	.008	.013	.011	.203	.330	.279
J	---	---	.185	---	---	4.70
K	0°	8°	4°	0°	8°	4°
L	.025	.045	.040	.635	1.14	1.02
M	---	---	.030	---	---	.762
N	---	---	.100	---	---	2.54
P	---	---	.085	---	---	2.16
Q	---	---	.700	---	---	17.78