14 Pin DIP Quad TTL Compatible Active Delay Lines

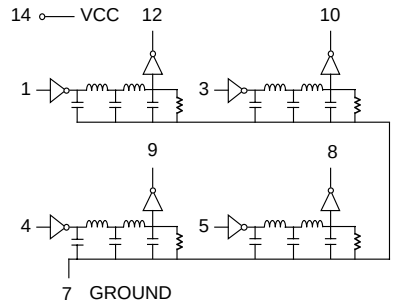
DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER
5	EP9734-5	16	EP9734-16	35	EP9734-35
6	EP9734-6	17	EP9734-17	40	EP9734-40
7	EP9734-7	18	EP9734-18	45	EP9734-45
8	EP9734-8	19	EP9734-19	50	EP9734-50
9	EP9734-9	20	EP9734-20	55	EP9734-55
10	EP9734-10	21	EP9734-21	60	EP9734-60
11	EP9734-11	22	EP9734-22	65	EP9734-65
12	EP9734-12	23	EP9734-23	70	EP9734-70
13	EP9734-13	24	EP9734-24	75	EP9734-75
14	EP9734-14	25	EP9734-25		
15	EP9734-15	30	EP9734-30		

†Whichever is greater. *Delay times referenced from input to leading edges at 25°C, 5.0V, with no load.*

DC Electrical Characteristics

	Parameter	Test Conditions	Min	Max	Unit
V_{OH}	High-Level Output Voltage	V_{CC} = min. V_{IL} = max. I_{OH} = max	2.7		V
V_{OL}	Low-Level Output Voltage	V_{CC} = min. V_{IH} = min. I_{OL} = max		0.5	V
V_{IK}	Input Clamp Voltage	V_{CC} = min. I_I = II_K		-1.2V	V
I_{IH}	High-Level Input Current	V_{CC} = max. V_{IN} = 2.7V		50	µA
		V_{CC} = max. V_{IN} = 5.25V		1.0	mA
I_{IL}	Low-Level Input Current	V_{CC} = max. V_{IN} = 0.5V		-2	mA
I_{OS}	Short Circuit Output Current	V_{CC} = max. V_{OUT} = 0. (One output at a time)	-40	-100	mA
I_{CCH}	High-Level Supply Current	V_{CC} = max. V_{IN} = OPEN		150	mA
I_{CCL}	Low-Level Supply Current	V_{CC} = max. V_{IN} = 0		150	mA
T_{RO}	Output Rise Time			4	nS
N_H	Fanout High-Level Output	V_{CC} = max. V_{OH} = 2.7V		20 TTL LOAD	
N_L	Fanout Low-Level Output	V_{CC} = max. V_{OL} = 0.5V		10 TTL LOAD	

Schematic



Recommended Operating Conditions

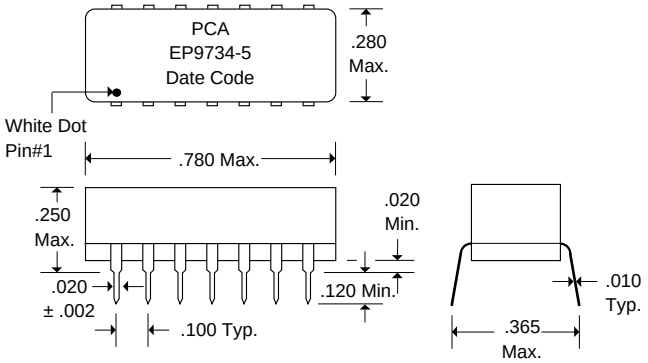
		Min	Max	Unit
V_{CC}	Supply Voltage	4.75	5.25	V
V_{IH}	High-Level Input Voltage	2.0		V
V_{IL}	Low-Level Input Voltage		0.8	V
I_{IK}	Input Clamp Current		-18	mA
I_{OH}	High-Level Output Current		-1.0	mA
I_{OL}	Low-Level Output Current		20	mA
P_W*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T_A	Operating Free-Air Temperature	0	+70	°C

**These two values are inter-dependent.*

Input Pulse Test Conditions @ 25° C

			Unit
E_{IN}	Pulse Input Voltage	3.2	Volts
P_W	Pulse Width % of Total Delay	110	%
T_{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
P_{RR}	Pulse Repetition Rate @ Td ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ Td > 200 nS	100	KHz
V_{CC}	Supply Voltage	5.0	Volts

Package Dimensions



Unless Otherwise Noted Dimensions in Inches
Tolerances:
Fractional = ± 1/32
.XX = ± .030 .XXX = ± .010

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