

# 14 Pin DIP Delayed Pulse Width Generator TTL Compatible Active Delay Line Modules

## Features

- Precise output pulse width
- Positive-edge triggered (10 nS) min.
- Fast rise and fall time (4 nS max. measured from 0.75V to 2.4V)
- Low Profile 14 pin DIP for auto-insertion
- Propagation Delays :  $7 \pm 2$  nS from pin 8 to pin 1  
:  $7 \pm 2$  nS from pin 8 to pin 13

PART NUMBER	PULSE WIDTH * $\pm 2$ NS or $\pm 5\%$ † (P <sub>WO</sub> )	MAX OUTPUT FREQ (mHz)
EP9981-5	5	100
EP9981-10	10	50
EP9981-15	15	33
EP9981-20	20	25
EP9981-25	25	20
EP9981-30	30	16
EP9981-35	35	14
EP9981-40	40	12
EP9981-45	45	11
EP9981-50	50	10
EP9981-60	60	8.4
EP9981-70	70	7.1
EP9981-80	80	6.3
EP9981-90	90	5.5
EP9981-100	100	5.0

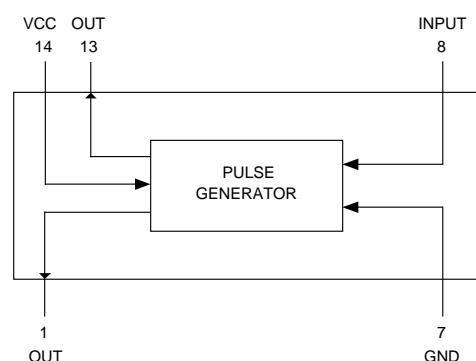
\* Measured at 1.5V Levels

† Whichever is greater.

## DC Electrical Characteristics

Parameter	Test Conditions	Min	Max	Unit
V <sub>OH</sub>	High-Level Output Voltage	2.7		V
V <sub>OL</sub>	Low-Level Output Voltage	0.5		V
V <sub>IK</sub>	Input Clamp Voltage	-1.2V		V
I <sub>IH</sub>	High-Level Input Current	50		$\mu$ A
I <sub>IL</sub>	Low-Level Input Current	-2		mA
I <sub>OS</sub>	Short Circuit Output Current	-100		mA
I <sub>CCH</sub>	High-Level Supply Current	75		mA
I <sub>CCL</sub>	Low-Level Supply Current	75		mA
N <sub>H</sub>	Fanout High-Level Output	20 TTL LOAD		
N <sub>L</sub>	Fanout Low-Level Output	10 TTL LOAD		

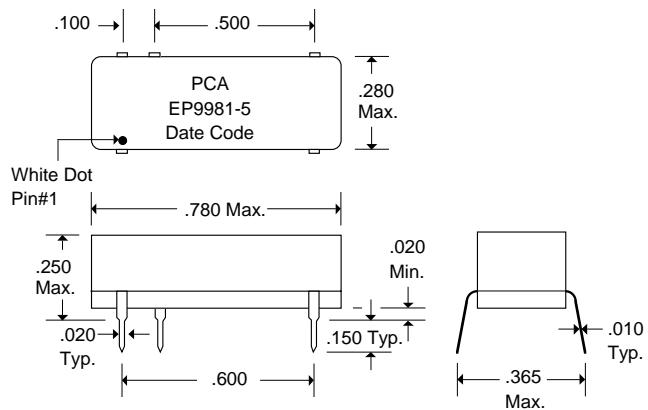
## Schematic



## Recommended Operating Conditions

Parameter	Min	Max	Unit
V <sub>CC</sub>	4.75	5.25	V
V <sub>IH</sub>	2.0		V
V <sub>IL</sub>	0.8		V
I <sub>IK</sub>	-18		mA
I <sub>OH</sub>	-1.0		mA
I <sub>OL</sub>	20		mA
P			nS
P <sub>WI</sub>	10		nS
T <sub>A</sub>	0	+70	°C

## Package



## Input Pulse Test Conditions @ 25°C

Parameter	Unit	Test Conditions	
		Min	Max
E <sub>IN</sub>	Pulse Input Voltage	3.2	
T <sub>RI</sub>	Pulse Rise Time	2.0	
P <sub>WI</sub>	Pulse Width	10	
P	Period	P <sub>WO</sub> x2	
(For EP9981-5)		20	
V <sub>CC</sub>	Supply Voltage	5.0	