

10/100 LAN Interface Module with Enhanced Common Mode Attenuation

EPF8104S

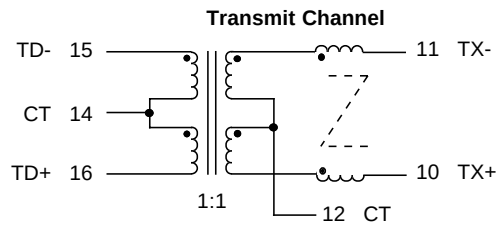
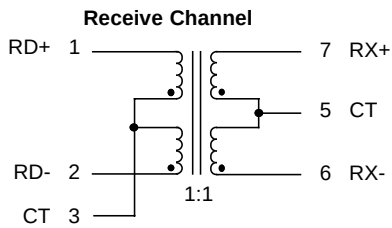
- Optimized for DP83840A/DP83223 Chip Set •
- Recommended for use with ICS 1890 Series and SS578Q2120 •
when connected per appropriate schematic
- Guaranteed to operate with 8 mA DC bias at 70°C •
- Complies with or exceeds IEEE 802.3, 10 BT/100 BX Standards •

Electrical Parameters @ 25° C

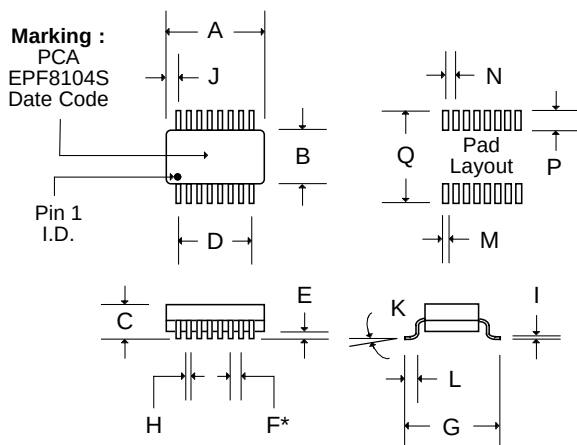
OCL @ 70°C	Insertion Loss (dB Max.)						Return Loss (dB Min.)						Common Mode Rejection (dB Min.)					Crosstalk (dB Min.)
100 KHz, 0.1 Vrms 8 mA DC Bias	1-80 MHz		80-100 MHz		100-150 MHz		1-30 MHz		30-60 MHz		60-100 MHz		1-30 MHz	30-100 MHz	100-200 MHz	200-300 MHz	300-500 MHz	1-100 MHz
Cable Side	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Xmit	Xmit	Xmit	Xmit	
350μH	-1	-1	-1	-1	-2	-2	-18	-18	-12	-12	-10	-10	-45	-40	-30	-20	-10	-35

- Isolation : 1500 Vrms • Impedance : 100 Ω • Rise Time : 3.0 nS Max. •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.590	.610	.600	14.99	15.49	15.24
B	.270	.290	.280	6.68	7.37	7.11
C	.210	.230	.220	5.33	5.84	5.59
D	.345	.355	.350	8.76	9.02	8.89
E	.010	.015	.0125	.254	.381	.3175
F	.047	.053	.050	1.19	1.35	1.27
G	.390	.410	.400	9.9	10.41	10.16
H	.017	.022	.020	.432	.559	.508
I	.008	.013	.011	.203	.330	.279
J	.120	.130	.125	3.05	3.30	3.18
K	0°	8°	4°	0°	8°	4°
L	.020	.040	.030	.508	1.02	.762
M			.030			.762
N			.050			1.27
P			.092			2.34
Q			.460			11.68