

8 Pin Triple Fast Logic TTL Compatible Active Delay Lines

Delay Time ±5% or ±2nS†	Part Number	Delay Time ±5% or ±2nS†	Part Number	Delay Time ±5% or ±2nS†	Part Number
5	EPA2200-5	17	EPA2200-17	45	EPA2200-45
6	EPA2200-6	18	EPA2200-18	50	EPA2200-50
7	EPA2200-7	19	EPA2200-19	55	EPA2200-55
8	EPA2200-8	20	EPA2200-20	60	EPA2200-60
9	EPA2200-9	21	EPA2200-21	65	EPA2200-65
10	EPA2200-10	22	EPA2200-22	70	EPA2200-70
11	EPA2200-11	23	EPA2200-23	75	EPA2200-75
12	EPA2200-12	24	EPA2200-24	80	EPA2200-80
13	EPA2200-13	25	EPA2200-25	85	EPA2200-85
14	EPA2200-14	30	EPA2200-30	90	EPA2200-90
15	EPA2200-15	35	EPA2200-35	95	EPA2200-95
16	EPA2200-16	40	EPA2200-40	100	EPA2200-100

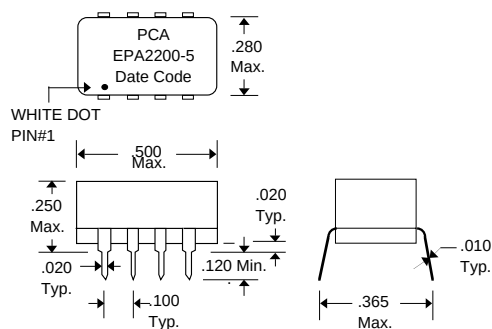
†Whichever is greater. Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.

Notes :

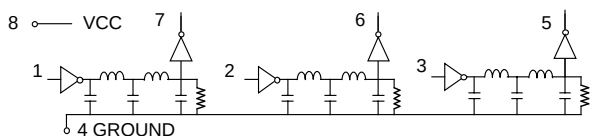
DC Electrical Characteristics

Parameter	Test Conditions	Min	Max	Unit
V _{OH}	High-Level Output Voltage	2.7		V
V _{OL}	Low-Level Output Voltage		0.5	V
V _{IK}	Input Clamp Voltage		-1.2V	V
I _{IH}	High-Level Input Current		50	µA
	V _{CC} = max. V _{IN} = 2.7V		1.0	mA
	V _{CC} = max. V _{IN} = 5.25V		-2	mA
I _{IL}	Low-Level Input Current		-2	mA
I _{OS}	Short Circuit Output Current	-40	-100	mA
	V _{CC} = max. V _{OUT} = 0. (One output at a time)			
I _{CCH}	High-Level Supply Current		15	mA
I _{CCL}	Low-Level Supply Current		75	mA
T _{RO}	Output Rise Time		4	nS
N _H	Fanout High-Level Output		20 TTL LOAD	
N _L	Fanout Low-Level Output		10 TTL LOAD	
	V _{CC} = max. V _{OH} = 2.7V			
	V _{CC} = max. V _{OL} = 0.5V			

Package



Schematic



Recommended Operating Conditions

*These two values are inter-dependent.

Parameter	Test Conditions	Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

Input Pulse Test Conditions @ 25° C

Parameter	Test Conditions	Min	Max	Unit
E _{IN}	Pulse Input Voltage		3.2	Volts
PW	Pulse Width % of Total Delay		110	%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)		2.0	nS
PRR	Pulse Repetition Rate		1.0	MHz
V _{CC}	Supply Voltage		5.0	Volts



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