

100 BX Interface Module for Multi-port Application

EPF8071S



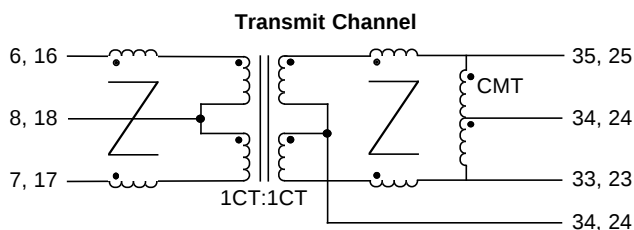
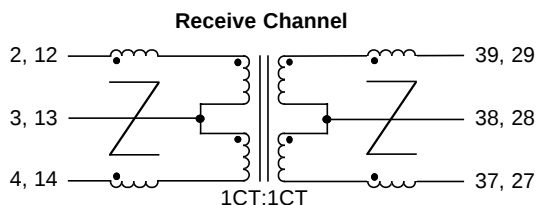
- Optimized for DP83840A/DP83223 Chip Set •
- Recommended for use with ICS 1890 Series and SS578Q2120 • when connected per appropriate schematic
- Guaranteed to operate with 8 mA DC bias at 70°C •
- Complies with or exceeds IEEE 802.3, 100 BX Standards •

Electrical Parameters @ 25° C

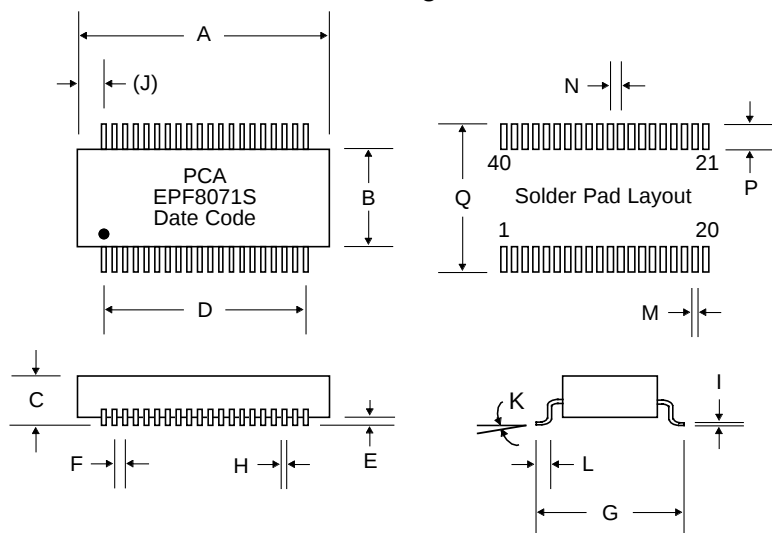
OC @ 70°C	Insertion Loss (dB Max.)						Return Loss (dB Min.)						Common Mode Rejection (dB Min.)						Crosstalk (dB Min.) [Between Channels]
100 KHz, 0.1 Vrms 8 mA DC Bias	1-80 MHz		80-100 MHz		100-150 MHz		1-30 MHz		30-60 MHz		60-100 MHz		1-30 MHz		100 MHz		200 MHz		1-100 MHz
Cable Side	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	
350μH	-1	-1	-2	-2	-3.5	-3	-18	-18	-12	-12	-10	-10	-38	-38	-35	-30	-25	-25	-35

- Isolation : 1500 Vrms • Impedance : 100 Ω •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	1.110	1.130		28.19	28.70	
B	.470	.490		11.94	12.45	
C	.235	.255		5.97	6.48	
D	.950	Typ.		24.13	Typ.	
E	.010	.015		.254	.381	
F	.050	Typ.		1.27	Typ.	
G	.590	.610		14.99	15.49	
H	.016	.022		.406	.559	
I	.008	.012		.203	.305	
(J)	.085	Typ.		2.16	Typ.	
K	0°	8°		0°	8°	
L	.025	.045		.635	1.14	
M			.030			.762
N			.050			1.27
P			.090			2.29
Q			.670			17.02