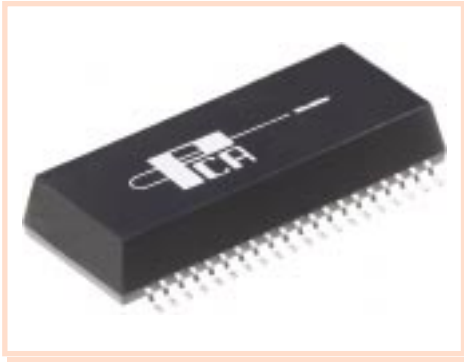


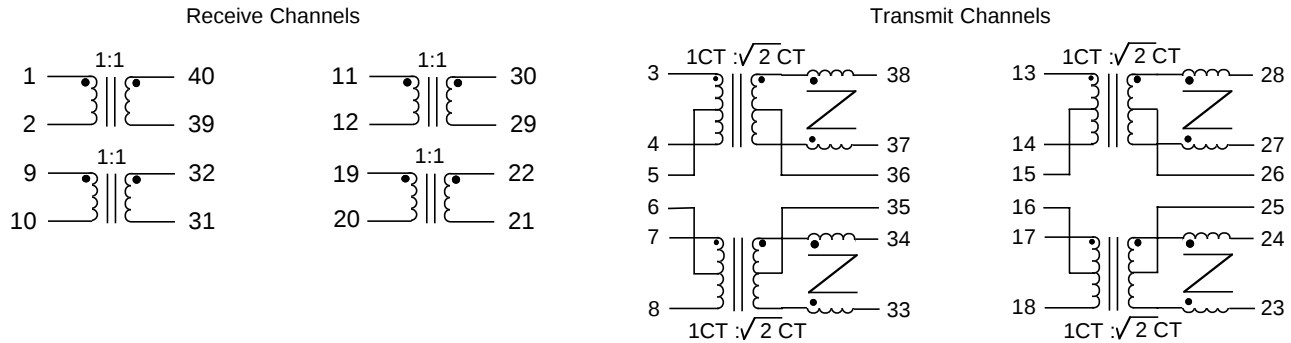


- Optimized for Level One LXT901/944 and Quad PHY •
- (Also recommended for transceivers such as TNETE 2004)
- Suggested construction for withstanding SMD process •
- Complies with IEEE 802.3 Recommendations for 10 Base T •

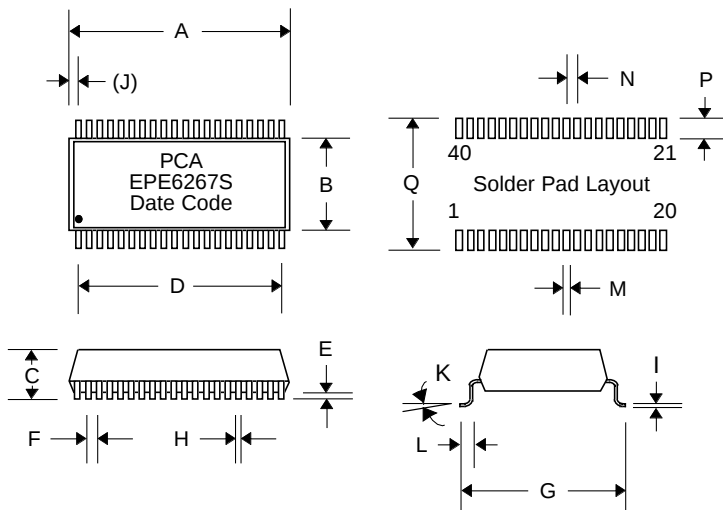
Electrical Parameters @ 25° C

Inductance (μ H Min.)	Interwinding Capacitance (pF Max.)	Leakage Inductance (μ H Max.)	CMRR (dB Min.)			Return Loss (dB Min.)		Hipot (Vrms)	Crosstalk (dB Min.)
@ 100 KHz, 0.1 Vrms	@ 100 KHz, 0.1 Vrms	@ 100 KHz, 0.1 Vrms	30MHz-50 MHz	100 MHz	200 MHz	5-10 MHz @ 100 Ω	5-10 MHz @ 85 Ω , 115 Ω		1-10 MHz
200	12	0.3	-35	-30	-20	-20	-15	1500	-45

Schematic



Package



Dimensions

Dim.	(Inches)			(millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	1.110	1.130		28.19	28.70	
B	.470	.490		11.94	12.45	
C	.205	.225		5.21	5.72	
D	.950	Typ.		24.13	Typ.	
E	.005	.015		.127	.381	
F	.050	Typ.		1.27	Typ.	
G	.620	.640		15.75	16.26	
H	.016	.022		.406	.559	
I	.010	Typ.		.127	.381	
(J)	.075	.095		1.91	2.41	
K	0°	8°		0°	8°	
L	.025	.045		.635	1.14	
M			.030			.762
N			.050			1.27
P			.090			2.29
Q			.670			17.02