

10/100 LAN Interface Module For PC Card Applications

EPF8023G



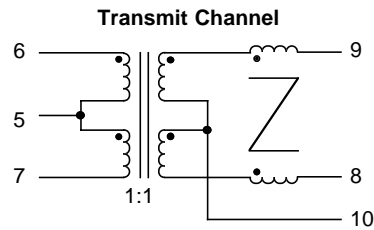
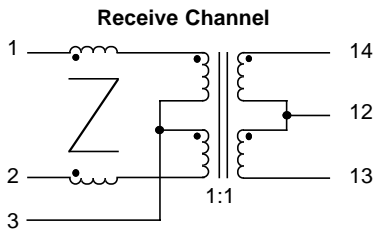
- Optimized for DP83840A/DP83223 Chip Set •
- Recommended for use with ICS 1890 Series and SS578Q2120 •
(when connected per appropriate schematic)
- Guaranteed to operate with 8 mA DC bias at 70°C •
- Complies with or exceeds IEEE 802.3, 10 BT/100 BX Standards •

Electrical Parameters @ 25° C

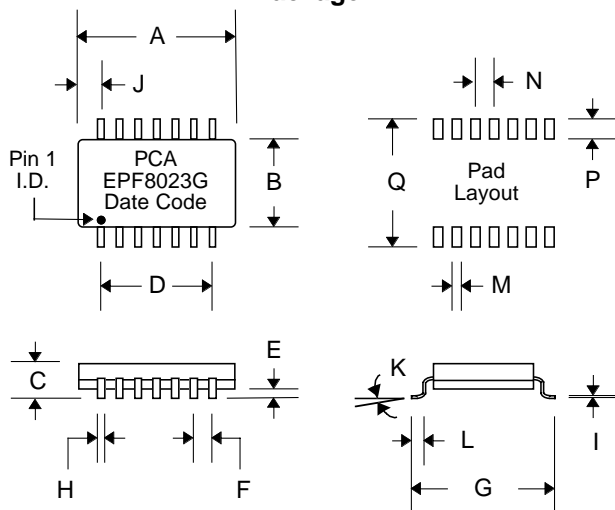
OCL @ 70°C	Insertion Loss (dB Max.)						Return Loss (dB Min.)						Common Mode Rejection (dB Min.)						Crosstalk (dB Min.) [Between Channels]	
	1-80 MHz		80-100 MHz		100-150 MHz		1-30 MHz		30-60 MHz		60-100 MHz		1-30 MHz		30-100 MHz		100-500 MHz		1-10 MHz	10-100 MHz
	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv		
100 KHz, 0.1 Vrms 8 mA DC Bias	-1	-1	-1	-1	-3.5	-3	-20	-20	-17	-17	-10	-10	-38	-38	-38	-38	-10	-10	-38	-38

- Isolation : 1500 Vrms • Impedance : 100 Ω • Rise Time : 3.0 nS Max. •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.790	.810		20.06	20.57	
B	.510	.530		12.95	13.46	
C	.090	.098		2.29	2.49	
D	.600	Typ.		15.24	Typ.	
E	.003	.020		0.076	.508	
F	.100	Typ.		2.54	Typ.	
G	.660	.680		16.76	17.27	
H	.016	.022		.406	.559	
I	.008	.012		.203	.305	
J	.090	Typ.		2.28	Typ.	
K	0°	8°		0°	8°	
L	.025	.045		.635	1.14	
M			.040			1.02
N			.100			2.54
P			.085			2.16
Q			.710			18.03