

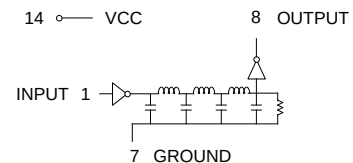
14 Pin DIP High Precision Single Output TTL Compatible Active Delay Lines

TIME DELAYS (nS)	PART NUMBER	TIME DELAYS (nS)	PART NUMBER	TIME DELAYS (nS)	PART NUMBER
5±1	EPA500-5	23±1	EPA500-23	125±4	EPA500-125
6±1	EPA500-6	24±1	EPA500-24	150±4.5	EPA500-150
7±1	EPA500-7	25±1	EPA500-25	175±5	EPA500-175
8±1	EPA500-8	30±1.5	EPA500-30	20±6	EPA500-200
9±1	EPA500-9	35±1.5	EPA500-35	225±7	EPA500-225
10±1	EPA500-10	40±1.5	EPA500-40	250±8	EPA500-250
11±1	EPA500-11	45±2	EPA500-45	275±9	EPA500-275
12±1	EPA500-12	50±2	EPA500-50	300±10	EPA500-300
13±1	EPA500-13	55±2	EPA500-55	359±11	EPA500-350
14±1	EPA500-14	60±2	EPA500-60	400±12	EPA500-400
15±1	EPA500-15	65±2.5	EPA500-65	450±14	EPA500-450
16±1	EPA500-16	70±2.5	EPA500-70	500±15	EPA500-500
17±1	EPA500-17	75±2.5	EPA500-75	600±18	EPA500-600
18±1	EPA500-18	80±2.5	EPA500-80	700±20	EPA500-700
19±1	EPA500-19	85±3	EPA500-85	800±22	EPA500-800
20±1	EPA500-20	90±3	EPA500-90	900±24	EPA500-900
21±1	EPA500-21	95±3	EPA500-95	1000±26	EPA500-1000
22±1	EPA500-22	100±3	EPA500-100		

Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics					
Parameter	Test Conditions	Min	Max	Unit	
V _{OH}	High-Level Output Voltage	V _{CC} = min. V _{IL} = max. I _{OH} = max	2.7		V
V _{OL}	Low-Level Output Voltage	V _{CC} = min. V _{IH} = min. I _{OL} = max	0.5		V
V _{IK}	Input Clamp Voltage	V _{CC} = min. I _I = I _{IK}	-1.2		V
I _{IH}	High-Level Input Current	V _{CC} = max. V _{IN} = 2.7V	50		µA
		V _{CC} = max. V _{IN} = 5.25V	1.0		mA
I _{IL}	Low-Level Input Current	V _{CC} = max. V _{IN} = 0.5V	-2		mA
I _{OS}	Short Circuit Output Current	V _{CC} = max. V _{OUT} = 0.	-40		mA
I _{CCCH}	High-Level Supply Current	V _{CC} = max. V _{IN} = OPEN	75		mA
I _{CCCL}	Low-Level Supply Current	V _{CC} = max. V _{IN} = 0	75		mA
T _{RO}	Output Rise Time	T _d £ 500 nS (0.75 to 2.4 Volts)	4		nS
N _H	Fanout High-Level Output	V _{CC} = max. V _{OH} = 2.7V	20		TTL LOAD
N _L	Fanout Low-Level Output	V _{CC} = max. V _{OL} = 0.5V	10		TTL LOAD

Schematic



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
P _{RR}	Pulse Repetition Rate @ T _d £ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts

Package Dimensions

