élantec.

PRODUCT BRIEF

Features

- "Shrink-small" outline package
- Voltage-controlled output current source requiring one external set resistor per channel
- Current-controlled output current source
- Rise time = 1.6 nsec
- Fall time = 1.9 nsec
- On chip oscillator with frequency and amplitude control by use of external resistors to ground
- Oscillator to 500MHz
- Oscillator to 100mA pk/pk
- Single +5V supply $(\pm 10\%)$
- Disable feature for power-up protection and power savings
- CMOS control signals

Applications

- CD-RW applications
- Writable optical drives
- · Laser diode current switching

Ordering Information Temp. Range

LL0211CO	0 C to 170 C	10-1 III Q5O1	WID1 0040

Package

Outline #

General Description

The EL6211C is a three-channel laser diode current amplifier that provides controlled current to a grounded laser diode. Channels 2 and 3 should be used as the write channels, with switching speeds of less than two nanoseconds rise/fall time. All three channels are summed together at the I_{OUT} output, allowing the user to create multilevel waveforms in order to optimize laser diode performance. The level of the output current is set by an analog voltage applied to an external resistor which converts the voltage into a current at the IIN pin (virtually ground).

An on-chip 500MHz oscillator is provided to allow output current modulation when in any mode. This is turned on when the OSCEN pin is held high. Complete control of amplitude and frequency is set by two external resistors connected to ground at pins RFREQ and RAMP (see graphs in this data sheet for further explanation).

Output current pulses are enabled when an 'L' signal is applied to the OUTEN pin. No output current flows when OUTEN is 'H', and additional laser diode protection is provided since the OUTEN input will float high when open. Complete I_{OUT} shutoff is also achieved by holding the ENABLE pin low, which will override the OUTEN control pins.

The external resistors allow the user to accurately and independently set each amplifier transconductance by applying a voltage to each resistor, without restriction on the voltage range, thus ensuring broad voltage DAC compatibility. Alternatively, the I_{IN} pin can be biased from a current DAC or other current source.

Connection Diagram

1	IINR	VCC	16
2	IIN2	IOUT	15
3	RFREQ	GND	14
4	IIN3	RAMP	13
5	GND	ENABLE	12
6	OUTENR	OSCEN	11
7	OUTEN2	VCC	10
8	OUTEN3	GND	9

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