



ETC1810 Microprocessor Reset Circuits

Description

The ETC1810 is an inexpensive microprocessor supervisory circuits that monitor power supplies in microprocessor based systems.

The function of these devices is to assert a reset if the power supply drops below a designated reset threshold level. Several different reset threshold levels are available to accommodate 5%, 10%, or 15% drop in 5V powered systems.

The ETC1810 has an active low $\overline{\text{RESET}}$ output. The reset output is guaranteed to remain asserted for a minimum of 100ms after VCC has risen above the designated reset threshold level. The ETC1810 comes in a 3-pin SOT-23 package.

Typical Applications

- Portable Equipment
- Intelligent Instruments
- Critical Microprocessor Power Monitoring
- Printers/Computers
- Controllers

Reset Threshold Voltage (V)	Device Suffix
4.62	-5
4.37	-10
4.12	-15

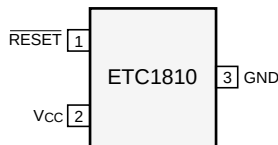
Ordering Information

Part	Package	Temp. Range
ETC1810_U	3-Lead SOT23	-40°C to +85°C
ETC1810_D	Tested Die	0°C to +70°C

Place the device suffix of desired reset threshold voltage from table above in blank to complete the part number.

Pin Configuration

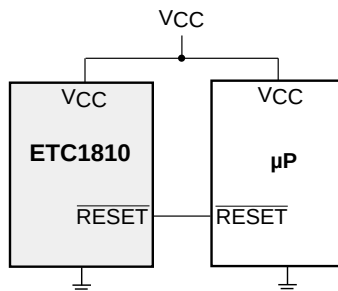
Top View



Features

- $\overline{\text{RESET}}$ Remains Valid with VCC as Low as 1.4V
- Precision Voltage Monitor for 5%, 10%, or 15% drop in 5V Power Supplies
- Available in 3-Pin SOT23 Package
- 9 μ A Supply Current (typical)
- 100ms Minimum Reset Pulse Width
- No External Components Required

Typical Operating Circuit



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Absolute Maximum Ratings

Terminal Voltage
VCC -0.3V to 6.0V

Input Current, VCC 20mA
Output Current, RESET 20mA
Rate of Rise, VCC 100V/ μ s

Operating Temperature Range
ETC1810_U -40°C to 85°C
Storage Temperature Range -65°C to 150°C
Lead Temperature (Soldering - 10 sec.) 300°C
Power Dissipation (T_A = +70°C) 320mW

Stresses above those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent device failure. Functionality at or above these limits is not implied. Exposure to absolute maximum ratings for extended periods may affect device reliability. Operating ranges define those limits between which the functionality of the device is guaranteed.

Electrical Characteristics					
VCC = 5V, T _A = Operating Temperature Range, unless otherwise noted.					
Parameter	Conditions	Min	Typ	Max	Units
Operating Voltage Range, VCC	T _A = 0°C to 70°C	1.4		5.5	V
	T _A = -40°C to 85°C	1.6		5.5	
Supply Current, ICC			9	20	μ A
Reset Voltage Threshold, VTH	ETC1810-5	4.50	4.62	4.75	V
	ETC1810-10	4.25	4.37	4.50	
	ETC1810-15	4.00	4.12	4.24	
Reset Timeout Period		100	150	250	ms
RESET Output Voltage, VOH	I _{Source} = 800 μ A	VCC - 1.5V			V
RESET Output Voltage, VOL	VCC=VTH Min., I _{Sink} =3.2mA			0.4	V
	VCC>1.4V, I _{Sink} =50 μ A			0.3	

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Pin Functions

Pin Name	Pin No.	
$\overline{\text{RESET}}$	1	$\overline{\text{RESET}}$ goes low if V_{CC} falls below the reset threshold and remains asserted for one reset timeout period (100ms min.) after V_{CC} exceeds the reset threshold.
V_{CC}	2	Power supply input, 5V.
GND	3	IC Ground Pin.

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Block Diagram

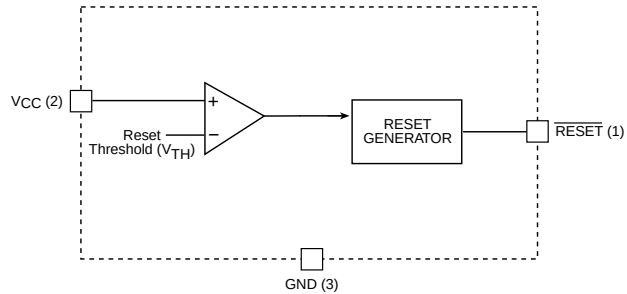


Figure 1. ETC1810 Block Diagram

Circuit Description

Microprocessor Reset

The $\overline{\text{RESET}}$ pin is asserted whenever V_{CC} falls below the reset threshold voltage. The reset pin remains asserted for a period of 150ms after V_{CC} has risen above the reset threshold voltage. The reset function ensures the microprocessor is properly reset and powers up into a known condition after a power failure. $\overline{\text{RESET}}$ will remain valid with V_{CC} as low as 1.4V.

VCC Transients

The ETC1810 is relatively immune to negative-going V_{CC} glitches below the reset threshold. Typically, a negative-going transient 125mV below the reset threshold with a duration of 50 μ s or less will not cause an unwanted reset.

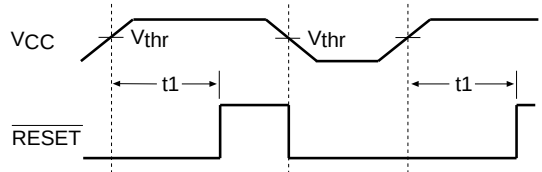


Figure 2. Reset Timing Diagram

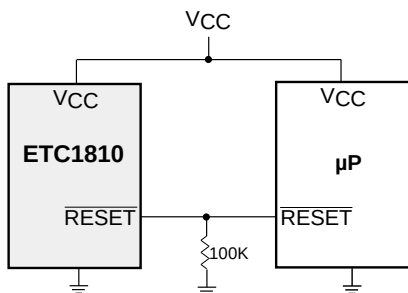


Figure 3. $\overline{\text{RESET}}$ Valid to $V_{CC} = 0V$

$\overline{\text{RESET}}$ Valid to 0V

A resistor can be added from the $\overline{\text{RESET}}$ pin to ground to ensure the $\overline{\text{RESET}}$ output remains low with V_{CC} down to 0V. A 100K Ω resistor connected from $\overline{\text{RESET}}$ to ground is recommended. The size of the resistor should be large enough to not load the $\overline{\text{RESET}}$ output and small enough to pull-down any stray leakage currents.

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Alternate Source Cross Reference Guide

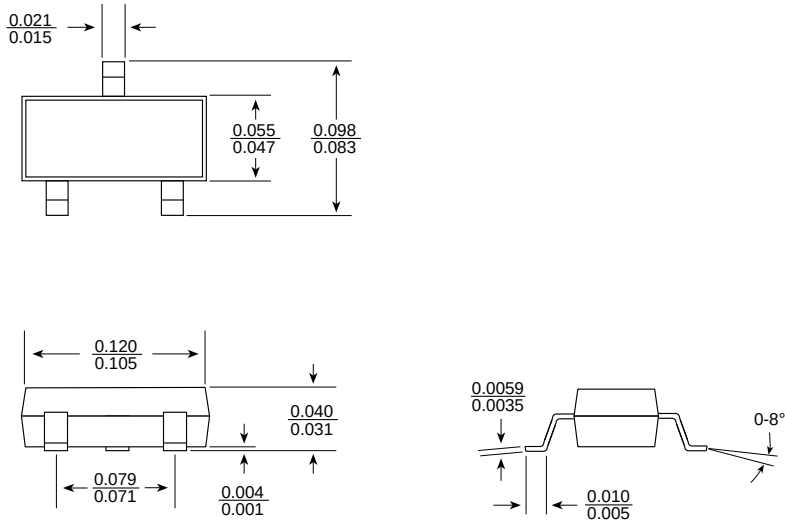
<u>Industry P/N</u>	<u>ETC Direct Replacement</u>
DS1810R-5	ETC1810-5U
DS1810R-10	ETC1810-10U
DS1810R-15	ETC1810-15U

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Packaging Information

U Package, 3-Pin SOT-23 Small-Outline Transistor Package



Dimensions are in inches.

Device Marking Information

Lot Code
XXXX = ETC1810-5U
XXXX = ETC1810-10U
XXXX = ETC1810-15U

Packaging Information

Technical drawing of a mechanical part, showing a top view and a side view.

Top View Dimensions:

- Overall width: 4.0 ± 0.1
- Distance between centerlines of the first two holes: 2.0 ± 0.05
- Distance between centerlines of the last two holes: 4.0 ± 0.1
- Overall height: 8.0 ± 0.3
- Distance from the top edge to the centerline of the first hole: 1.75 ± 0.1
- Distance from the centerline of the first hole to the centerline of the last hole: 3.5 ± 0.05
- Radius of the circular holes: $\varnothing 1.5 \pm 0.1$
- Radius of the rectangular holes: $\varnothing 1.1 \pm 0.1$

Side View Dimensions:

- Overall width: 2.7 ± 0.1
- Overall height: 1.2 ± 0.1
- Distance between centerlines of the two rectangular holes: 3.1 ± 0.1

Electronic Technology

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