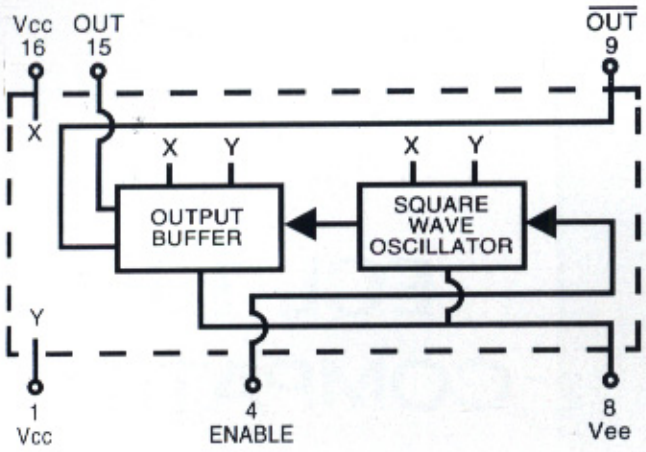
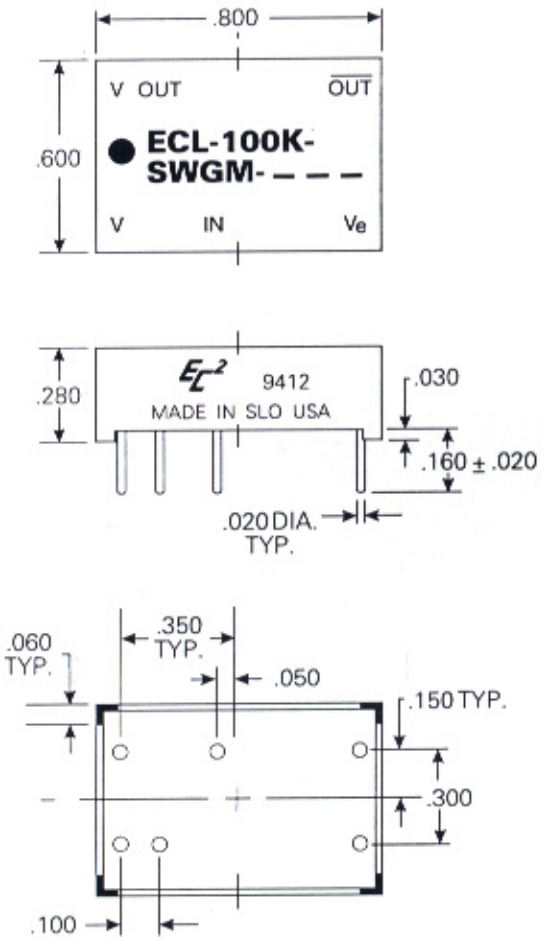
BLOCK DIAGRAM IS SHOWN BELOW



MECHANICAL DETAIL IS SHOWN BELOW



OPERATING SPECIFICATIONS

*Supply voltage:	-4.5 ± 5% to Vee
Supply current:	80 mA typical
Logic 1 Input at 25°C:	
Voltage	-1.165V min.
Current	350 uA max.
Logic 0 Input at 25°C:	
Voltage	-1.475V max.
Current	.5 uA min.
Logic 1 Output at 25°C:	-1.025V min.
Logic 0 Output at 25°C:	-1.620V max.
Operating temperature range:	0 to +85°C
Storage temperature:	-55 to +125°C

*Output frequency will increase or decrease less than 1% for a respective increase or decrease of 5% in supply voltage.

PART NUMBER TABLE

Part Number	Output Frequency	Part Number	Output Frequency
ECL-100K-SWGM-50	50 MHz	ECL-100K-SWGM-130	130 MHz
ECL-100K-SWGM-55	55 MHz	ECL-100K-SWGM-140	140 MHz
ECL-100K-SWGM-60	60 MHz	ECL-100K-SWGM-150	150 MHz
ECL-100K-SWGM-65	65 MHz	ECL-100K-SWGM-160	160 MHz
ECL-100K-SWGM-70	70 MHz	ECL-100K-SWGM-170	170 MHz
ECL-100K-SWGM-75	75 MHz	ECL-100K-SWGM-180	180 MHz
ECL-100K-SWGM-80	80 MHz	ECL-100K-SWGM-190	190 MHz
ECL-100K-SWGM-85	85 MHz	ECL-100K-SWGM-200	200 MHz
ECL-100K-SWGM-90	90 MHz	ECL-100K-SWGM-210	210 MHz
ECL-100K-SWGM-95	95 MHz	ECL-100K-SWGM-220	220 MHz
ECL-100K-SWGM-100	100 MHz	ECL-100K-SWGM-230	230 MHz
ECL-100K-SWGM-110	110 MHz	ECL-100K-SWGM-240	240 MHz
ECL-100K-SWGM-120	120 MHz	ECL-100K-SWGM-250	250 MHz

TEST CONDITIONS

1. All measurements are made at 25°C.
2. Vee supply voltage is maintained at -4.5V DC.
3. All units are tested using a standard open emitter ECL 100K gate at the input. The input and output utilize a 50 ohm pulldown resistor to -2V; the output is also loaded with one ECL 100K gate.

Special modules can be readily manufactured to improve accuracies and/or provide customer specified random frequencies for specific applications.