DDTC (R2-ONLY SERIES) KA

NPN PRE-BIASED SMALL SIGNAL SC-59 SURFACE MOUNT TRANSISTOR

SPICE MODELS: DDTC114GKA DDTC124GKA DDTC144GKA DDTC115GKA

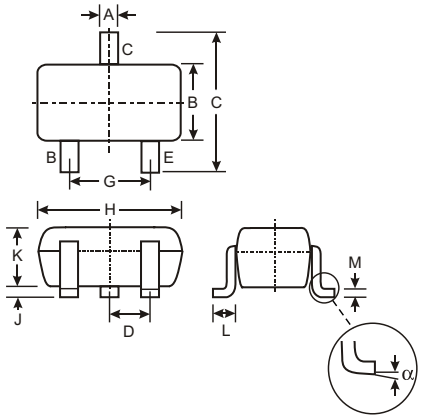
NEW PRODUCT

Features

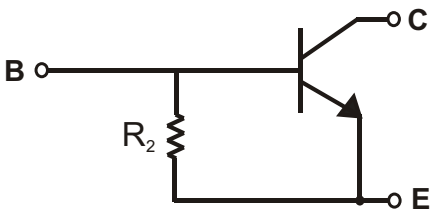
- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistor, R2 only
- Also Available in Lead Free Version

Mechanical Data

- Case: SC-59, Molded Plastic
- Case material - UL Flammability Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020A
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish). Please see Ordering Information, Note 3, on Page 2
- Terminal Connections: See Diagram
- Marking: Date Code and Marking Code (See Diagrams & Page 2)
- Weight: 0.006 grams (approx.)
- Ordering Information (See Page 2)



SC-59		
Dim	**Min**	**Max**
A	0.35	0.50
B	1.50	1.70
C	2.70	3.00
D	0.95	
G	1.90	
H	2.90	3.10
J	0.013	0.10
K	1.00	1.30
L	0.35	0.55
M	0.10	0.20
α	0°	8°
All Dimensions in mm		



SCHEMATIC DIAGRAM

P/N	R2 (NOM)	MARKING
DDTC114GKA	10KΩ	N26
DDTC124GKA	22KΩ	N27
DDTC144GKA	47KΩ	N28
DDTC115GKA	100KΩ	N29

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	V
Collector-Emitter Voltage	V_{CEO}	50	V
Emitter-Base Voltage	V_{EBO}	5	V
Collector Current	I_C (Max)	100	mA
Power Dissipation	P_d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	625	°C/W
Operating and Storage and Temperature Range	T_j, T_{STG}	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.

NEW PRODUCT

Electrical Characteristics @ $T_A = 25°C$ unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV_{CBO}	50	—	—	V	$I_C = 50\mu A$
Collector-Emitter Breakdown Voltage		BV_{CEO}	50	—	—	V	$I_C = 1mA$
Emitter-Base Breakdown Voltage		BV_{EBO}	5	—	—	V	$I_E = 720\mu A$, DDTC114GKA $I_E = 330\mu A$, DDTC124GKA $I_E = 160\mu A$, DDTC144GKA $I_E = 72\mu A$, DDTC115GKA
Collector Cutoff Current		I_{CBO}	—	—	0.5	μA	$V_{CB} = 50V$
Emitter Cutoff Current	DDTC114GKA DDTC124GKA DDTC144GKA DDTC115GKA	I_{EBO}	300 140 65 30	—	580 260 130 58	μA	$V_{EB} = 4V$
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	—	—	0.3	V	$I_C = 10mA$, $I_B = 0.5mA$
DC Current Transfer Ratio	DDTC114GKA DDTC124GKA DDTC144GKA DDTC115GKA	h_{FE}	30 56 68 82	—	—	—	$I_C = 5mA$, $V_{CE} = 5V$
Bleeder Resistor (R_2) Tolerance		DR_2	-30	—	+30	%	—
Gain-Bandwidth Product*		f_T	—	250	—	MHz	$V_{CE} = 10V$, $I_E = -5mA$, $f = 100MHz$

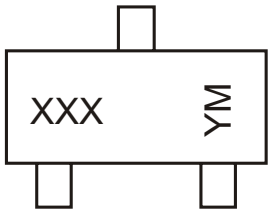
* Transistor - For Reference Only

Ordering Information (Note 2)

Device	Packaging	Shipping
DDTC114GKA-7	SC-59	3000/Tape & Reel
DDTC124GKA-7	SC-59	3000/Tape & Reel
DDTC144GKA-7	SC-59	3000/Tape & Reel
DDTC115GKA-7	SC-59	3000/Tape & Reel

Notes: 2. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.
3. For Lead Free version (with Lead Free terminal finish) part number, please add "-F" suffix to part number above. Example: DDTC115GKA-7-F.

Marking Information



XXX = Product Type Marking Code
See Sheet 1 Diagrams
YM = Date Code Marking
Y = Year ex: N = 2002
M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009
Code	N	P	R	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

TYPICAL CURVES - DDTC114GKA

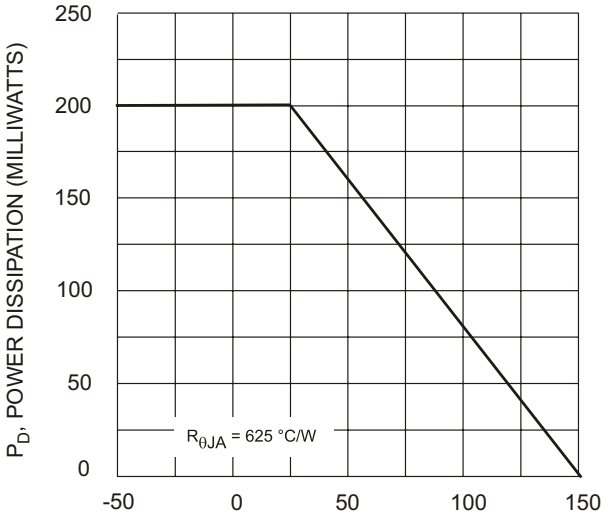


Fig. 1 Derating Curve

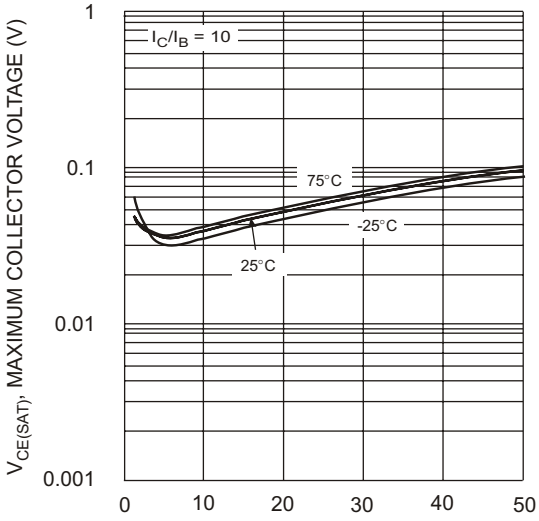


Fig. 2 $V_{CE(SAT)}$ vs. I_C

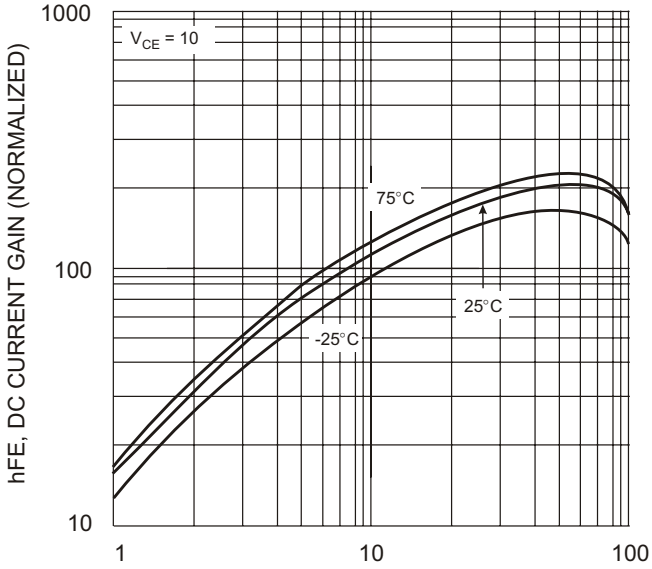


Fig. 3 DC CURRENT GAIN

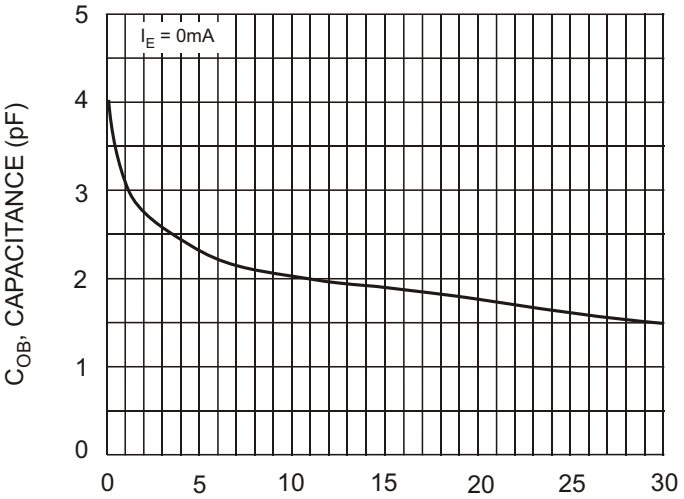


Fig. 4 Output Capacitance

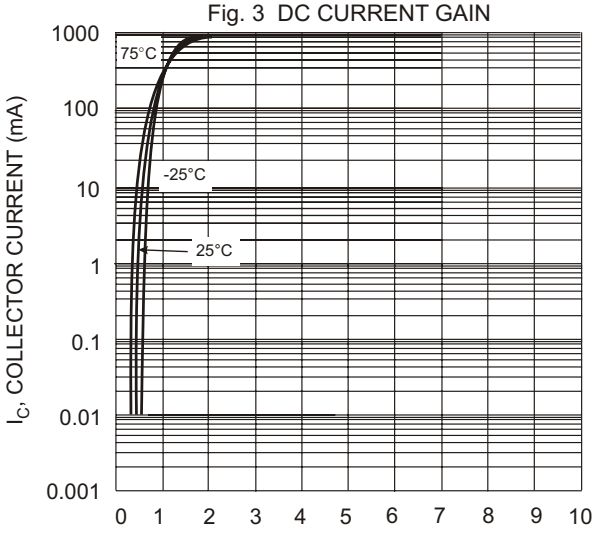


Fig. 5 Collector Current Vs. Input Voltage

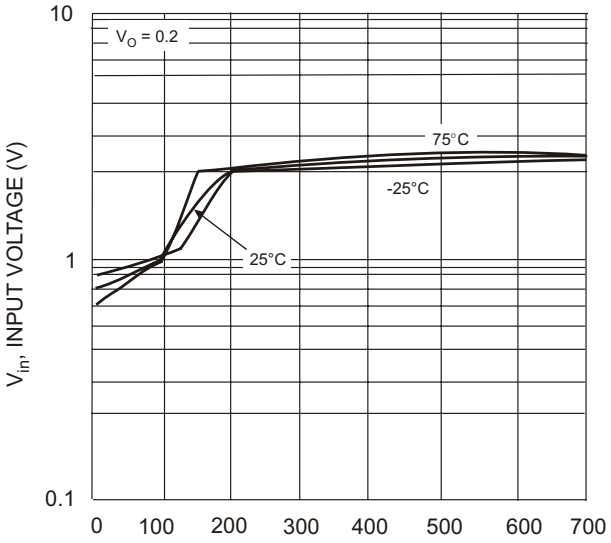


Fig. 6 Input Voltage vs. Collector Current