

**Identification**

DTM60124:32Mx64

DTM60125:32Mx72

Performance range

133MHz (7.5ns @ CL=3)

Features

Burst mode operation

Auto & self refresh capability (4096 Cycles/64ms)

LVTTTL compatible inputs and outputs

Single 3.3V \pm 0.3V power supplyMRS cycle with address key programs Latency
(Access from column address) Burst length (1, 2, 4
& 8 page) Data scramble (Sequential & Interleave)

Serial presence detect with EEPROM

168-pin PC133 DIMM double-sided assembly
5.250" wide by 1.70" high**Description**

The Dataram DTM60124 and DTM60125 are registered 32Mx64 and 32Mx72 Synchronous Dynamic RAM high density memory modules. The DTM60124 consists of sixteen monolithic 32Mx4bit SDRAMs, and the DTM60125 consists of eighteen monolithic 32Mx4bit SDRAMs in TSOP-II 400Mil packages. Three 18-bit Drive ICs for input control, one PLL for clock, and one 2K EEPROM for Serial Presence Detect are also installed on a 168-pin glass-epoxy substrate. Synchronous design allows for precise cycle control with the use of a system clock. The DTM60124 and DTM60125 are Dual in-line Memory Modules intended for mounting into 168-pin connector sockets.