

**Identification**

Part number: DTM60123

Performance range

100MHz (10ns@ CL=2)

Features

Burst mode operation

Auto & self refresh capability (8192 Cycles/64ms)

LVTTTL compatible inputs and outputs

Single 3.3V±0.3V power supply

MRS cycle with address key programs

Latency (Access from column address)

Burst length (1, 2, 4 & 8 page) Data scramble (Sequential & Interleave)

Serial presence detect with EEPROM

168-pin PC100 DIMM double-sided assembly 5.250" wide by 1.70" high

Description

The Dataram DTM60123 Assembly is a 64M bit x 72 Synchronous Dynamic RAM high density memory module. The DTM60123 consists of eighteen CMOS Monolithic 64M x 4 bit Synchronous DRAMs in a TSOP-II 400MII package, three 20-bits Drive ICs for input control signal, one PLL in 24-pin TSSOP package for clock and one 2K EEPROM in 8 pin TSSOP package for Serial Presence Detect on a 168-pin glass-epoxy substrate. The DATARAM DTM60123 is a Dual in-line Memory Module and is intended for mounting into 168-pin edge connector sockets.