



Identification

Part number: DTM60117

Performance range

100MHz (10ns@ CL=3)

Features

Burst mode operation

Auto & self refresh capability (8192 Cycles/64ms)

LVTTL compatible inputs and outputs

Single 3.3V±0.3V power supply

MRS cycle with address key programs Latency (Access from column adress) Burst length (1, 2, 4 & 8 page) Data scramble (Sequential & Interleave)

Serial presence detect with EEPROM

168-pin PC100 DIMM double-sided assembly 5.250" wide by 1.70" high

Description

The Dataram DTM60117 Assembly is a 128M bit x 72 Synchronous Dynamic RAM high density memory module. The DTM60091 consists of eighteen CMOS Stacked 128M x 4 bit Synchronous DRAMs in two TSOP-II 400Mil packages, three 20-bits Drive ICs for input control signal, one PLL in 24-pin TSSOP package for clock and one 2K EEPROM in 8 pin TSSOP package for Serial Presence Detect on a 168pin glass-epoxy substrate. The DTM60117 is a Dual inline Memory Module and is intented for mounting into 168pin edge connector sockets.