

**Identification**

Part number: DTM60094

Performance range

Speed	T _{RAC}	T _{CAC}	T _{RC}	T _{HPC}
-5	50ns	18ns	84ns	20ns

Features

Extended Data-Out mode operation
$\overline{\text{CAS}}$ -before- $\overline{\text{RAS}}$ refresh capability
$\overline{\text{RAS}}$ -only and hidden refresh capability
4K refresh/64mS
LVTTL compatible inputs and outputs
Single 3.3V \pm 0.3V power supply
JEDEC standard pinout
Buffered input except for $\overline{\text{RAS}}$ and DQ
168-pin DIMM double-sided assembly
5.250" wide by 2.00" high
Gold-plated pins

Description

The Dataram DTM60094 is a 32M x 72 bit Dynamic RAM high-density memory module. The DTM60094 consists of thirty-six CMOS 16M x 4 bit DRAMs in TSOP-II 400 mil packages and two 16 bit driver ICs in TSSOP package mounted on a 168-pin glass epoxy substrate. A 0.1µf decoupling capacitor is mounted on the printed circuit board for each DRAM. The DTM60094 is a Dual In-line Memory Module and is intended for mounting into a 168-pin edge connector socket.