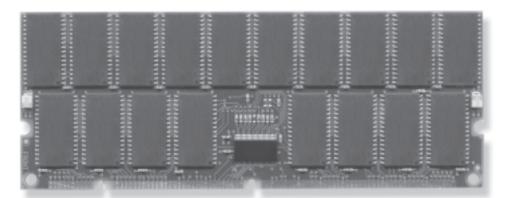


DTM60094



Identification

Part number: DTM60094

Performance range Speed | T_{RAC} | T_{CAC} | T_{RC} | T_{HPC}

50ns 18ns 84ns 20ns

Features

Gold-plated pins

Extended Data-Out mode operation CAS-before-RAS refresh capability RAS-only and hidden refresh capability 4K refresh/64mS LVTTL compatible inputs and outputs Single 3.3V±0.3V power supply JEDEC standard pinout Buffered input except for RAS and DQ 168-pin DIMM double-sided assembly 5.250" wide by 2.00" high

Description

The Dataram DTM60094 is a 32M x 72 bit Dynamic RAM high-density memory module. The DTM60094 consists of thirty-six CMOS 16M x 4 bit DRAMs in TSOP-II 400 mil packages and two 16 bit driver ICs in TSSOP package mounted on a 168-pin glass epoxy substrate. A 0.1µf decoupling capacitor is mounted on the printed circuit board for each DRAM. The DTM60094 is a Dual In-line Memory Module and is intended for mounting into a168-pin edge connector socket.