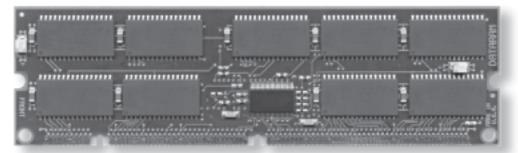


DTM60093

128MB-16M x 72, 168-Pin Buffered EDO DIMM



Identification

Part number: DTM60093

$\begin{array}{c|cccc} \textbf{Performance range} \\ \hline \textbf{Speed} & \textbf{T}_{\textbf{RAC}} & \textbf{T}_{\textbf{CAC}} & \textbf{T}_{\textbf{RC}} & \textbf{T}_{\textbf{HPC}} \\ \hline & -5 & 50 \text{ns} & 18 \text{ns} & 84 \text{ns} & 20 \text{ns} \\ \end{array}$

Features

Extended Data-Out mode operation

CAS-before-RAS refresh capability

RAS-only and Hidden refresh capability

4K refresh/64mS

LVTTL compatible inputs and outputs

Single 3.3V±0.3V power supply

JEDEC standard pinout

Buffered input except for RAS and DQ

168-pin DIMM double-sided assembly

5.250" wide by 1.250" high

Description

The Dataram DTM60093 is a 16M x 72 bit Dynamic RAM high-density memory module. The DTM60093 consists of eighteen CMOS 16M x 4 bit DRAMs in TSOP-II 400 mil packages and two 16 bit driver ICs in TSSOP package mounted on a 168-pin glass epoxy substrate. A 0.22µf decoupling capacitor is mounted on the printed circuit board for each DRAM. The DTM60093 is a Dual In-line Memory Module and is intended for mounting into a168-pin edge connector socket.