

# DIM25HHT12-B000

# **Trench Gate IGBT Half Bridge Module**

**Preliminary Information** 

DS5352-2.1 September 2001

#### **FEATURES**

- Trench Gate
- Enhancement Mode n-Channel Device
- Non Punch Through Structure
- High Switching Speed
- Low On-state Saturation Voltage
- High Input Impedance Simplifies Gate Drive
- Latch-Free Operation
- Fully Short Circuit Rated To 10µs
- Wide RBSOA

#### **APPLICATIONS**

- High Frequency Inverters
- Motor Control
- Switched Mode Power Supplies
- High Frequency Welding
- UPS Systems
- PWM Drives

The DIM25HHT12-B000 is a robust non punch through trench gate n-channel, enhancement mode insulated gate bipolar transistor (IGBT) module designed for low power dissipation in a wide range of low to medium voltage applications such as power supplies and motor drives. Trench Gate technology offers significant improvements when compared with conventional planar IGBTs. The high impedance gate simplifies gate drive considerations, allowing operation directly from low power control circuitry.

Low saturation voltages minimise power dissipation, thereby reducing the running cost of the overall system in which they are used.

The DIM25HHT12-B000 is fully short circuit rated making it especially suited for motor control and other arduous applications.

Typical applications include high frequency inverters for motor control, PWM, welding and heating apparatus. The Powerline range of IGBTs is also applicable to switched mode and uninterruptible power supplies.

#### **KEY PARAMETERS**

V <sub>CES</sub>	(max)	1200V
V <sub>CE(sat)</sub>	(typ)	1.9V
I <sub>C25</sub>	(max)	35A
I <sub>C75</sub>	(max)	25A
I <sub>CM</sub>	(max)	75A
t	(max)	<b>10</b> μ <b>s</b>

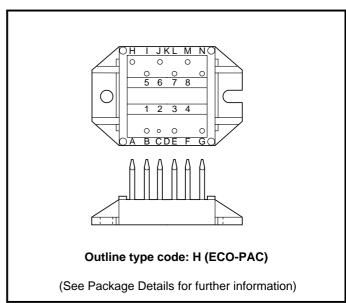


Fig.1 Pin connections - top view (not to scale)

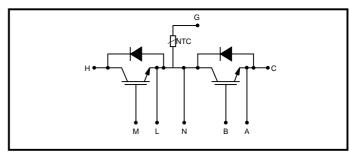


Fig.2 DIM25HHT12-B000 circuit

#### ORDERING INFORMATION

Order as:

DIM25HHT12-B000

Note: When ordering use complete part number.



## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V$	1200	V
$V_{GES}$	Gate-emitter voltage	-	±20	V
I <sub>C25</sub>	Continuous collector current	T <sub>case</sub> = 25°C	35	А
I <sub>C75</sub>	Continuous collector current	T <sub>case</sub> = 75°C	25	А
I <sub>CM</sub>	Pulsed collector current	1ms, T <sub>case</sub> = 75°C	75	А
$P_{tot}$	Power dissipation	T <sub>case</sub> = 75°C	45	W
V <sub>isol</sub>	Isolating voltage	I <sub>isol</sub> ≤ 1mA, 50/60Hz, t = 1 min	3000	V <sub>ac</sub>

#### THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Conditions	Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - IGBT - per device	DC junction to case	-	1.65	°C/W
R <sub>th(j-c)</sub>	Thermal resistance - Diode - per device	DC junction to case	-	4.0	°C/W
T <sub>j</sub>	Operating junction temperature range	-	-40	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	150	°C
-	Mounting torque	M4 Screw	1.5	2.0	Nm



## DC ELECTRICAL CHARACTERISTICS

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I <sub>CES</sub>	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V	-	-	0.2	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V, T <sub>c</sub> = 125°C	-	-	1	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = 20V, V_{CE} = 0V$	-	-	±500	nA
$V_{GE(TH)}$	Gate threshold voltage	$I_{\rm C} = 1$ mA, $V_{\rm CE} = V_{\rm GE}$	-	7	-	V
V <sub>CE(SAT)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 25A	-	1.9	2.3	V
		$V_{GE} = 15V, I_{C} = 25A, T_{c} = 125^{\circ}C$	-	2.1	-	V

## **AC ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
$C_{ies}$	Input capacitance	$V_{CE} = 75V, V_{GE} = 15V, f = 1MHz$	-	-	-	pF
C <sub>oes</sub>	Output capacitance	$V_{CE} = 75V, V_{GE} = 15V, f = 1MHz$	-	-	-	pF
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 75V, V <sub>GE</sub> = 15V, f = 1MHz	-	-	-	pF

## **SHORT CIRCUIT RATING**

T<sub>case</sub> = 125°C unless stated otherwise.

I	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I	t <sub>sc</sub>	Short circuit withstand time	$V_{GE} = 15V, V_{CE} = 80\% V_{CES}$	-	-	10	μs



## INDUCTIVE SWITCHING CHARACTERISTICS

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t <sub>d(ON)</sub>	Turn-on delay time		-	160	-	ns
t,	Rise time	I <sub>c</sub> = 25A	-	15	-	ns
E <sub>on</sub>	Turn-on energy loss - per cycle	$V_{GF} = \pm 15V$ ,	-	1.5	-	mJ
t <sub>d(OFF)</sub>	Turn-off delay time	92	-	270	-	ns
t <sub>f</sub>	Fall time	$V_{CE} = 50\%V_{ces}$	-	40	-	ns
E <sub>OFF</sub>	Turn-off energy loss - per cycle	$R_{_{\rm G}} = 5\Omega$	-	2.5	-	mJ

# T<sub>case</sub> = 125°C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t <sub>d(ON)</sub>	Turn-on delay time		-	170	-	ns
t <sub>r</sub>	Rise time	I <sub>c</sub> = 25A	-	17	-	ns
E <sub>on</sub>	Turn-on energy loss - per cycle	$V_{GF} = \pm 15V$ ,	-	2	-	mJ
t <sub>d(OFF)</sub>	Turn-off delay time	$V_{CE} = 50\%V_{ces}$	-	340	-	ns
t <sub>f</sub>	Fall time		-	60	-	ns
E <sub>OFF</sub>	Turn-off energy loss - per cycle	$R_{_{\rm G}} = 5\Omega$	-	3.5	-	mJ

## **DIODE CHARACTERISTICS**

# $T_c = 25^{\circ}C$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V <sub>FM</sub>	Forward voltage	At I <sub>F</sub> = 25A peak	-	1.9	-	V
		At I <sub>F</sub> = 25A peak, T <sub>case</sub> = 125°C	-	1.92	-	V
t <sub>rr</sub>	Reverse recovery time	$I_F = 25A$ , $di_{RR} / dt = 200A / \mu s$	-	90	-	ns
I <sub>RRM</sub>	Reverse recovery current	$V_R = 50\%V_{RRM}$	-	12	-	Α



## **BASIC TEST CIRCUIT**

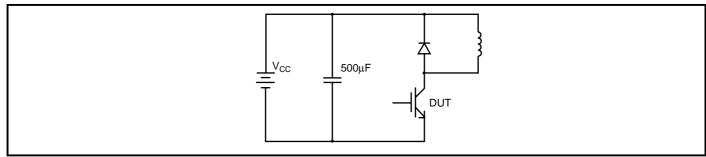


Fig.3 Basic d.c. chopper circuit

## **SWITCHING DEFINITIONS**

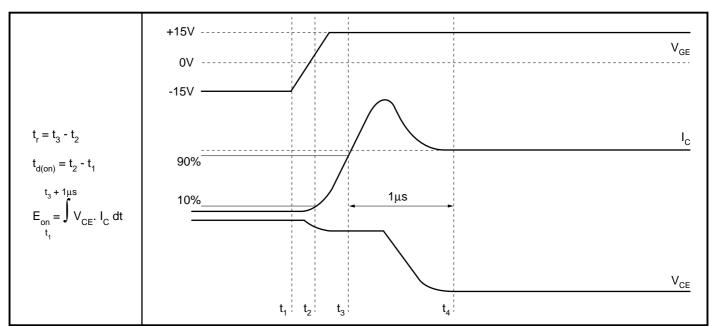


Fig.4 Turn-on characteristics



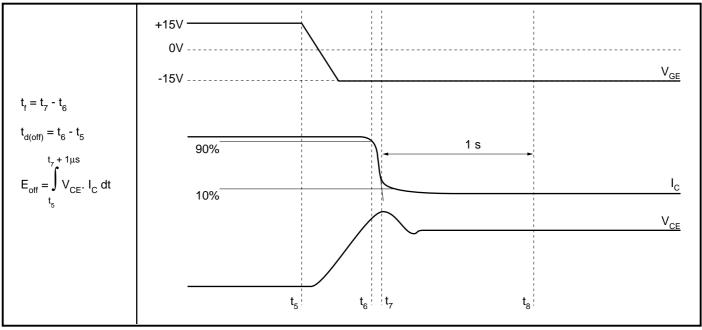
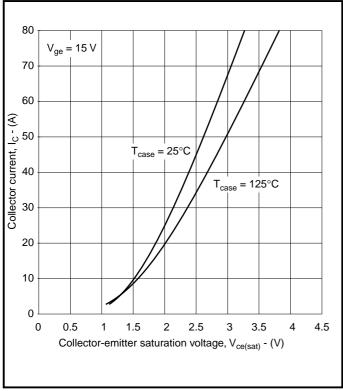
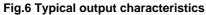


Fig.5 Turn-off characteristics

## **CURVES**





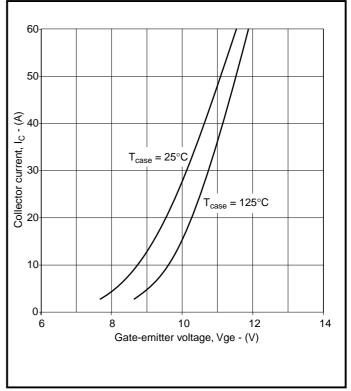


Fig.7 Typical transfer characteristics



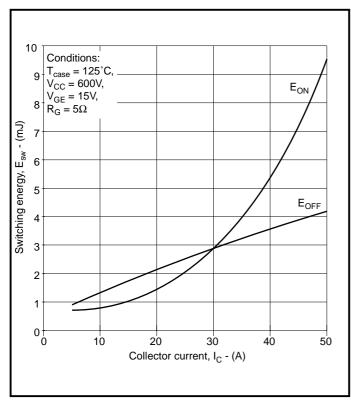


Fig.8 Typical switching losses vs collector current

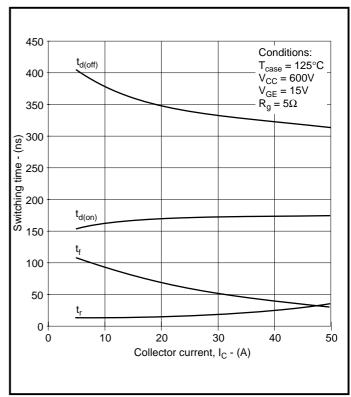


Fig.9 Typical switching times vs collector current

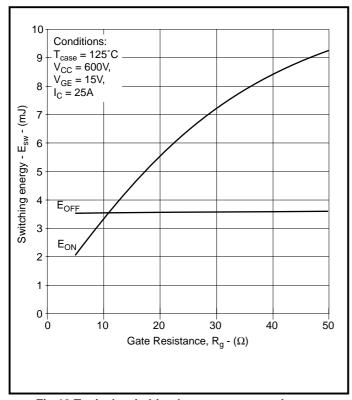


Fig.10 Typical switching losses vs gate resistance

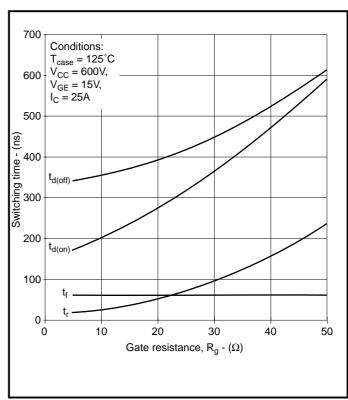
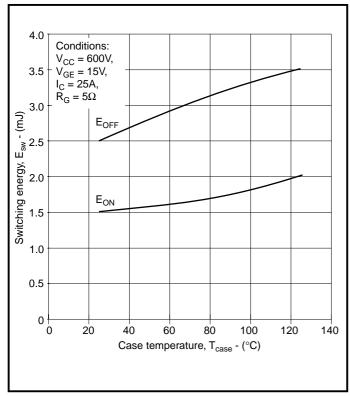


Fig.11 Typical switching times vs gate resistance





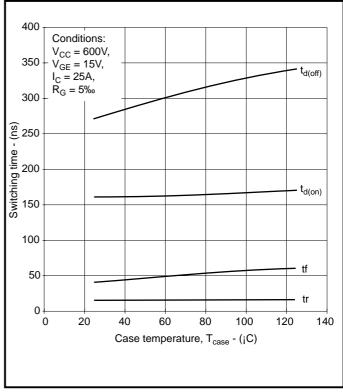
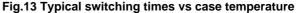
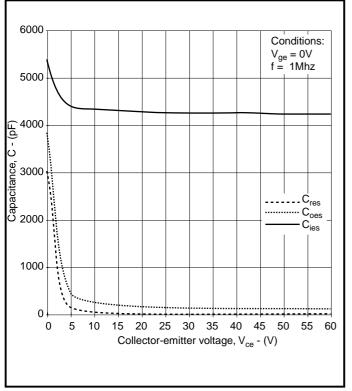


Fig.12 Typical switching losses vs case temperature





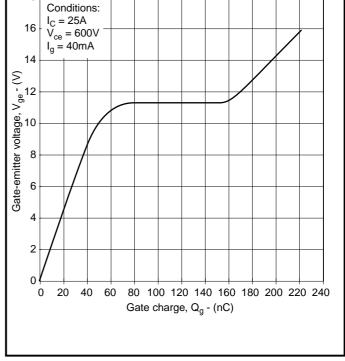


Fig.14 Typical capacitance

Fig.15 Typical gate charge



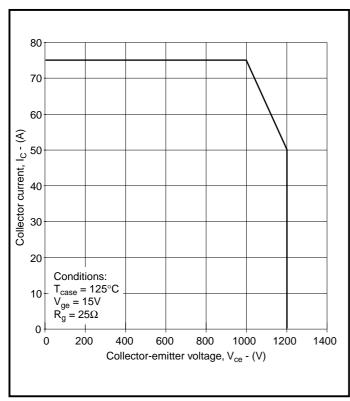


Fig.16 Reverse bias safe operating area

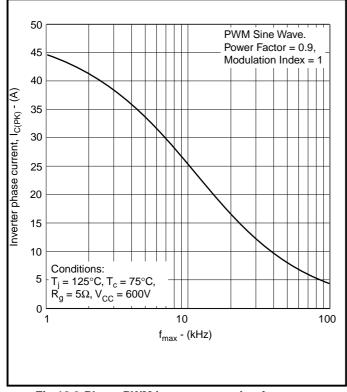


Fig.18 3-Phase PWM inverter operating frequency

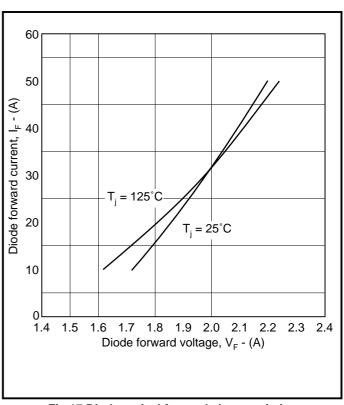


Fig.17 Diode typical forward characteristics

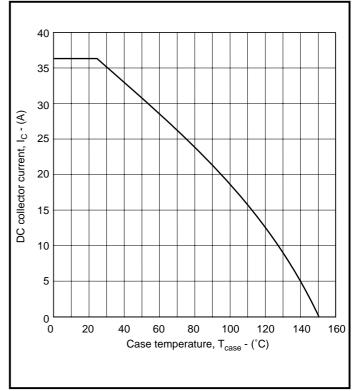
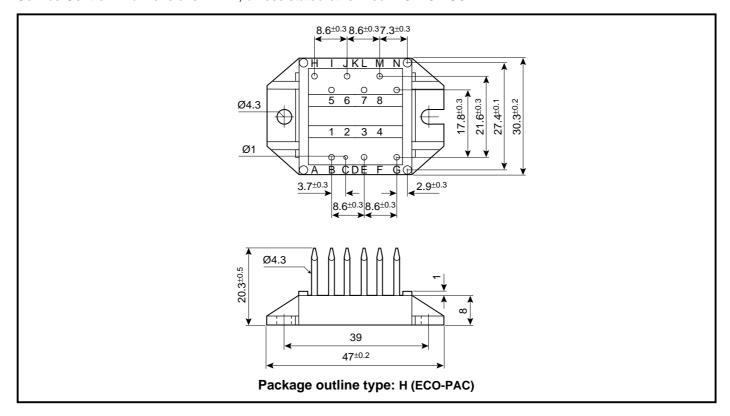


Fig.19 DC current rating vs case temperature



## **PACKAGE DETAILS**

For additional package information, please contact your nearest representative or Dynex Semiconductor Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.







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e-mail: power\_solutions@dynexsemi.com

HEADQUARTERS OPERATIONS
DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln. Lincolnshire. LN6 3LF. United Kingdom. Tel: 00-44-(0)1522-500500

Fax: 00-44-(0)1522-500550

DYNEX POWER INC.

99 Bank Street, Suite 410, Ottawa, Ontario, Canada, K1P 6B9 Tel: 613.723.7035

Fax: 613.723.1518

Toll Free: 1.888.33.DYNEX (39639)

CUSTOMER SERVICE CENTRES

Mainland Europe Tel: +33 (0)1 58 04 91 00. Fax: +33 (0)1 46 38 51 33

North America Tel: (613) 723-7035. Fax: (613) 723-1518.

UK, Scandinavia & Rest Of World Tel: +44 (0)1522 500500. Fax: +44 (0)1522 500020

SALES OFFICES

Mainland Europe Tel: +33 (0)1 58 04 91 00. Fax: +33 (0)1 46 38 51 33

North America Tel: (613) 723-7035. Fax: (613) 723-1518. Toll Free: 1.888.33.DYNEX (39639) /

Tel: (949) 733-3005. Fax: (949) 733-2986.

UK, Scandinavia & Rest Of World Tel: +44 (0)1522 500500. Fax: +44 (0)1522 500020

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