

### FEATURES

- Double Side Cooling
- High Surge Capability

### APPLICATIONS

- Rectification
- Freewheel Diode
- DC Motor Control
- Power Supplies
- Welding
- Battery Chargers

### VOLTAGE RATINGS

Type Number	Repetitive Peak Reverse Voltage $V_{RRM}$ V	Conditions
DRD420D18	1800	$V_{RSM} = V_{RRM} + 100V$
DRD420D16	1600	
DRD420D14	1400	
DRD420D12	1200	

Lower voltage grades available.

### KEY PARAMETERS

$V_{RRM}$	1800V
$I_{F(AV)}$	4200A
$I_{FSM}$	70000A

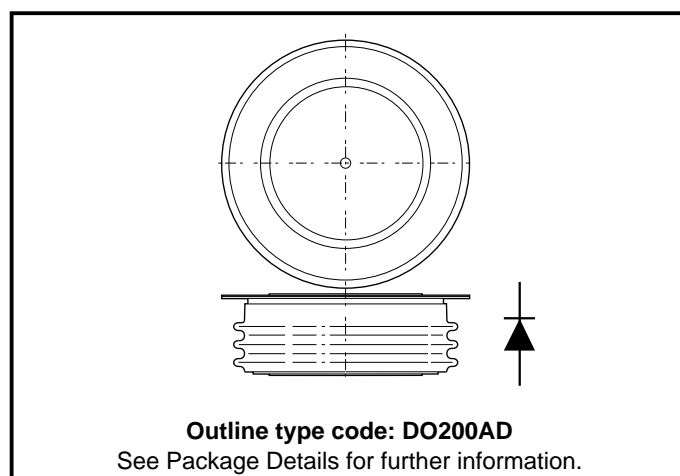


Fig. 1 Package outline

### ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table, e.g.:

#### DRD420D18

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

## CURRENT RATINGS

 $T_{case} = 75^{\circ}\text{C}$  unless otherwise stated

Symbol	Parameter	Conditions	Max.	Units
<b>Double Side Cooled</b>				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	5150	A
$I_{F(RMS)}$	RMS value	-	8090	A
$I_F$	Continuous (direct) forward current	-	7450	A
<b>Single Side Cooled (Anode side)</b>				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	3760	A
$I_{F(RMS)}$	RMS value	-	5910	A
$I_F$	Continuous (direct) forward current	-	4820	A

 $T_{case} = 100^{\circ}\text{C}$  unless otherwise stated

Symbol	Parameter	Conditions	Max.	Units
<b>Double Side Cooled</b>				
$I_{F(AV)}$	Mean forward current	Half wave resistive load, $T_{case} = 100^{\circ}\text{C}$	4200	A
$I_{F(RMS)}$	RMS value	$T_{case} = 100^{\circ}\text{C}$	6600	A
$I_F$	Continuous (direct) forward current	$T_{case} = 100^{\circ}\text{C}$	5980	A
<b>Single Side Cooled (Anode side)</b>				
$I_{F(AV)}$	Mean forward current	Half wave resistive load, $T_{case} = 100^{\circ}\text{C}$	3000	A
$I_{F(RMS)}$	RMS value	$T_{case} = 100^{\circ}\text{C}$	4780	A
$I_F$	Continuous (direct) forward current	$T_{case} = 100^{\circ}\text{C}$	3800	A

**SURGE RATINGS**

Symbol	Parameter	Conditions	Max.	Units
$I_{FSM}$	Surge (non-repetitive) forward current	10ms half sine; $T_{case} = 175^{\circ}C$	56	kA
$I^2t$	$I^2t$ for fusing	$V_R = 50\% V_{RRM}$ - 1/4 sine	$15.68 \times 10^6$	A <sup>2</sup> s
$I_{FSM}$	Surge (non-repetitive) forward current	10ms half sine; $T_{case} = 175^{\circ}C$	70	kA
$I^2t$	$I^2t$ for fusing	$V_R = 0$	$24.5 \times 10^6$	A <sup>2</sup> s

**THERMAL AND MECHANICAL DATA**

Symbol	Parameter	Conditions	Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	dc	-	0.013 °C/W
		Single side cooled	Anode dc	-	0.025 °C/W
			Cathode dc	-	0.027 °C/W
$R_{th(c-h)}$	Thermal resistance - case to heatsink	Clamping force 45.0kN with mounting compound	Double side	-	0.003 °C/W
			Single side	-	0.006 °C/W
$T_{vj}$	Virtual junction temperature	Forward (conducting)	-	185	°C
		Reverse (blocking)	-	175	°C
$T_{stg}$	Storage temperature range		-55	200	°C
-	Clamping force		40.0	48.0	kN

## CHARACTERISTICS

Symbol	Parameter	Conditions	Typ.	Max.	Units
$I_{RRM}$	Peak reverse current	At $V_{RRM}$ , $T_{case} = 175^{\circ}C$	-	100	mA
$Q_S$	Total stored charge	$I_F = 1000A$ , $di_{RR}/dt = 5A/\mu s$ $T_{case} = 175^{\circ}C$ , $V_R = 100V$	2500	-	$\mu C$
$I_{RR}$	Peak recovery current		155	-	A
$t_{rr}$	Reverse recovery time		50	-	$\mu s$
$V_{TO}$	Threshold voltage	At $T_{vj} = 175^{\circ}C$	-	0.75	V
$r_T$	Slope resistance	At $T_{vj} = 175^{\circ}C$	-	0.0494	m $\Omega$

## CURVES

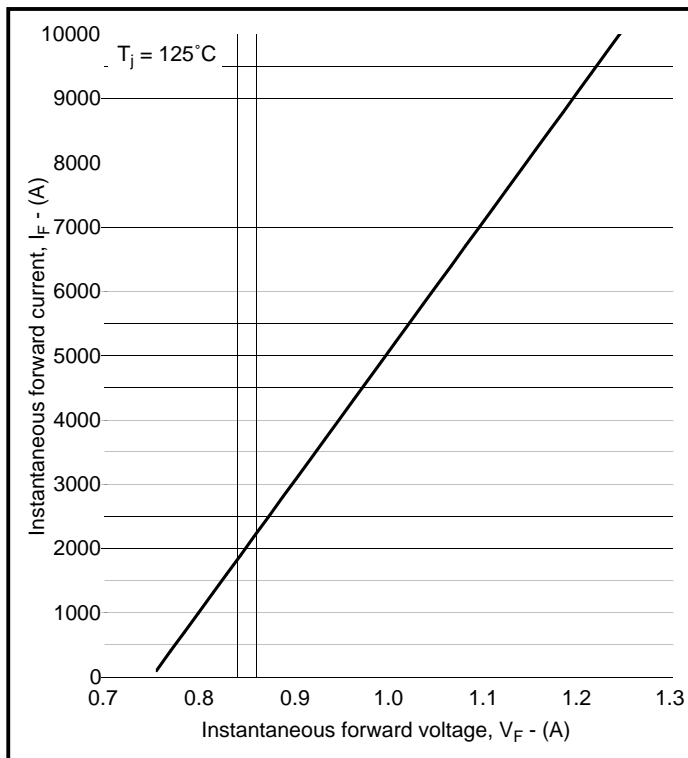


Fig.2 Maximum (limit) forward characteristics

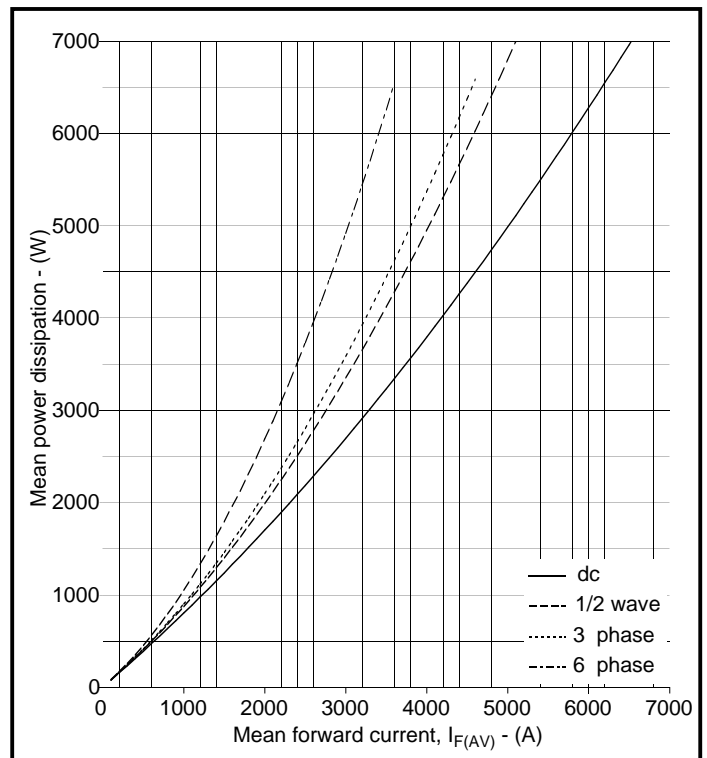
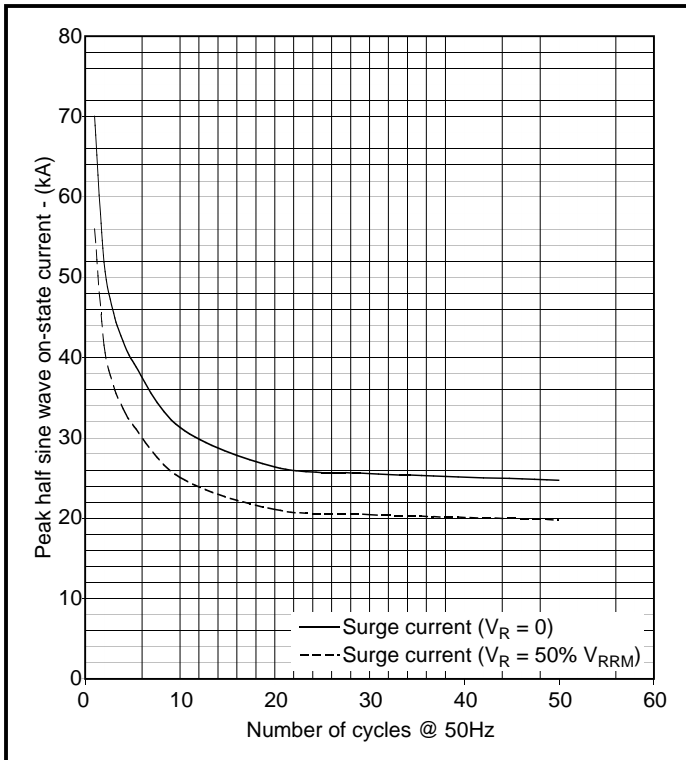
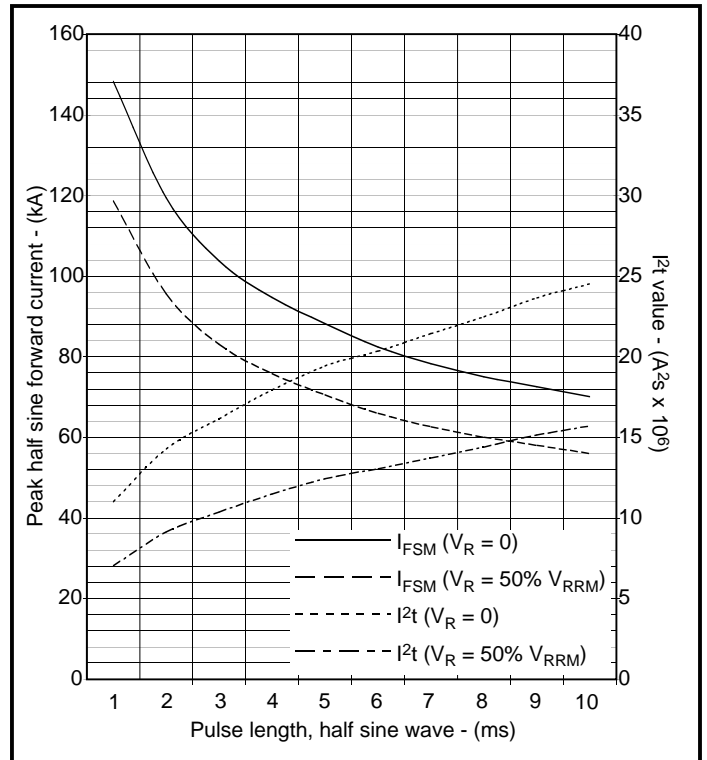


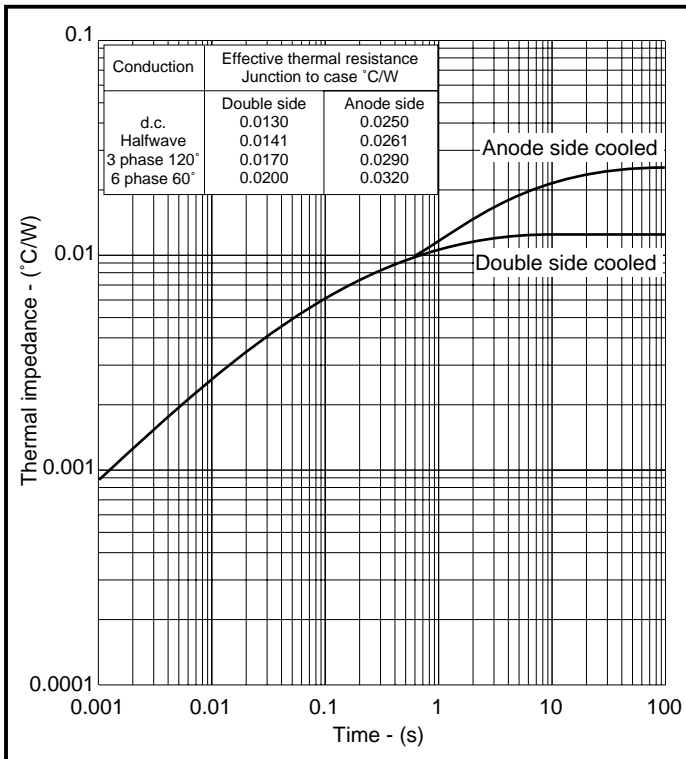
Fig.3 Dissipation curves



**Fig.4 Series multi-cycle surge current**



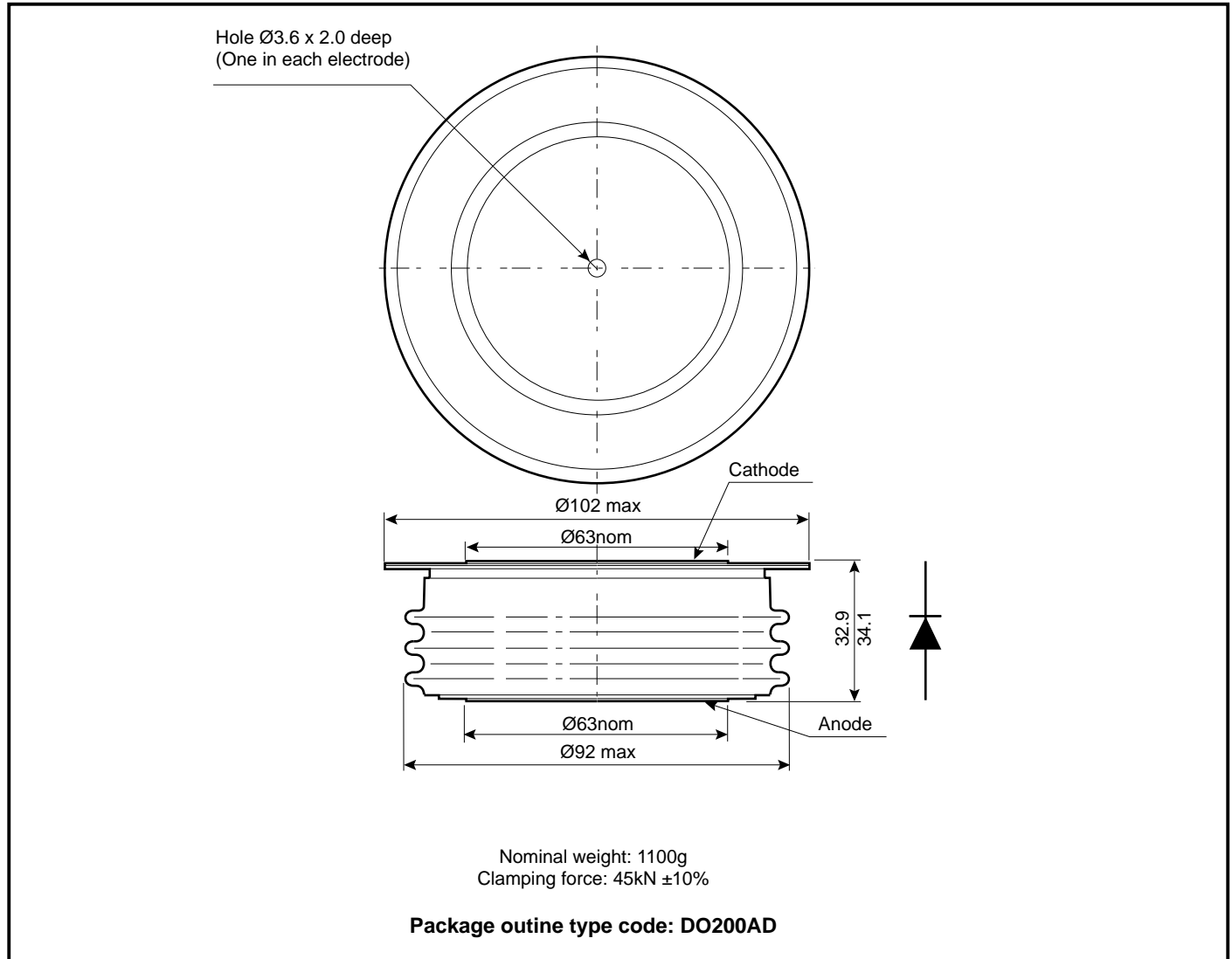
**Fig.5 Sub-cycle surge curves**



**Fig.5 Maximum (limit) transient thermal impedance - junction to case**

## PACKAGE DETAILS

For further package information, please contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



### Note:

1. Package maybe supplied with pins and/or tags.

## POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

## DEVICE CLAMPS

Disc devices require the correct clamping force to ensure their safe operation. The PACs range offers a varied selection of pre-loaded clamps to suit all of our manufactured devices. This include cube clamps for single side cooling of 'T' 22mm

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

## HEATSINKS

Power Assembly has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or Customer Services.



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**Target Information:** This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

**Preliminary Information:** The product is in design and development. The datasheet represents the product as it is understood but details may change.

**Advance Information:** The product design is complete and final characterisation for volume production is well in hand.

**No Annotation:** The product parameters are fixed and the product is available to datasheet specification.

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